

FIG. 1

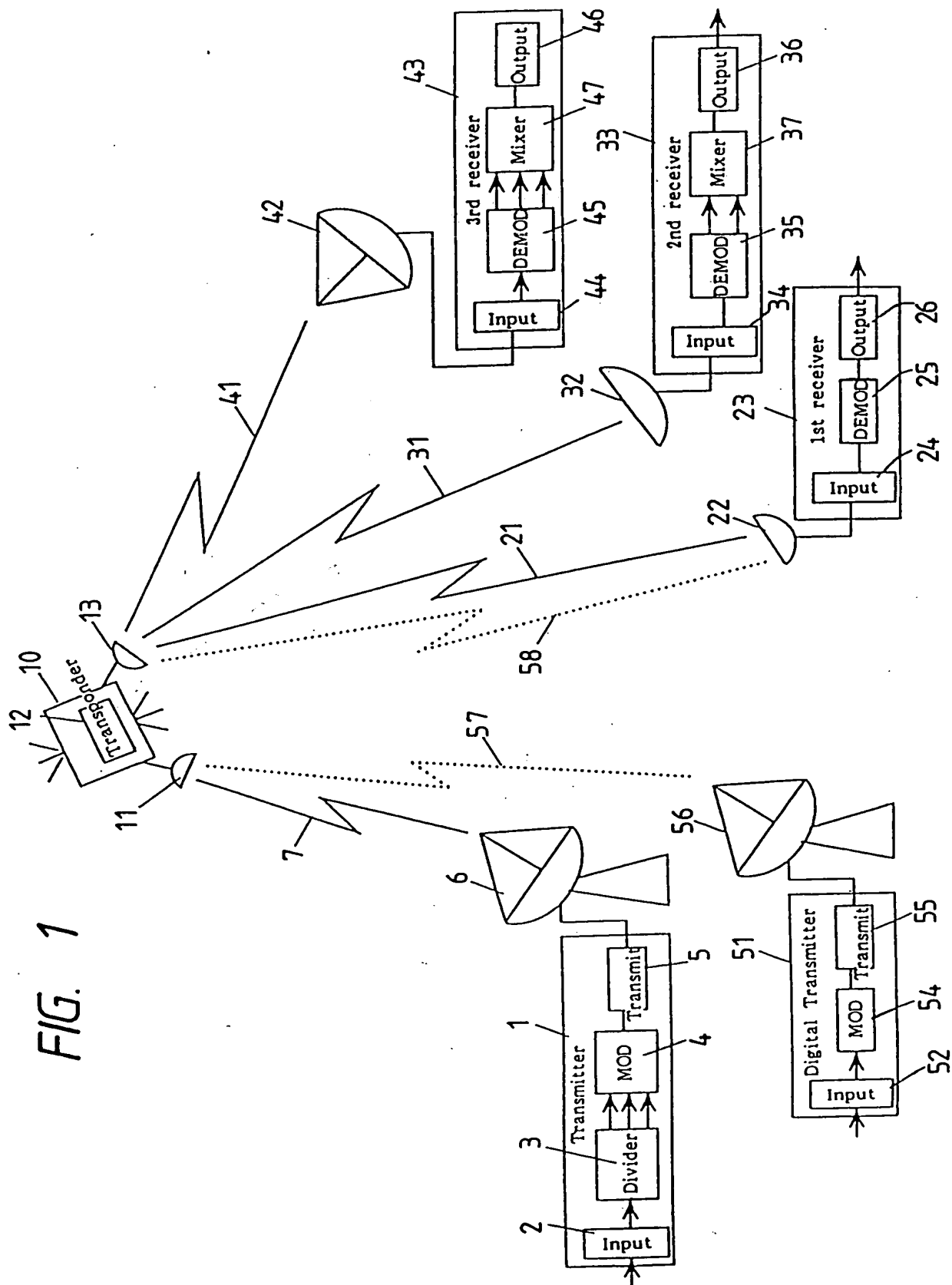
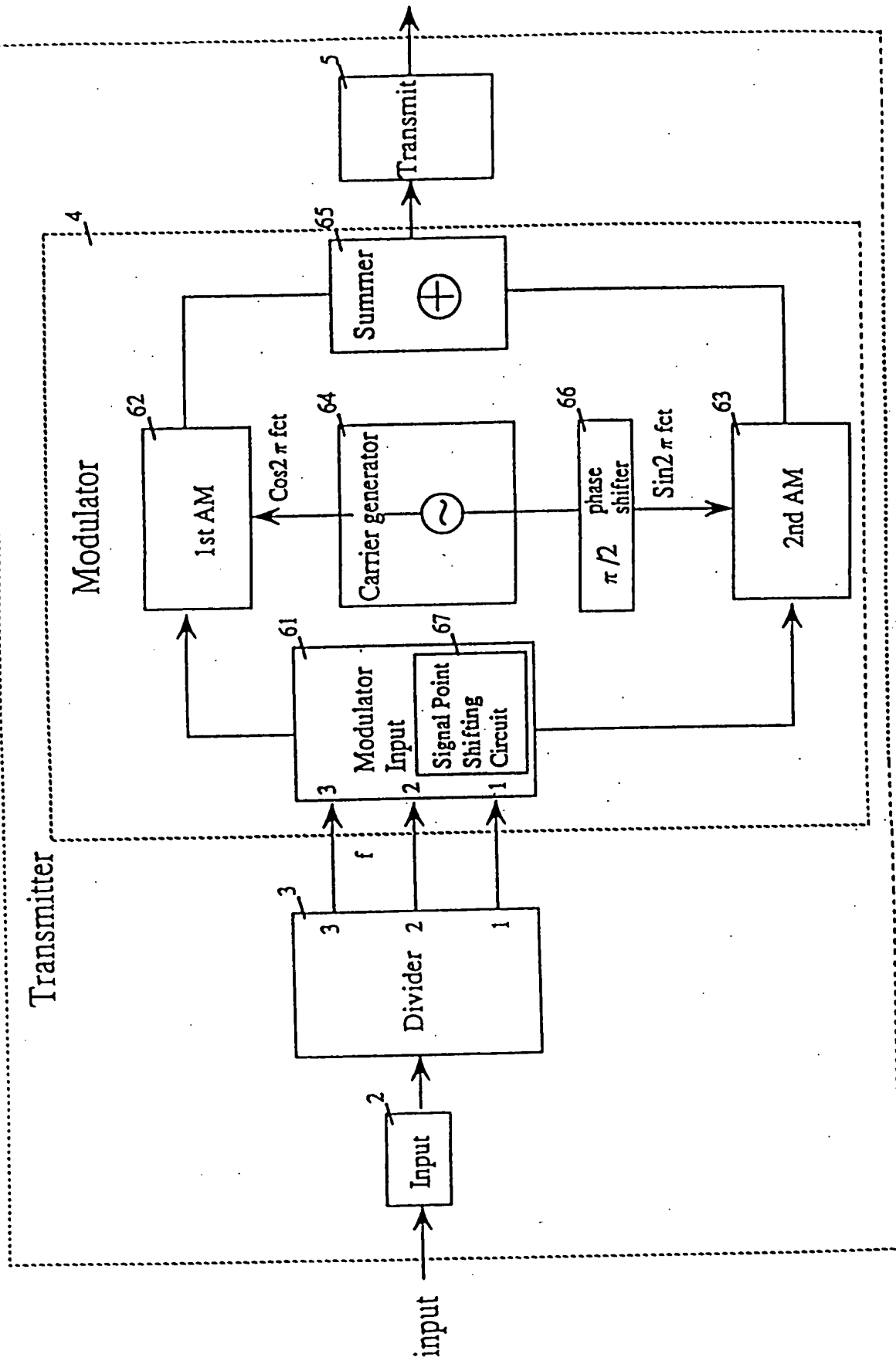


FIG. 2



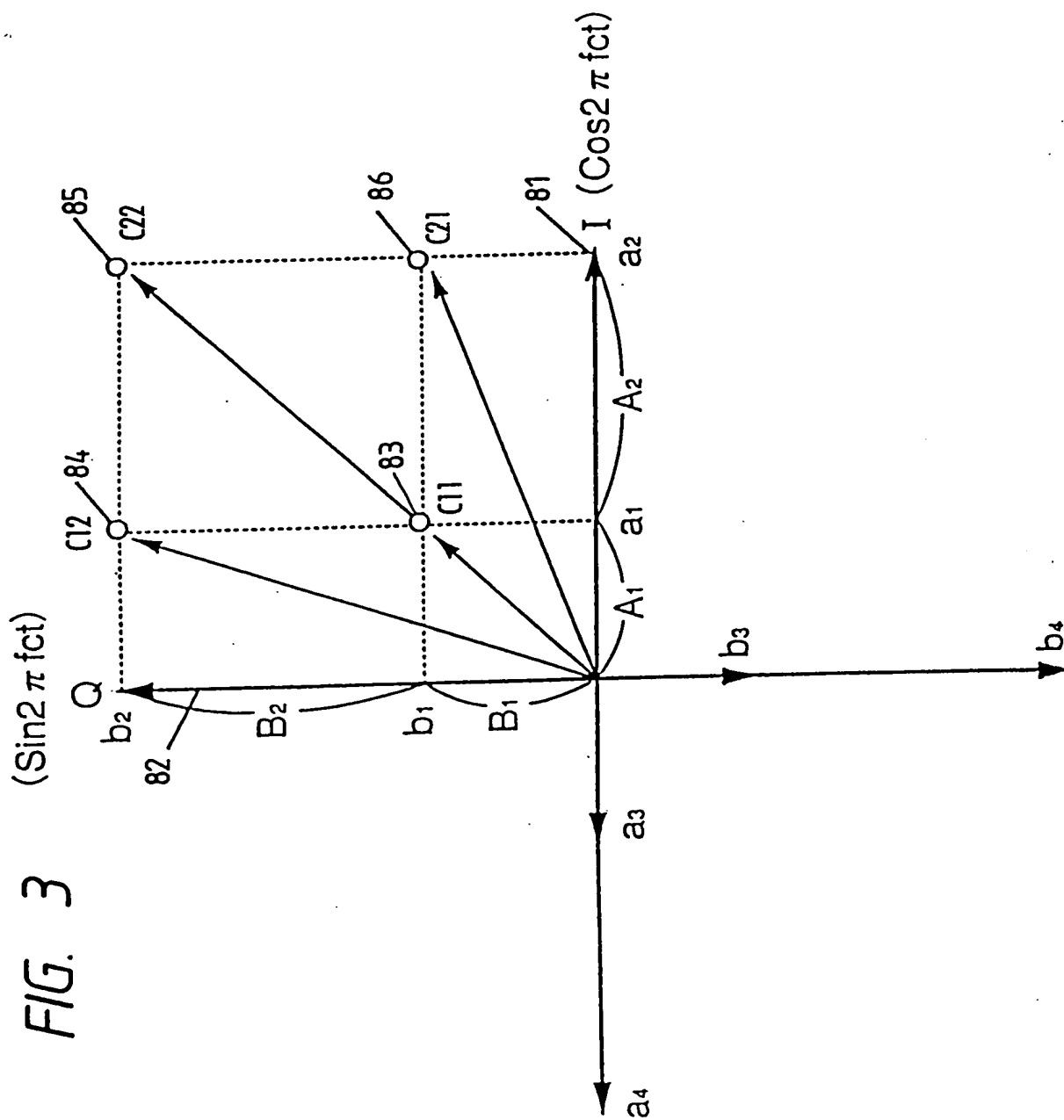


FIG. 4 $(\sin 2\pi \text{ fct})$

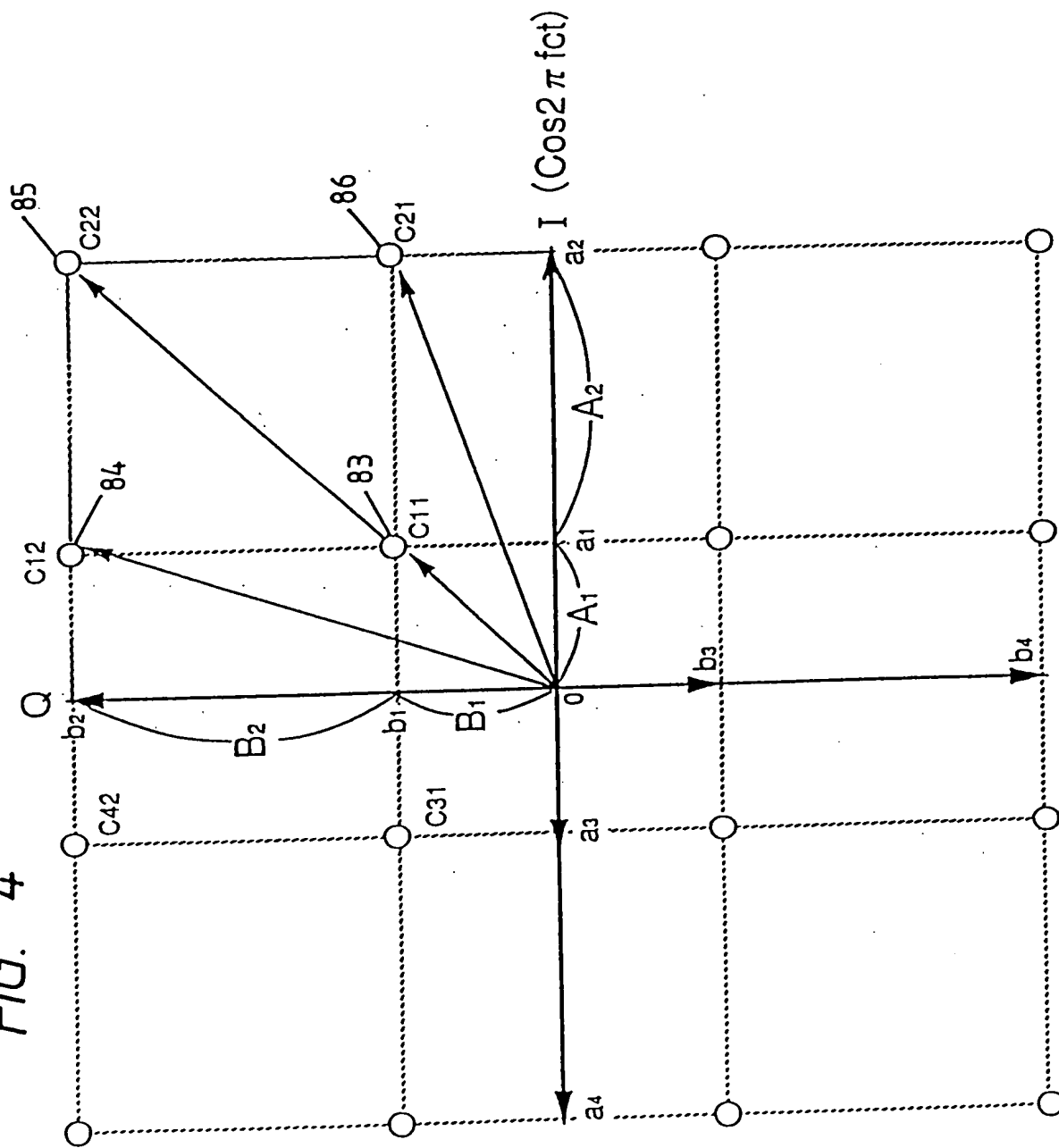


FIG. 5 ($\sin 2\pi \text{ fct}$)

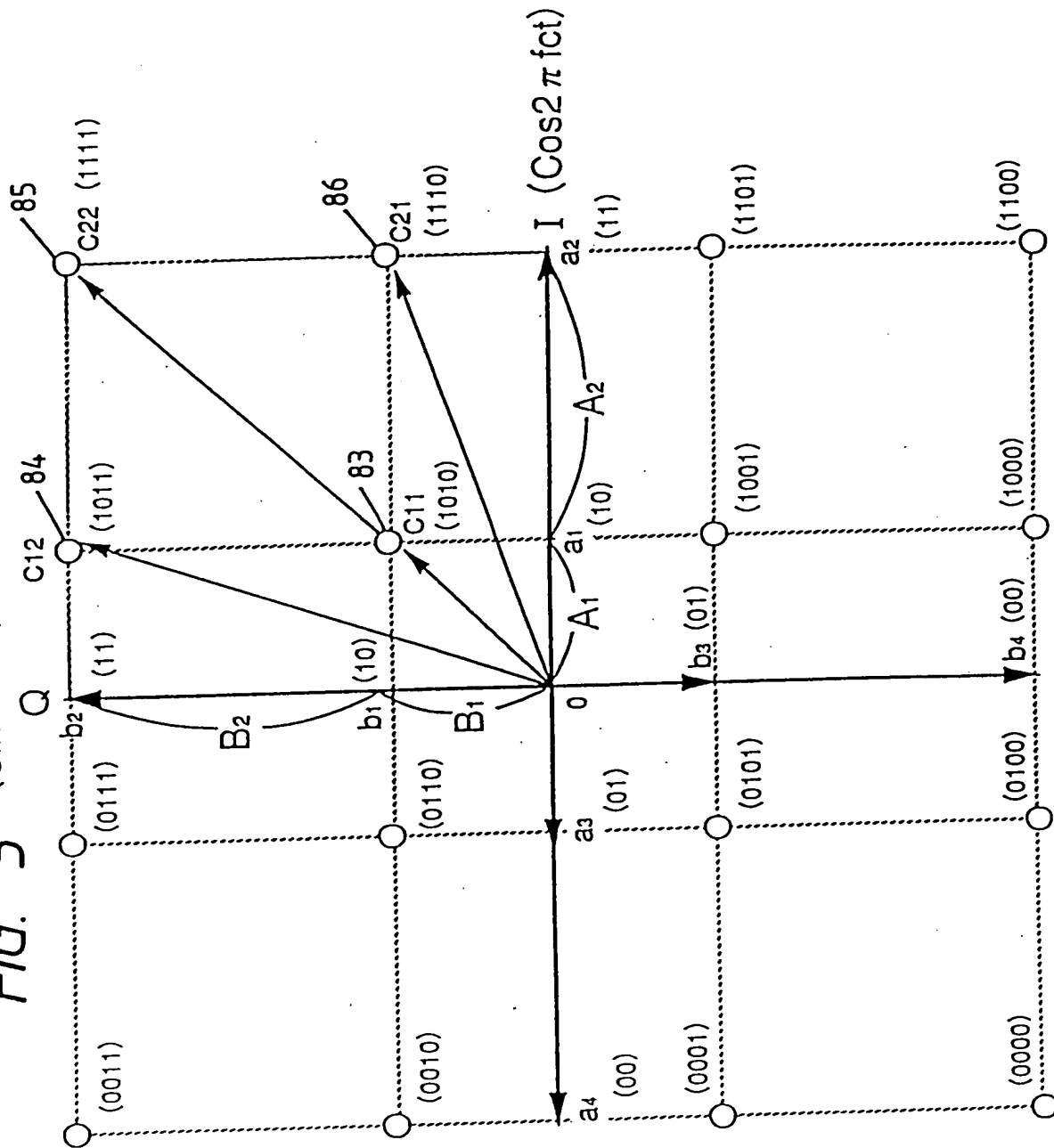


FIG. 6

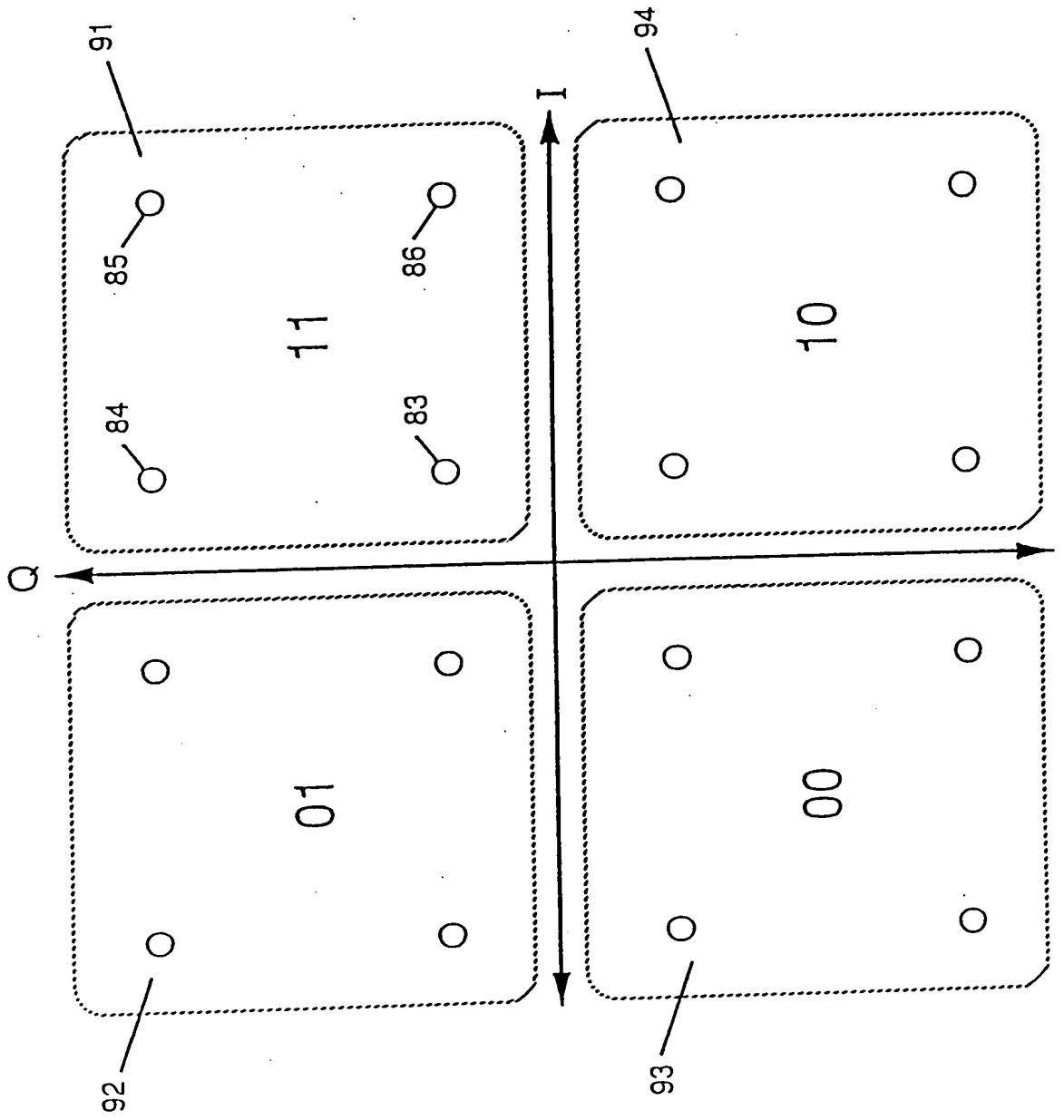


FIG. 7

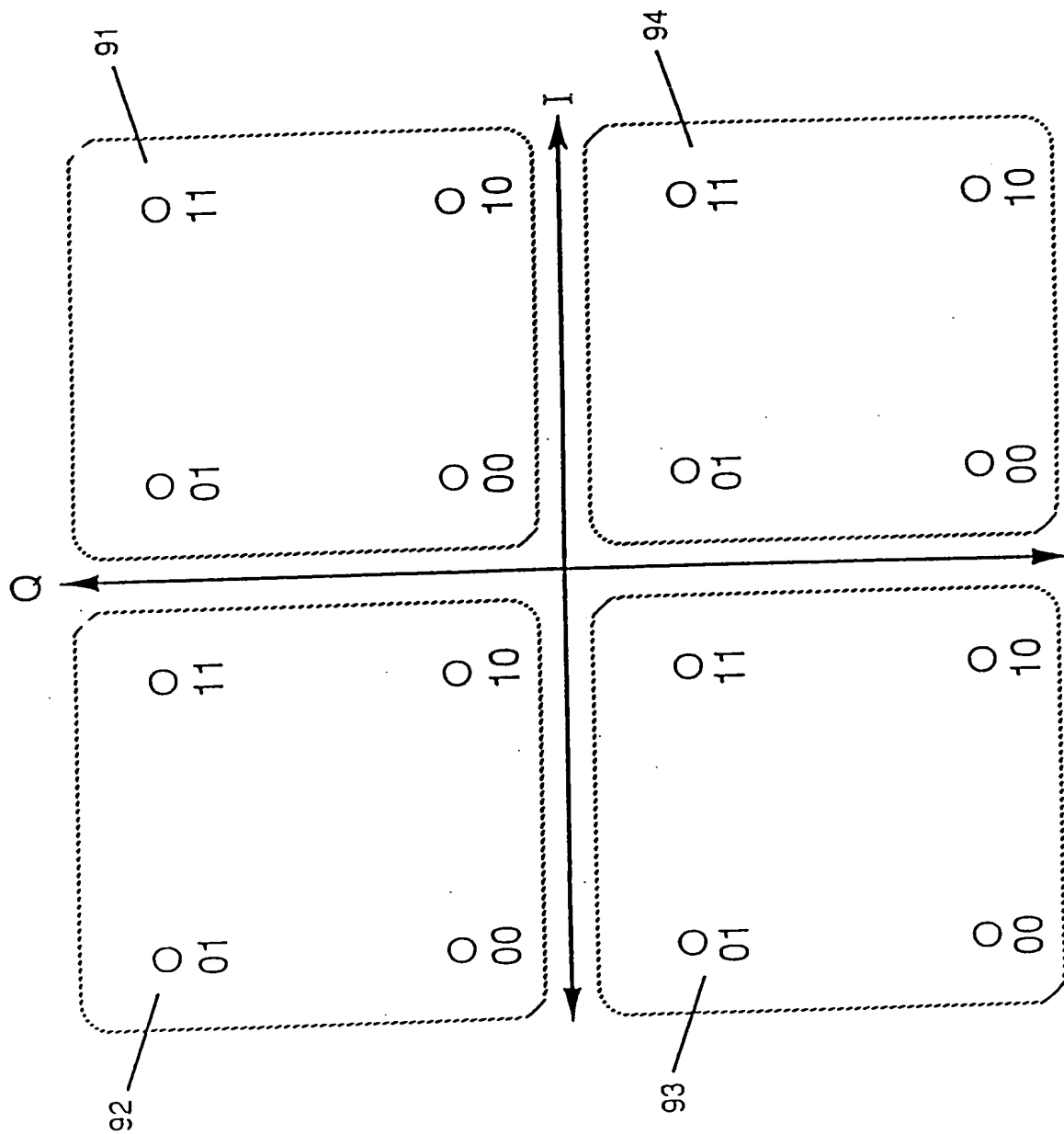


FIG. 9

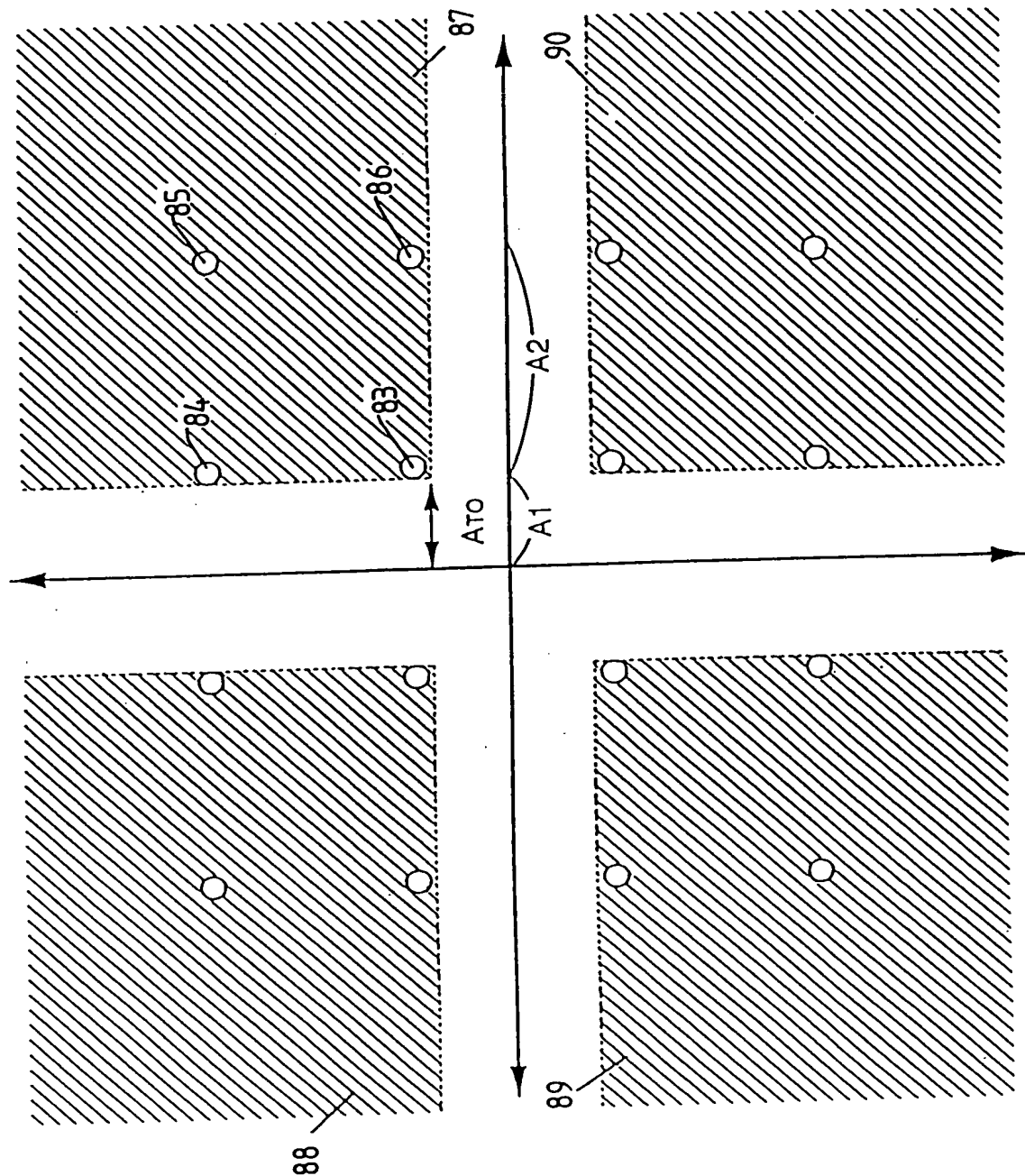


FIG. 10

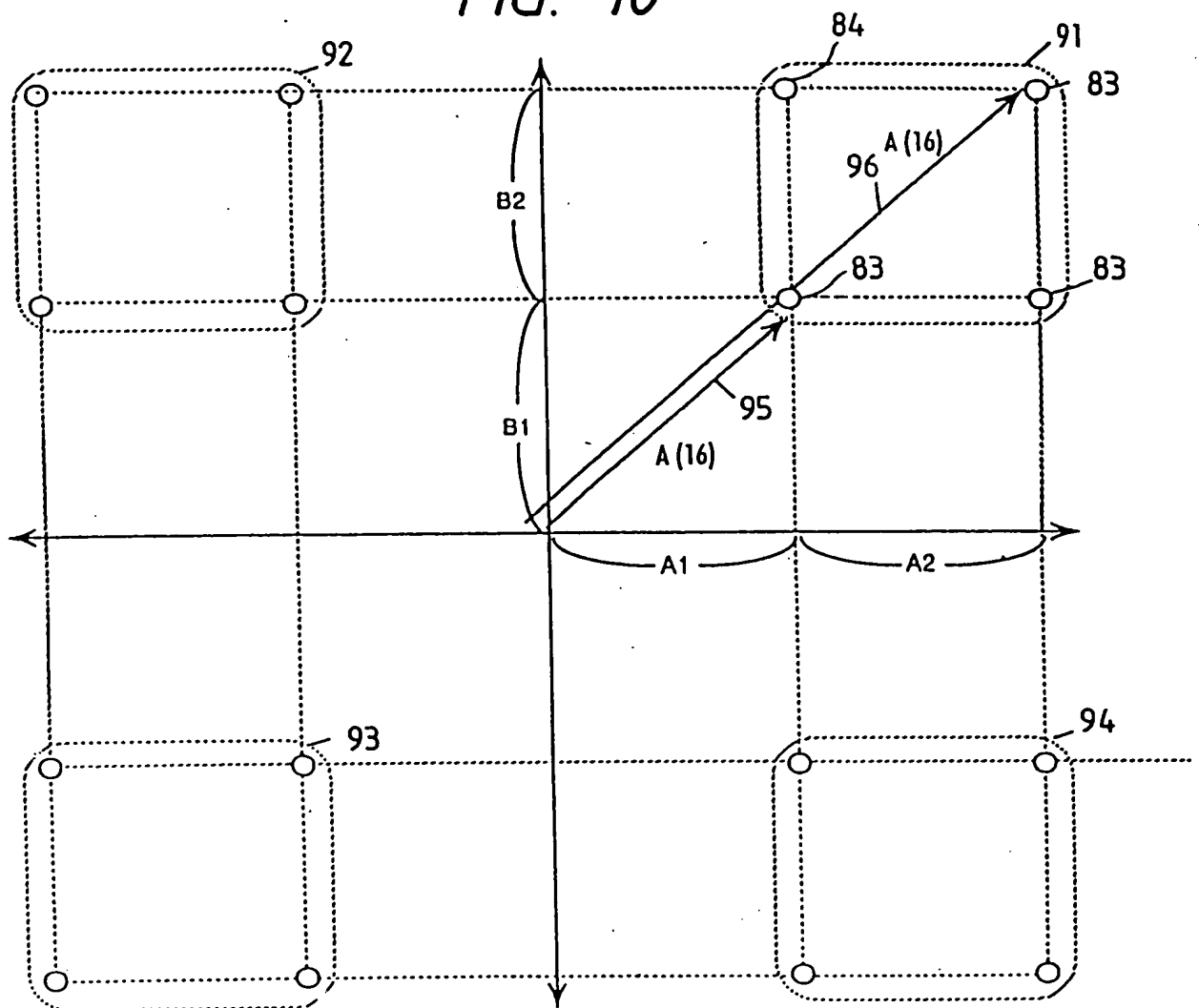


FIG. 12

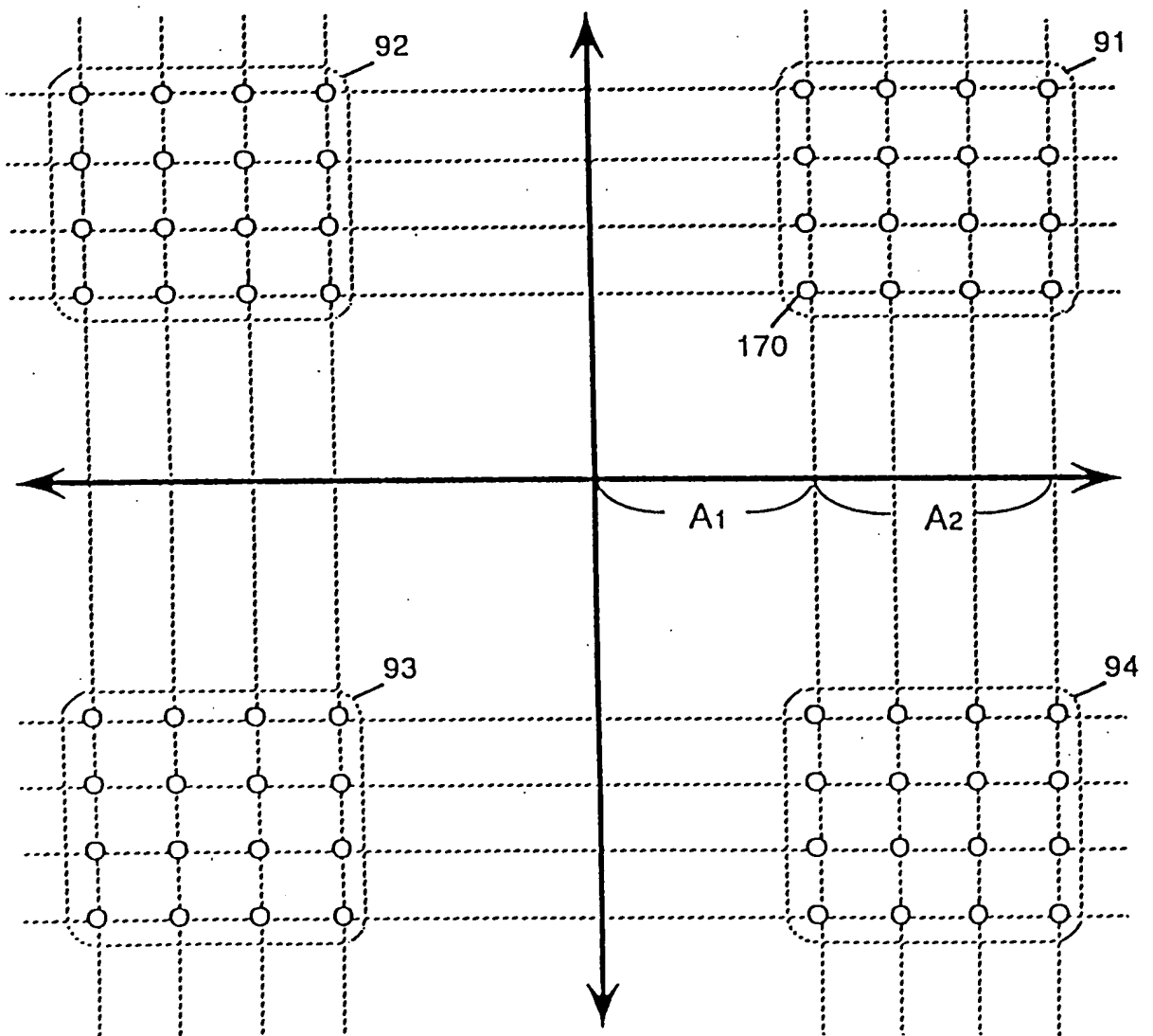


FIG. 13

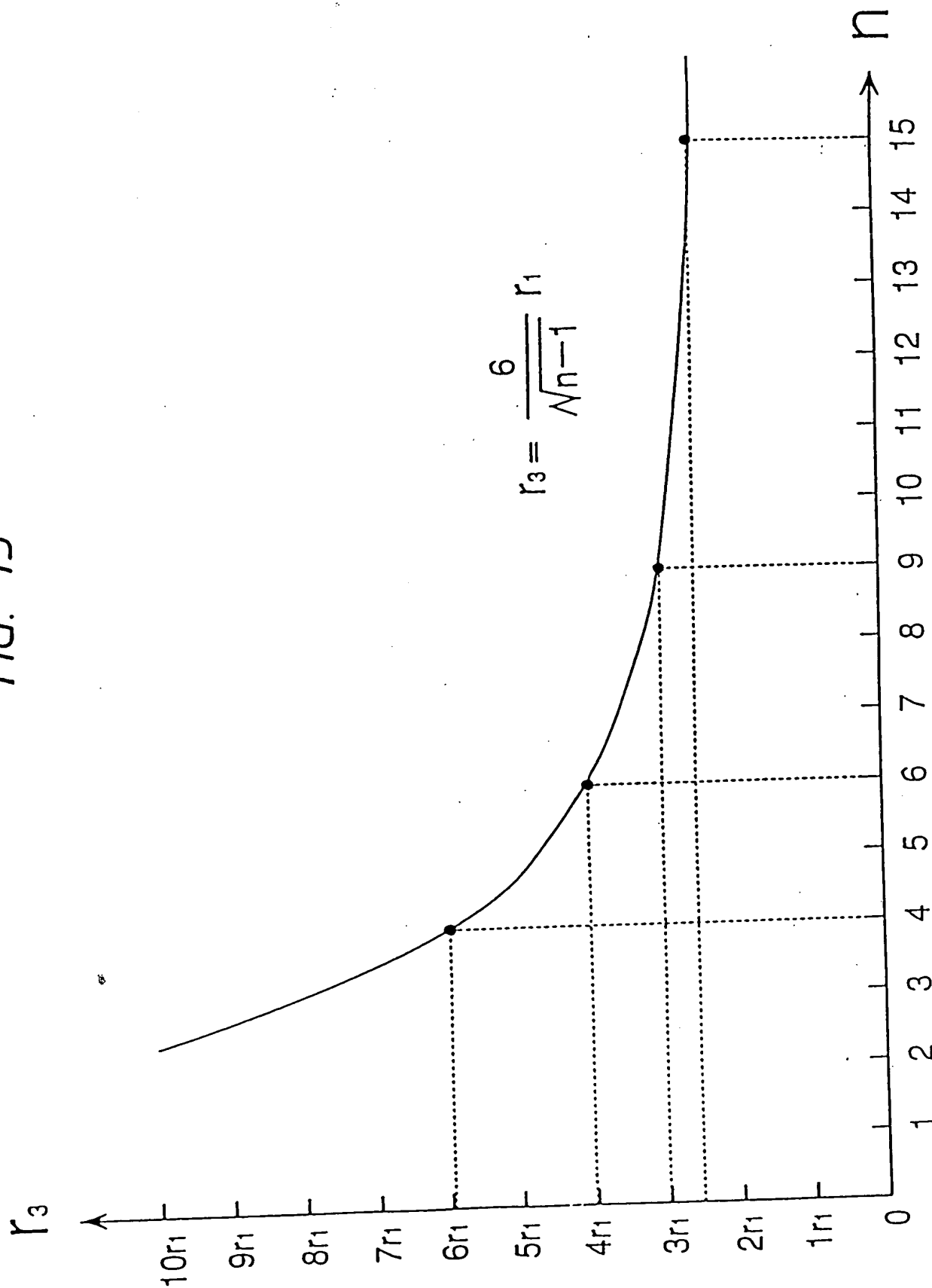


FIG. 14

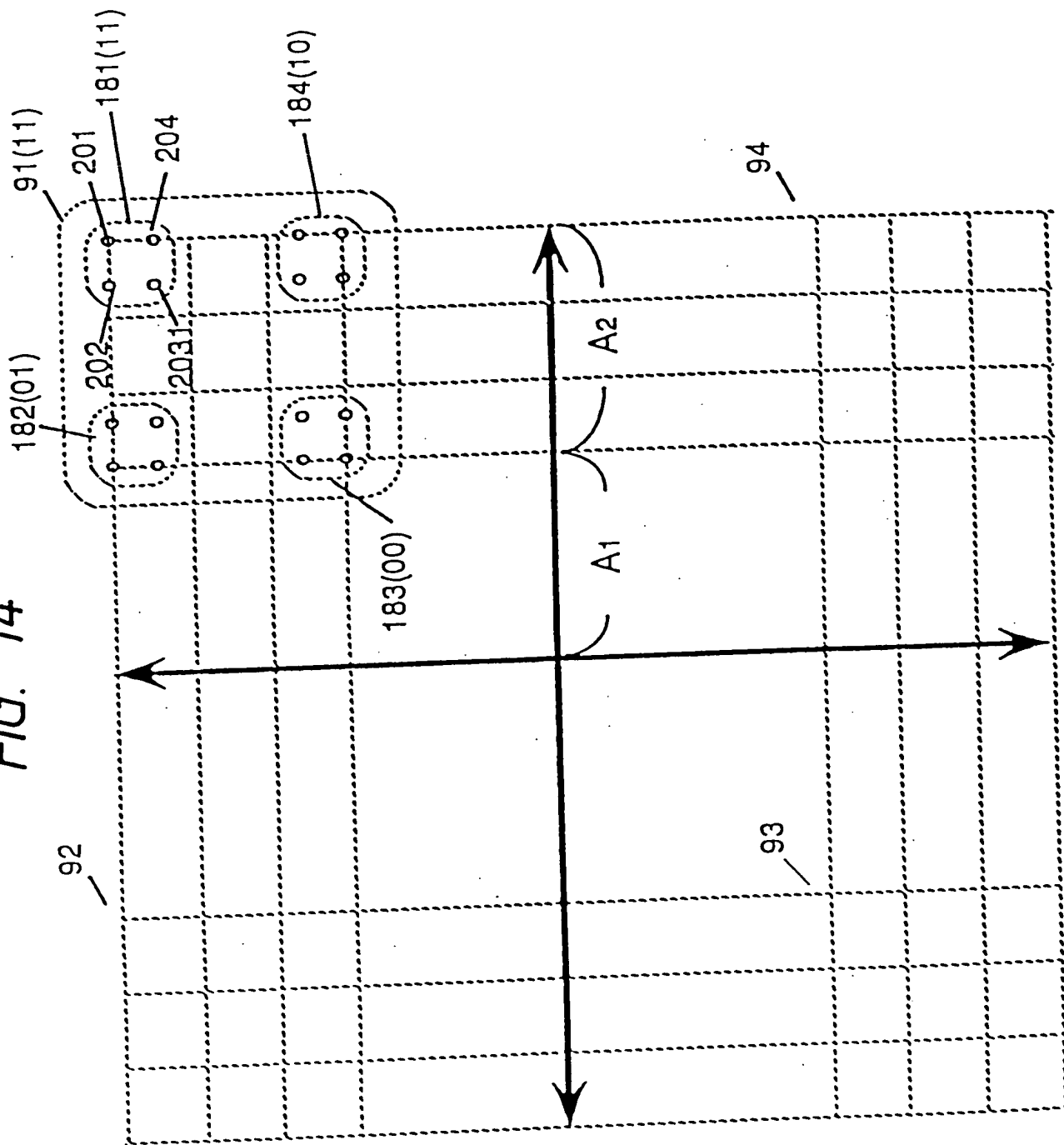


FIG. 15

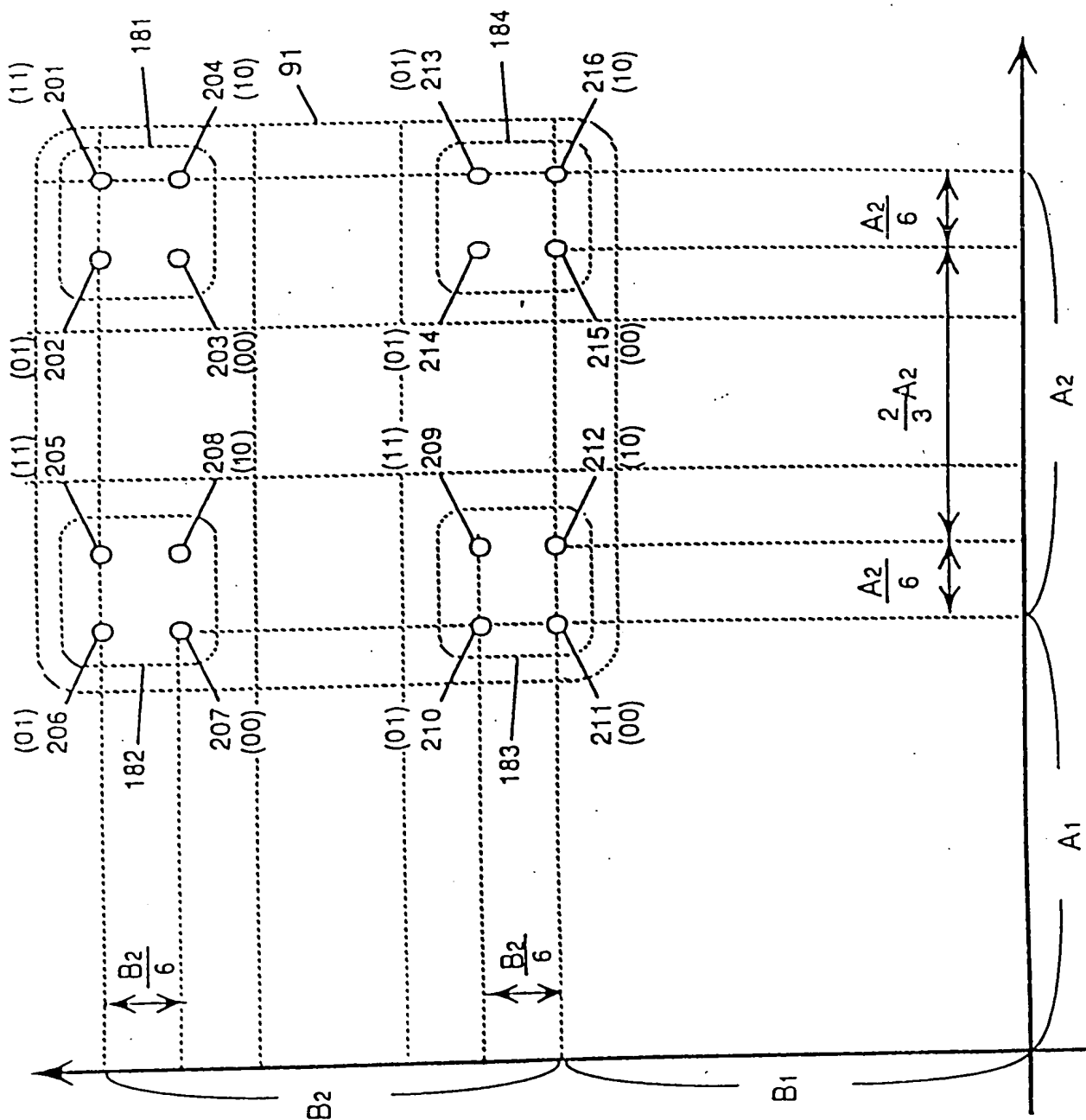


FIG. 16

FIG. 17

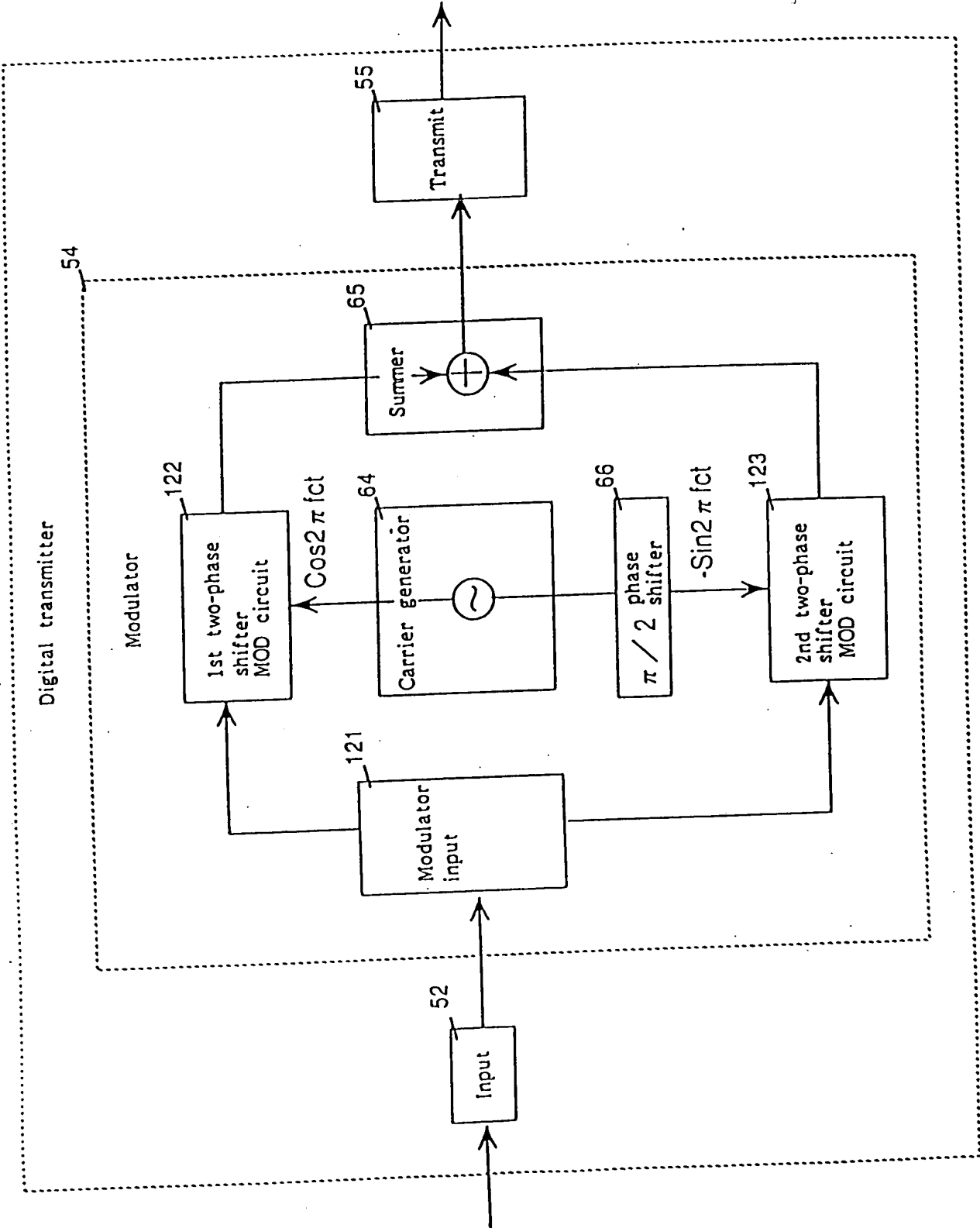


FIG. 18

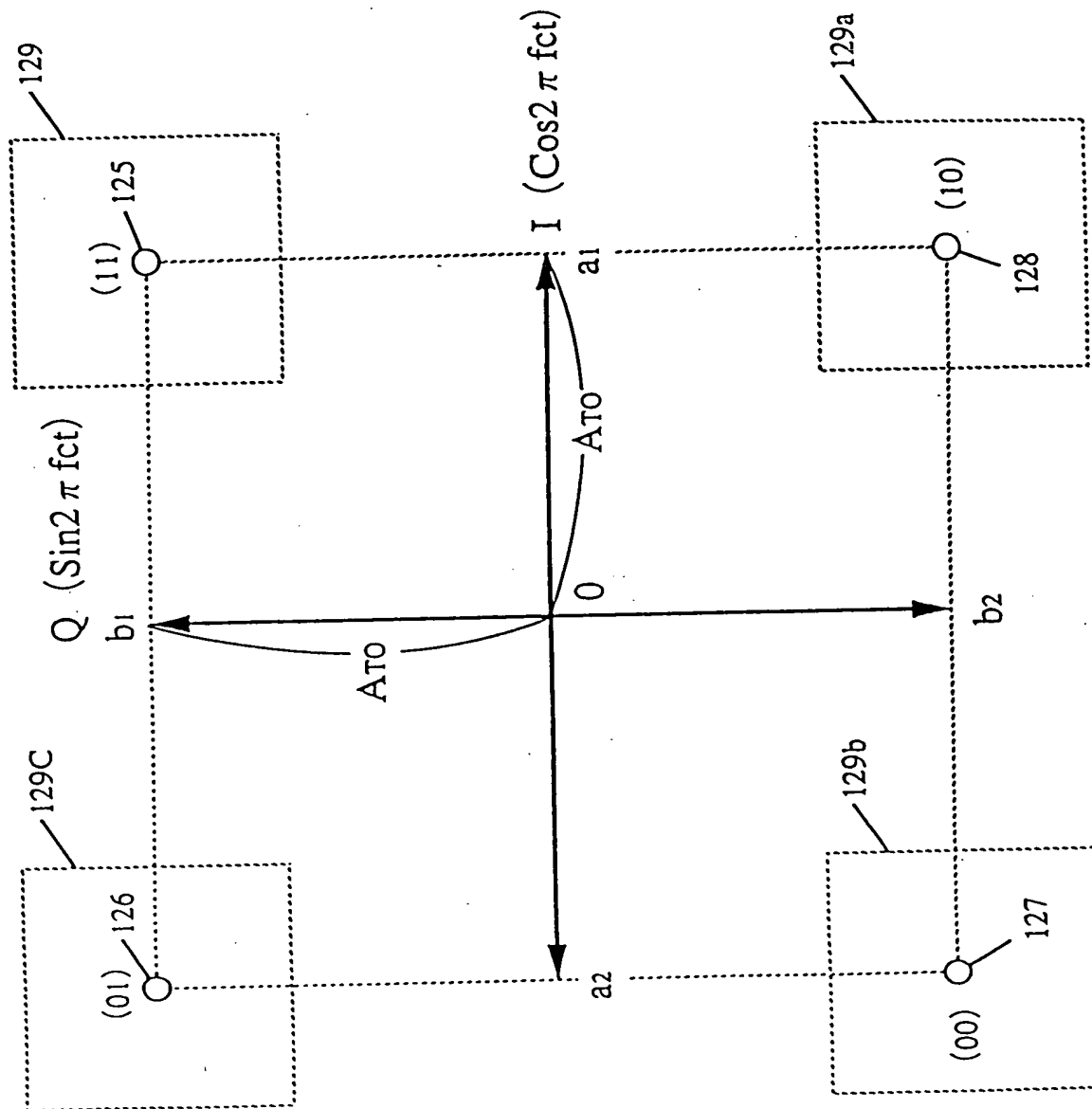
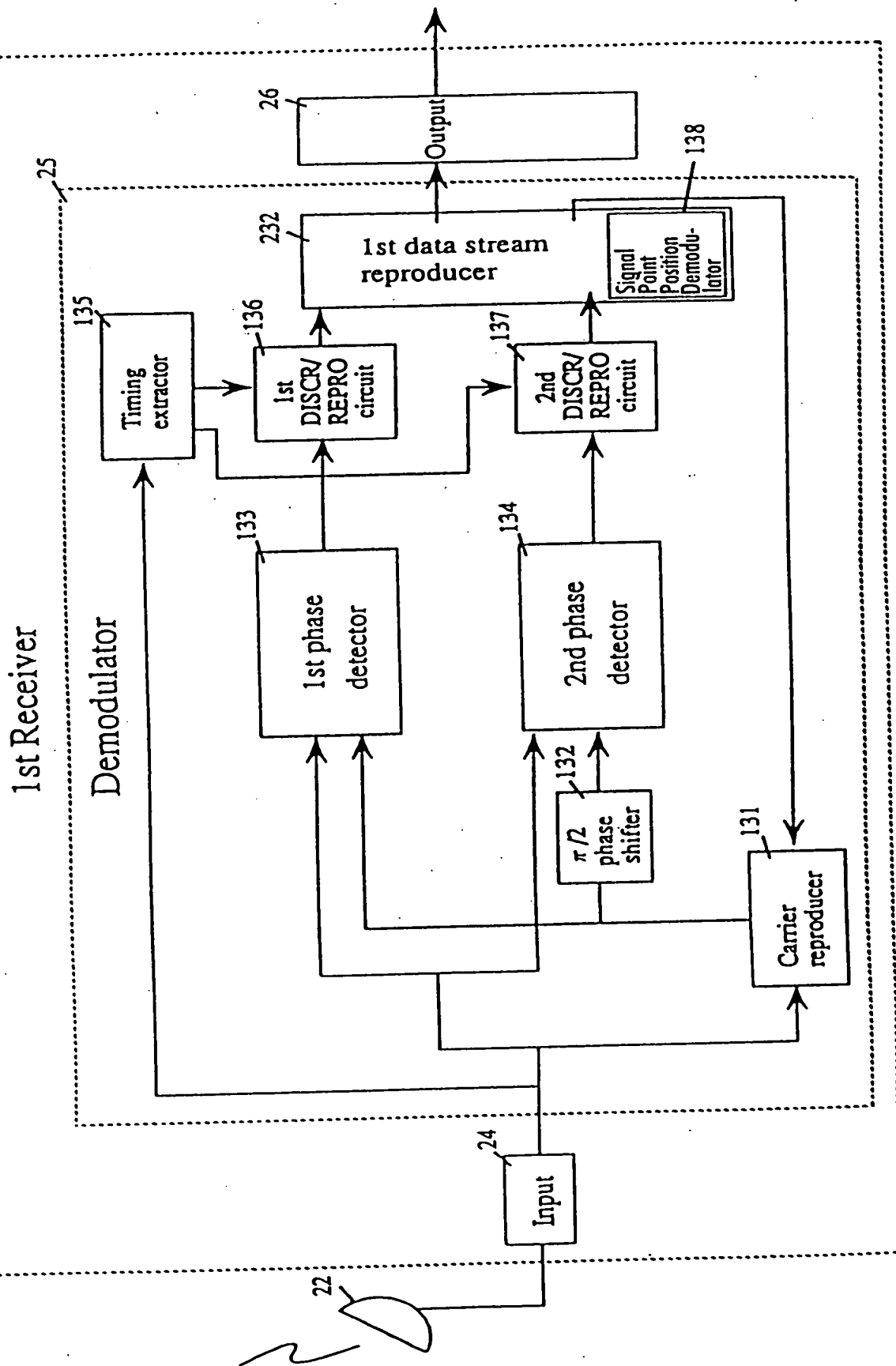


FIG. 19



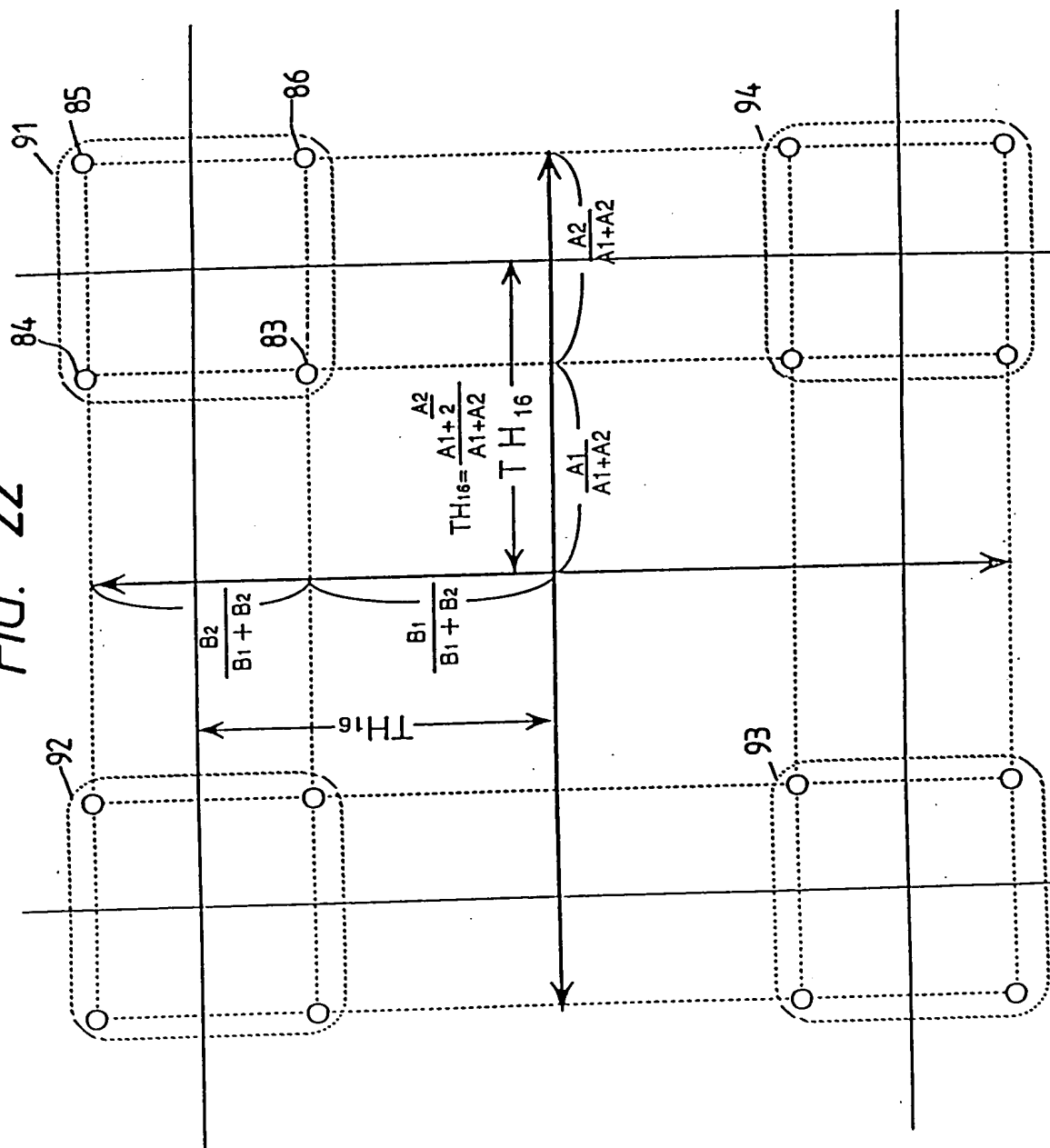


FIG. 23

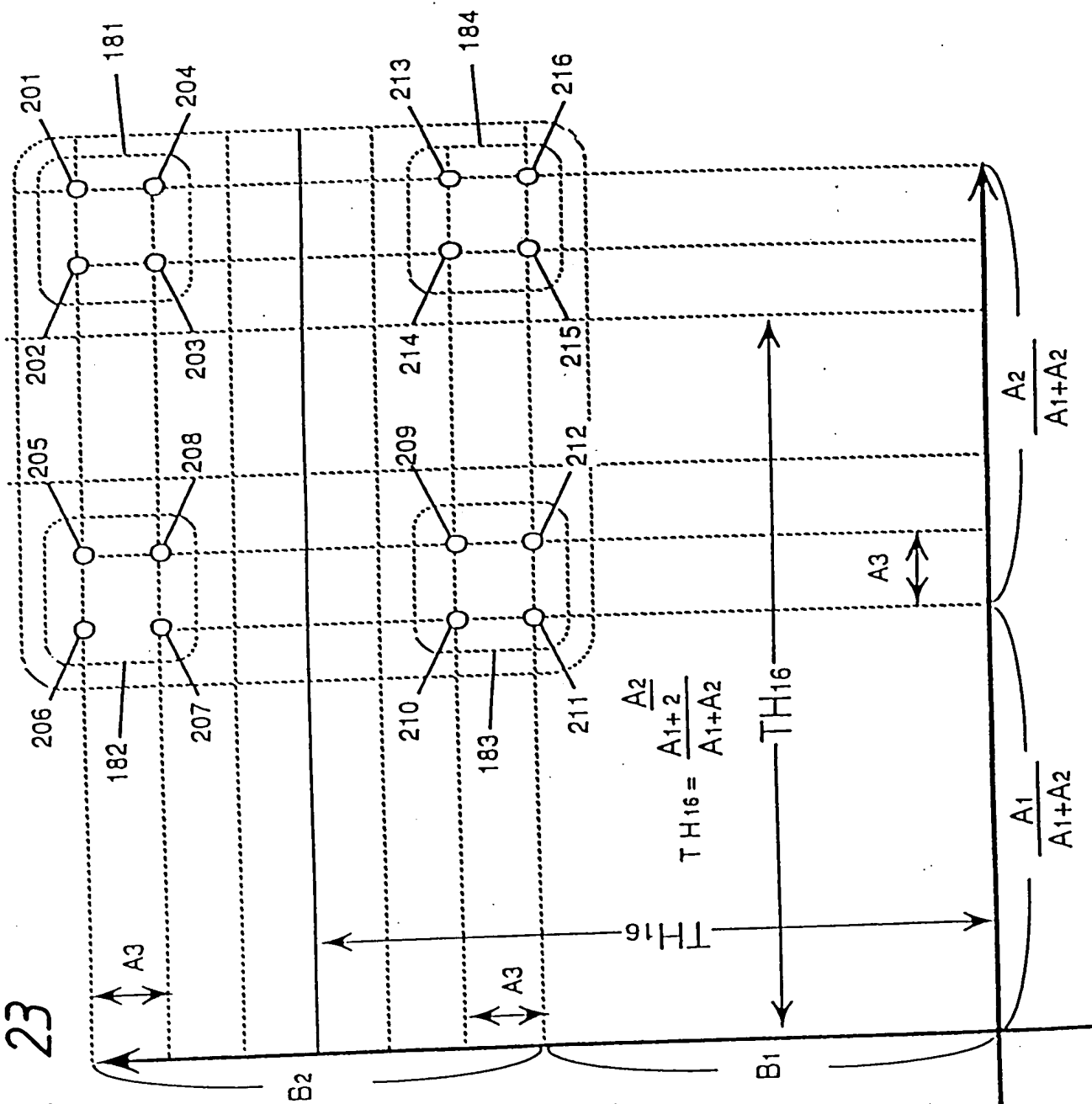


FIG. 24

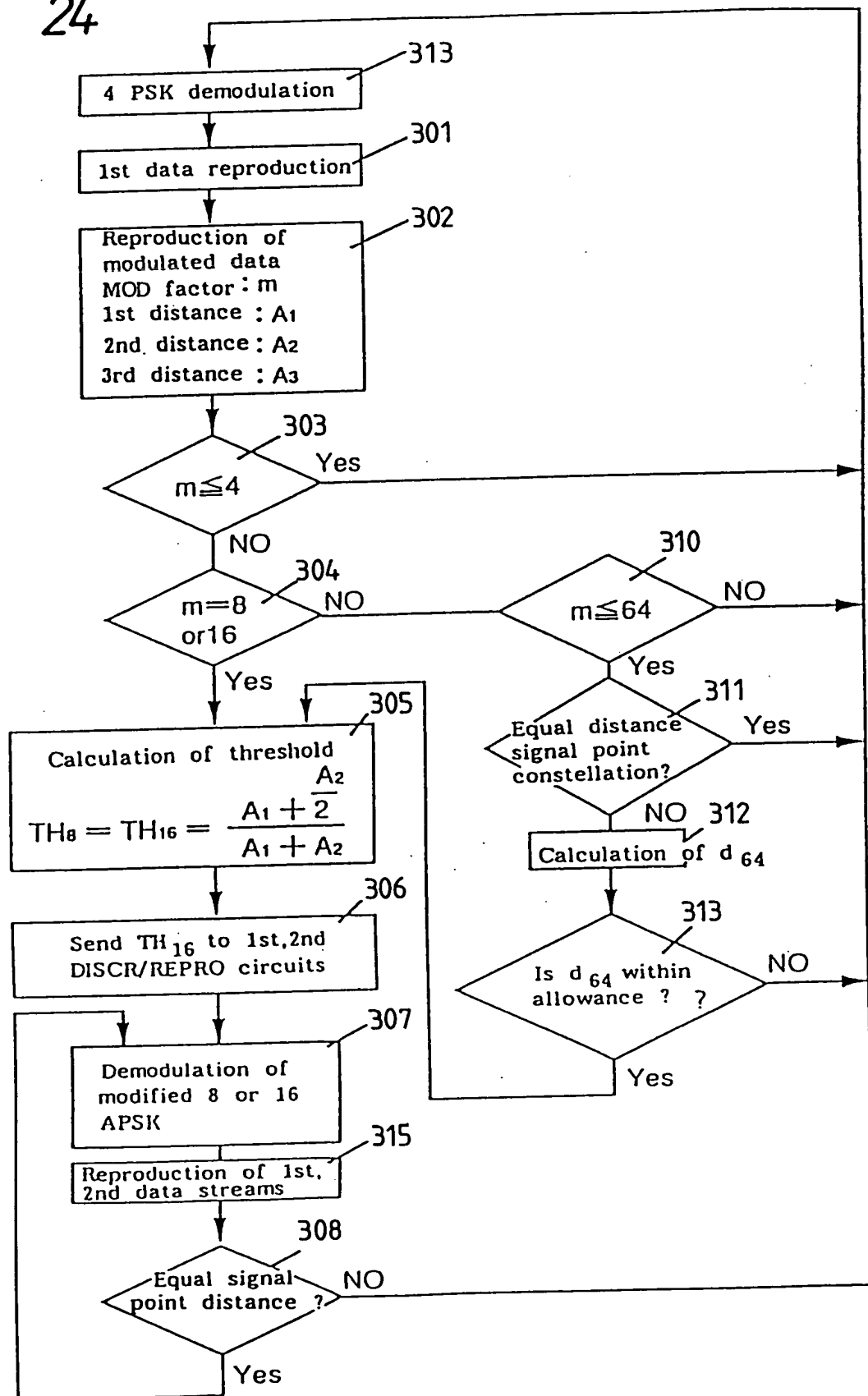


FIG. 25(a)

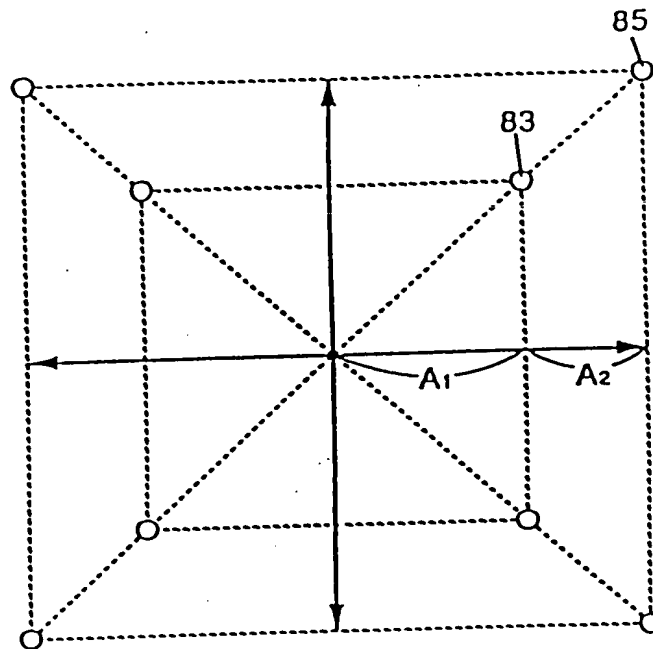


FIG. 25(b)

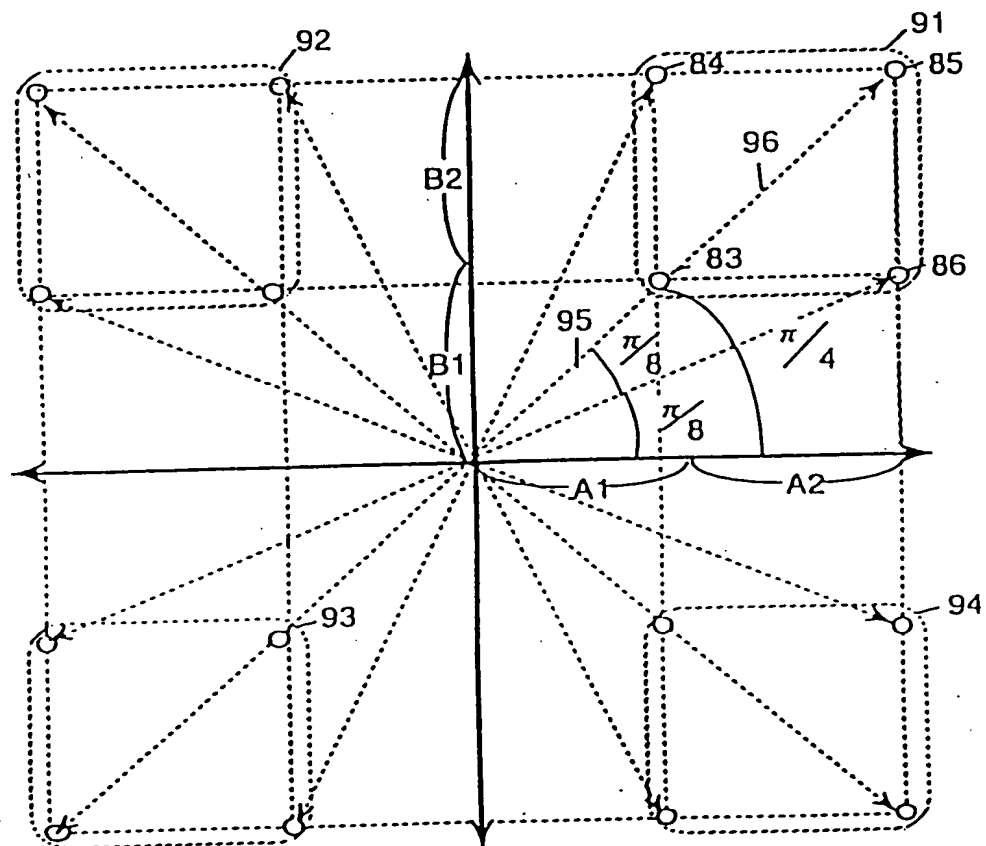


FIG. 26

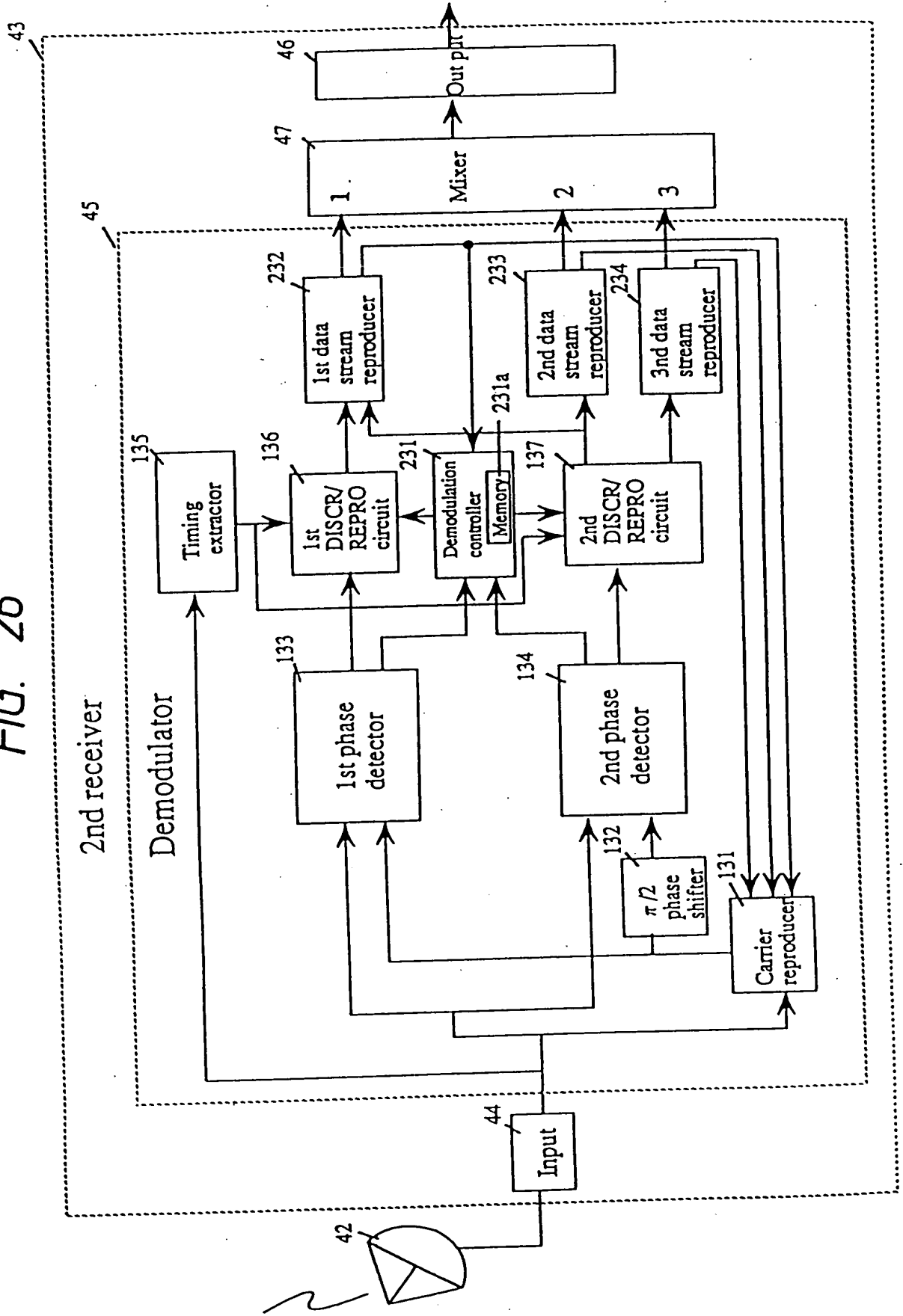


FIG. 27

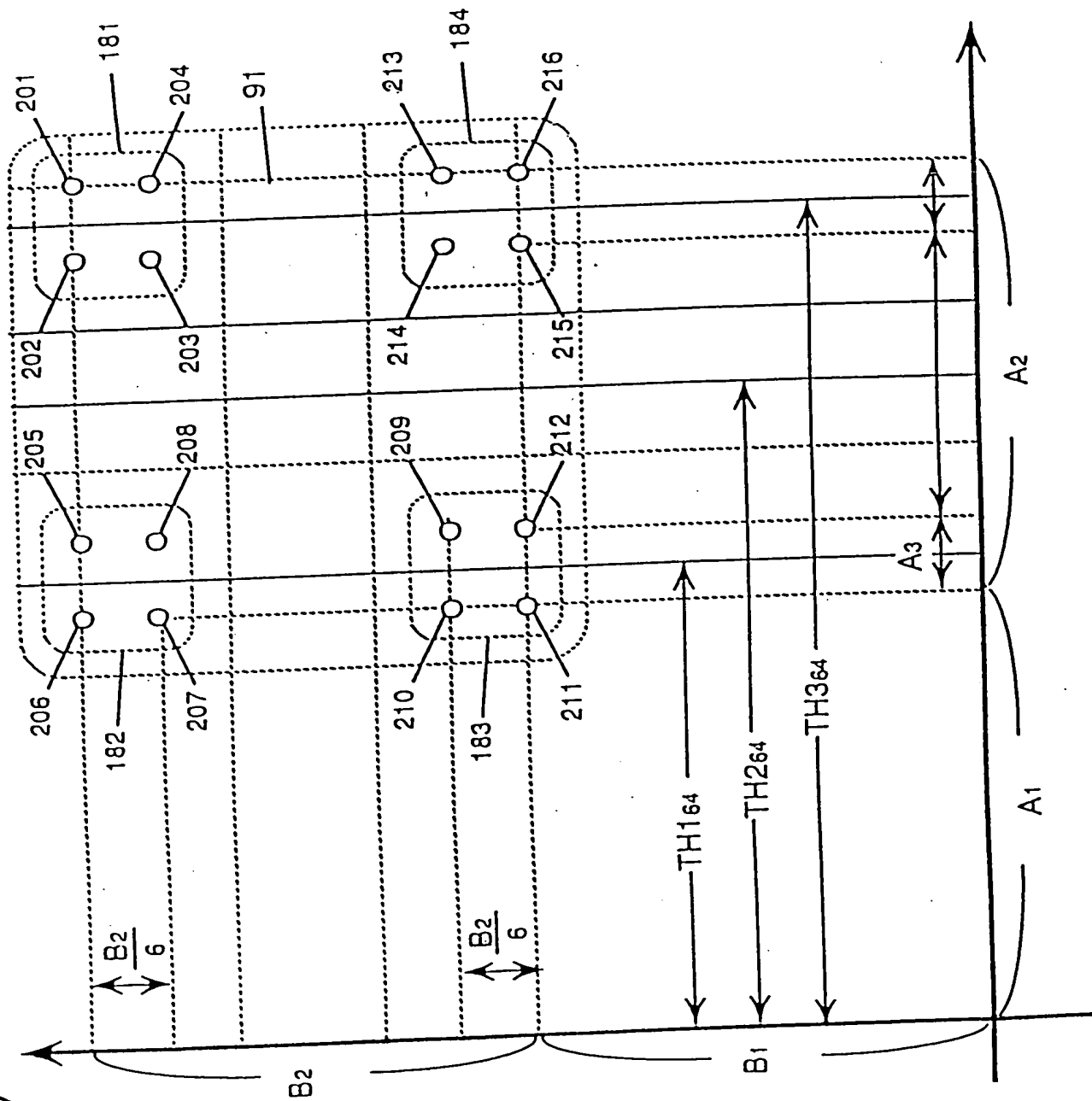


FIG. 28

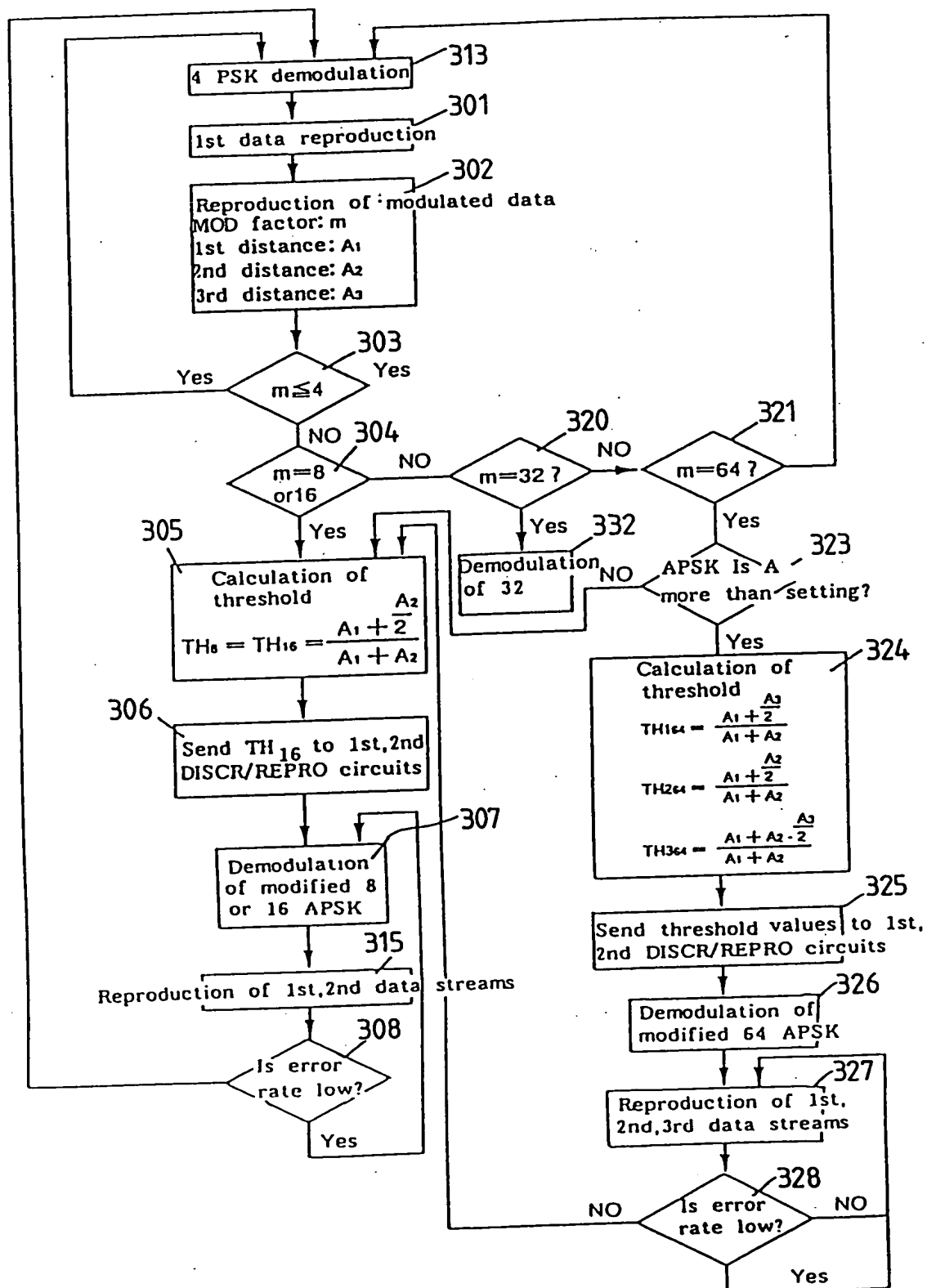


FIG. 29

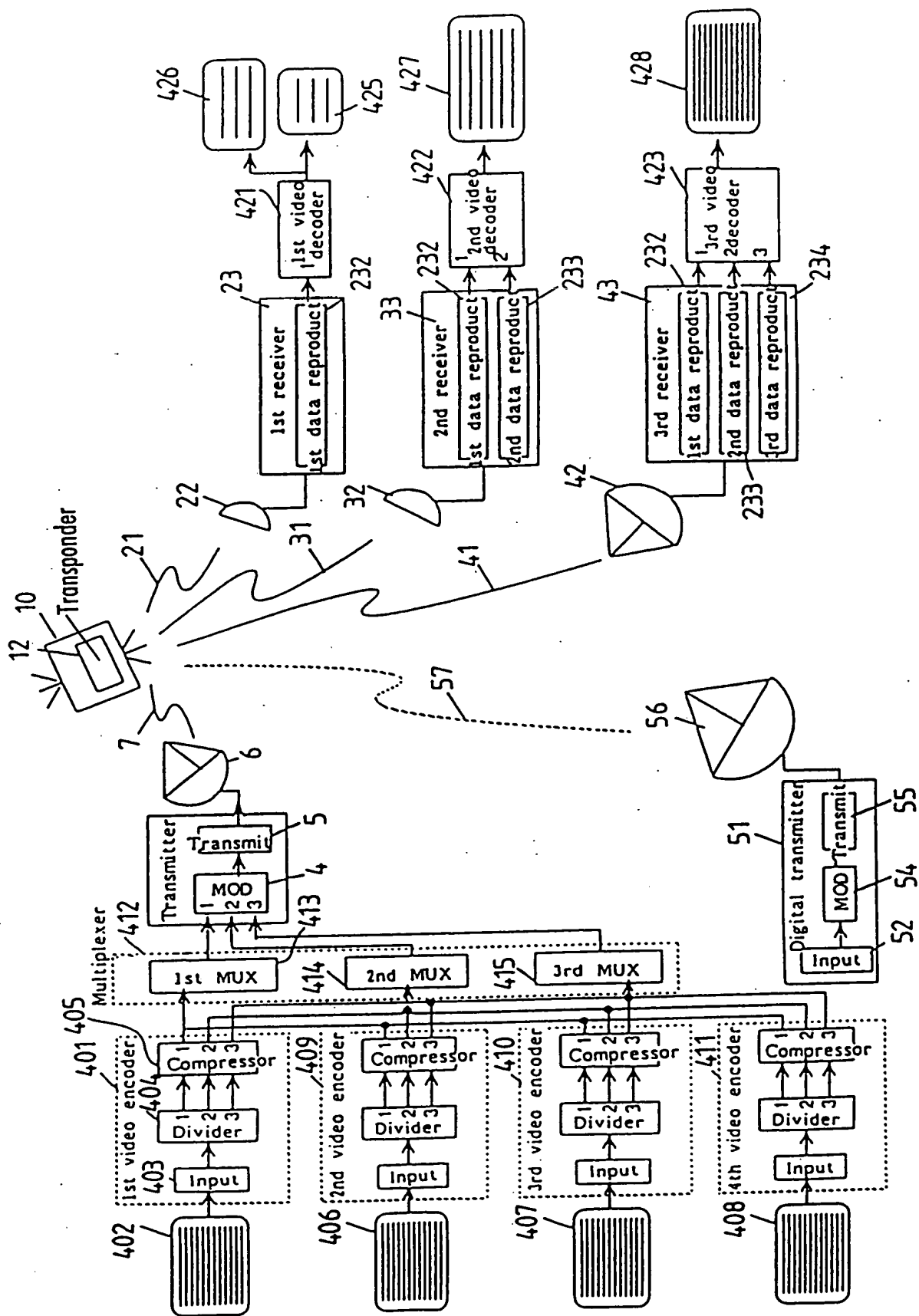


FIG. 30

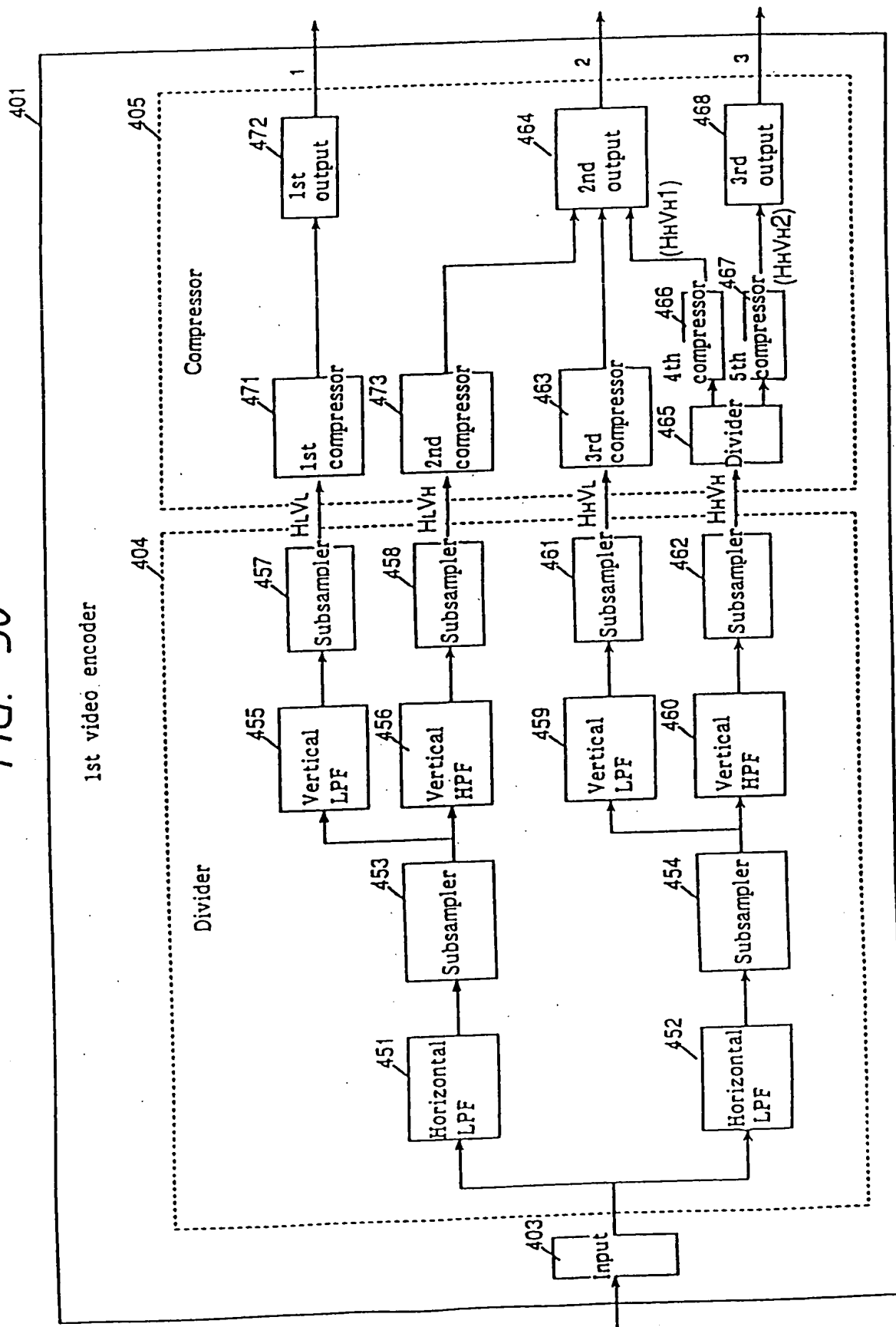


FIG. 31

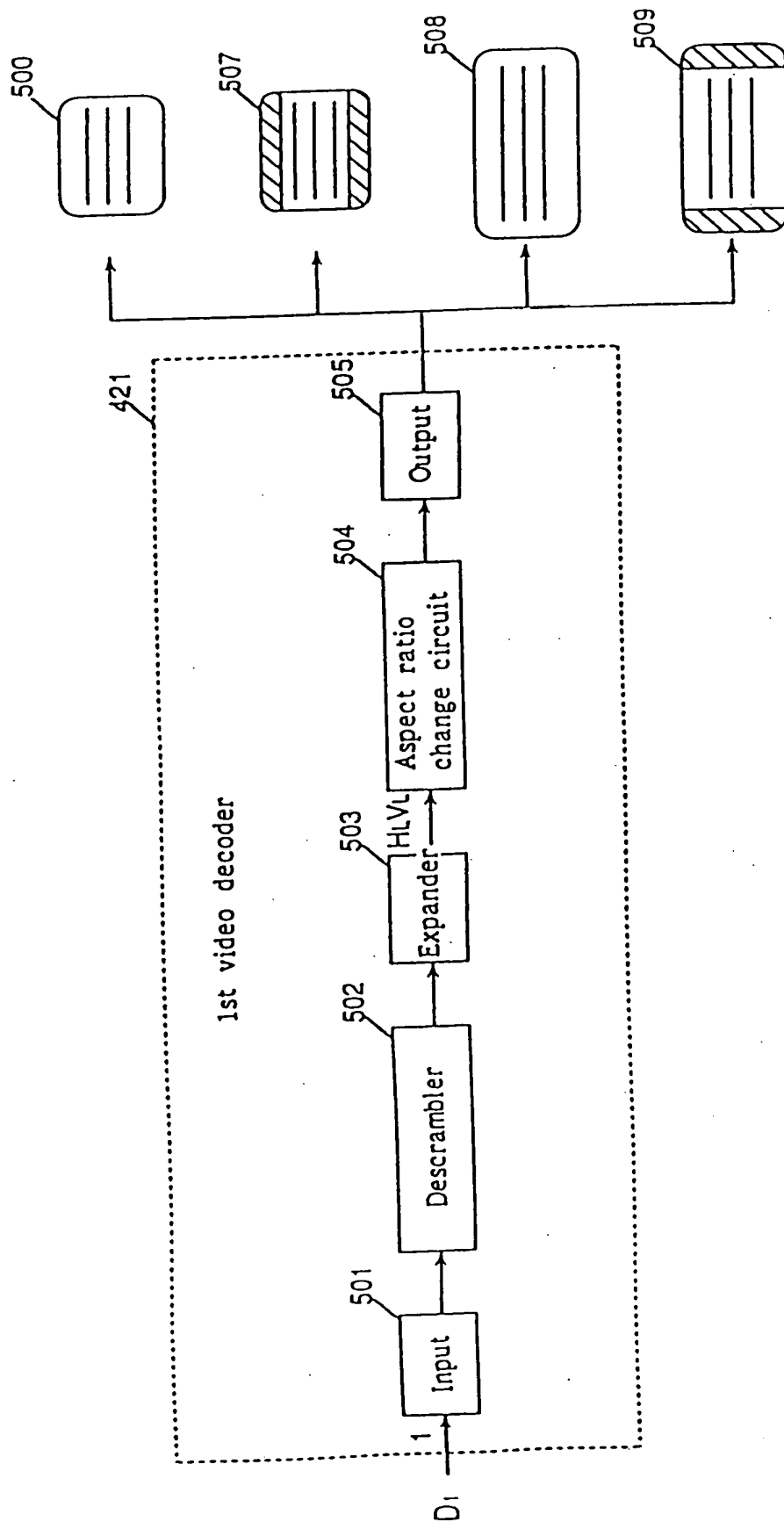


FIG. 32

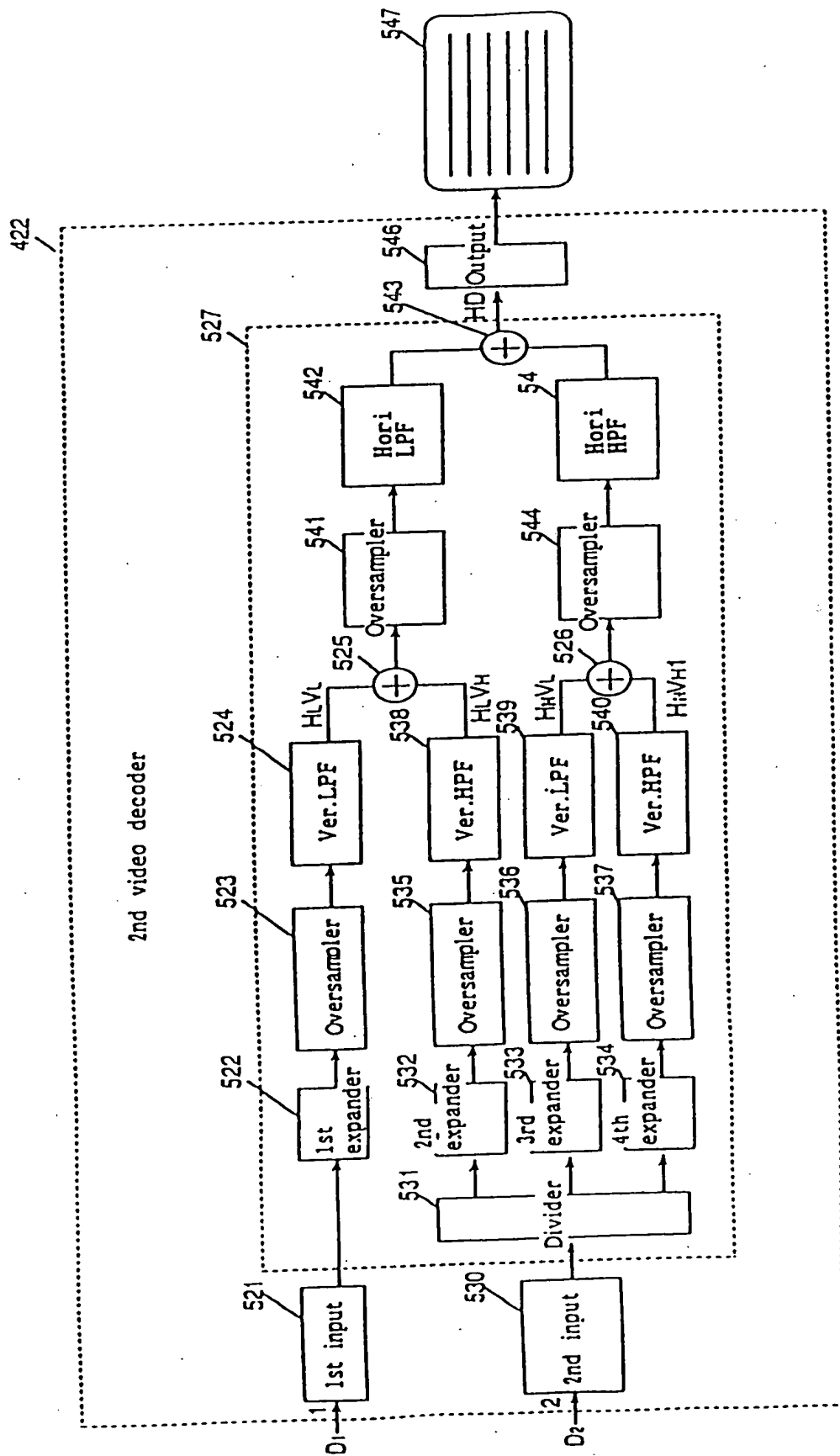


FIG. 33

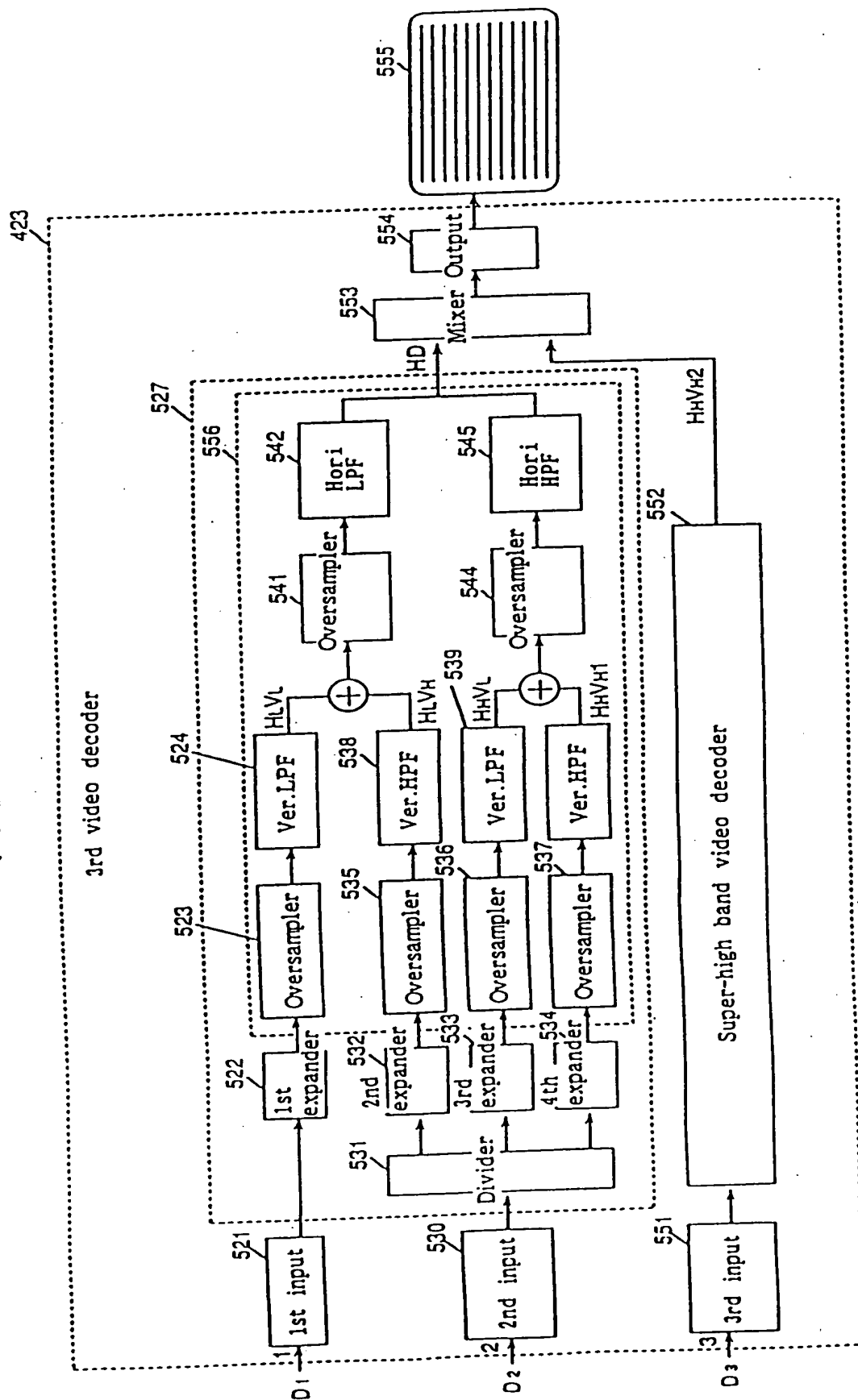


FIG. 34

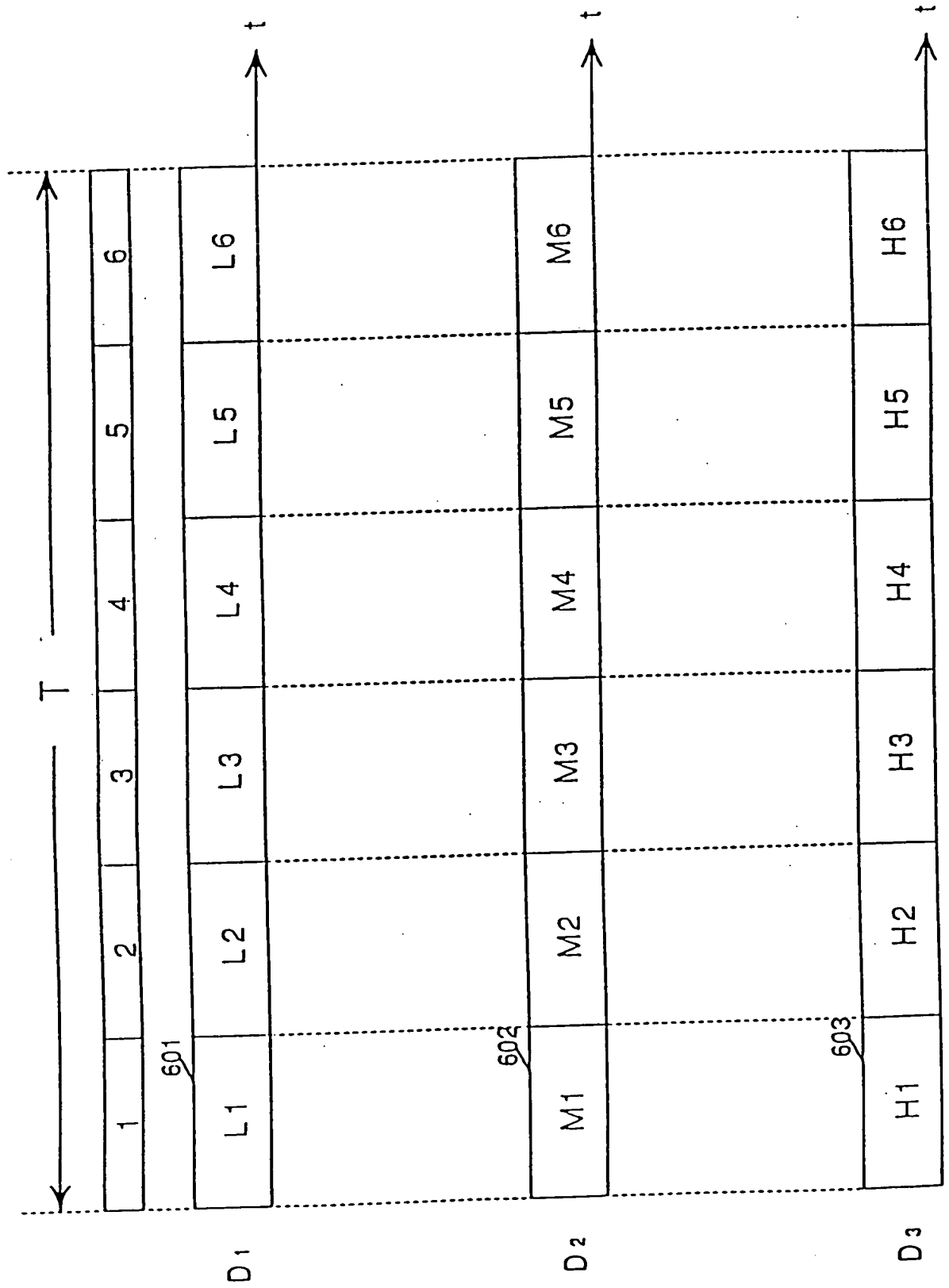


FIG. 35

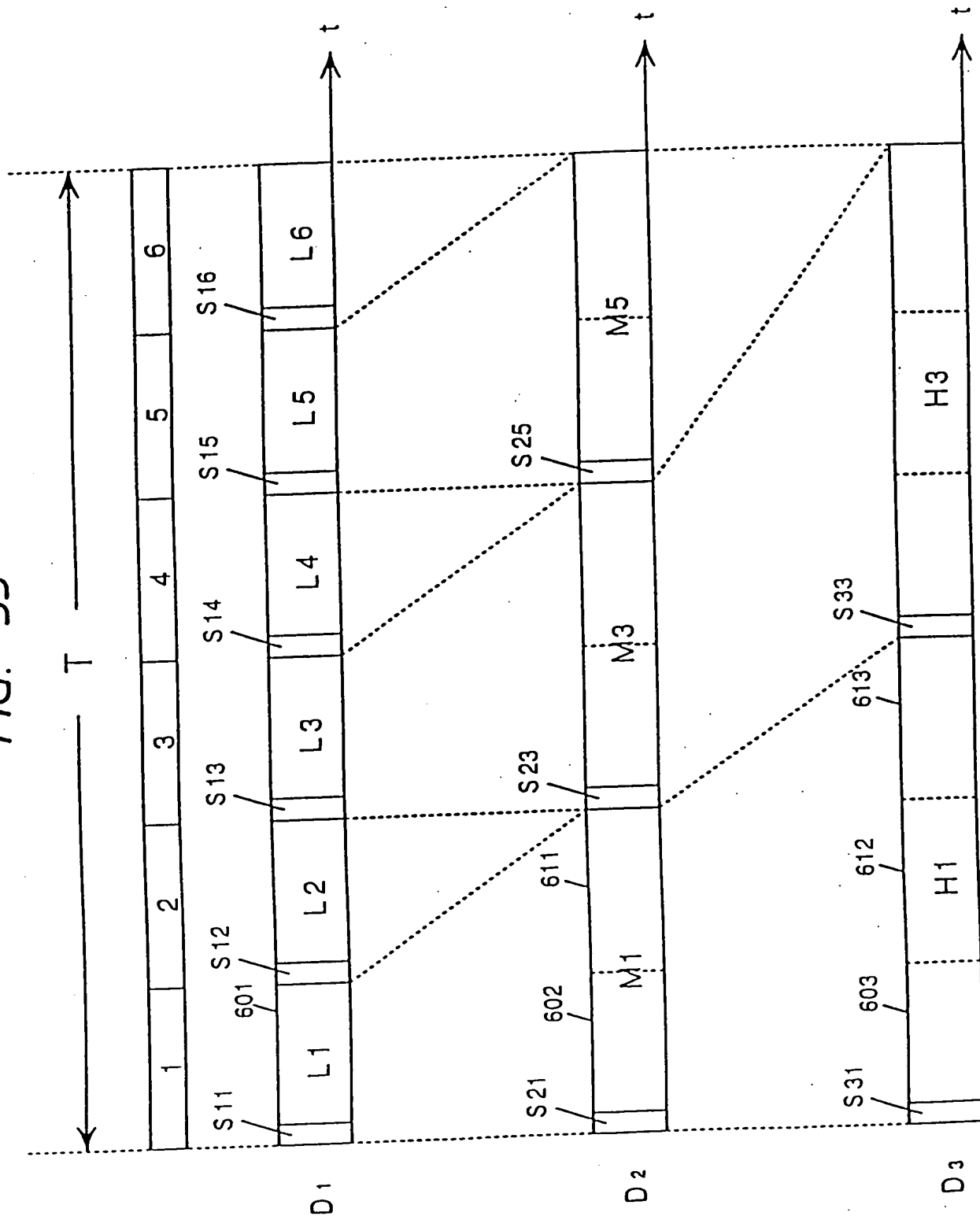


FIG. 36

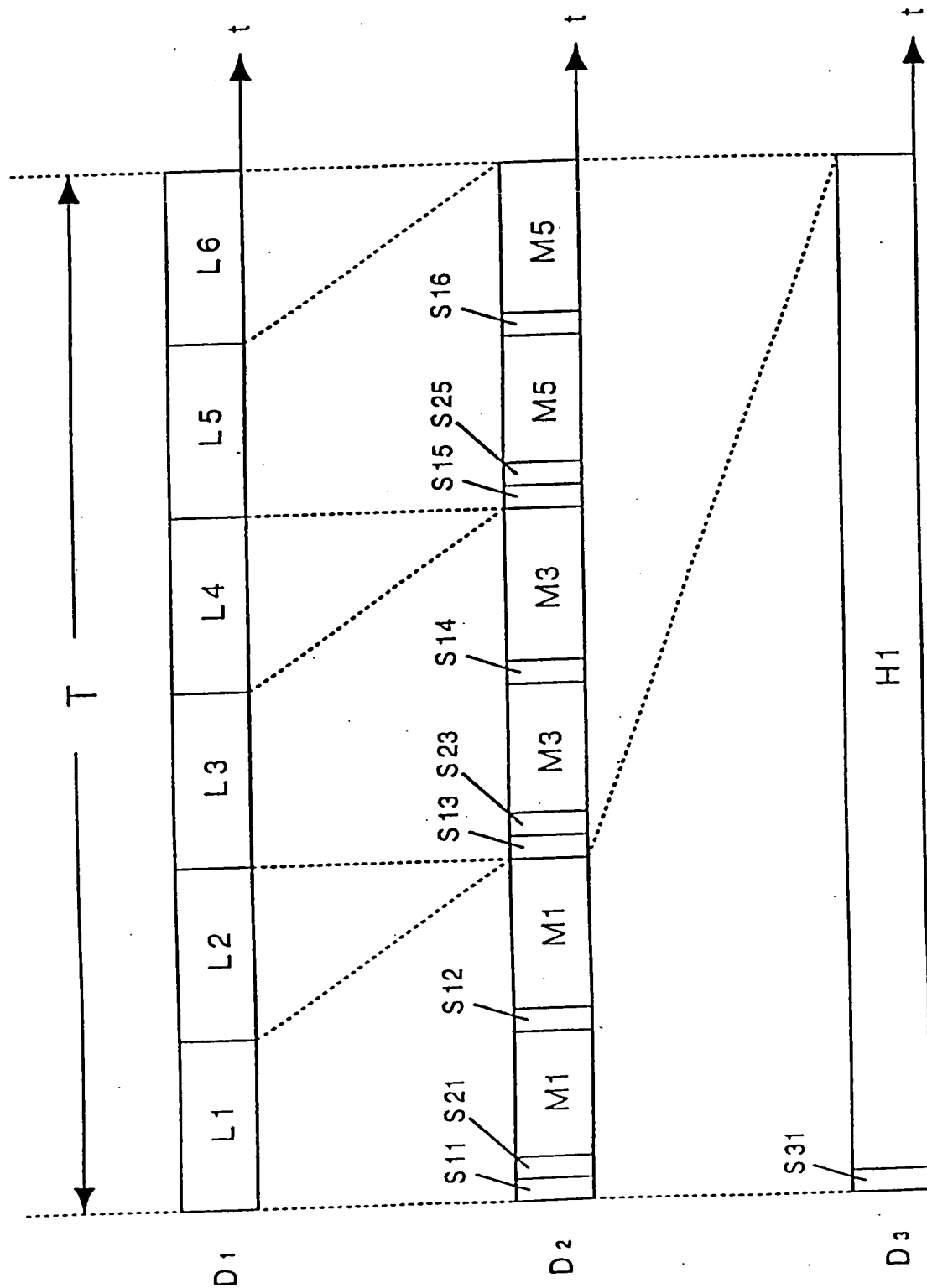
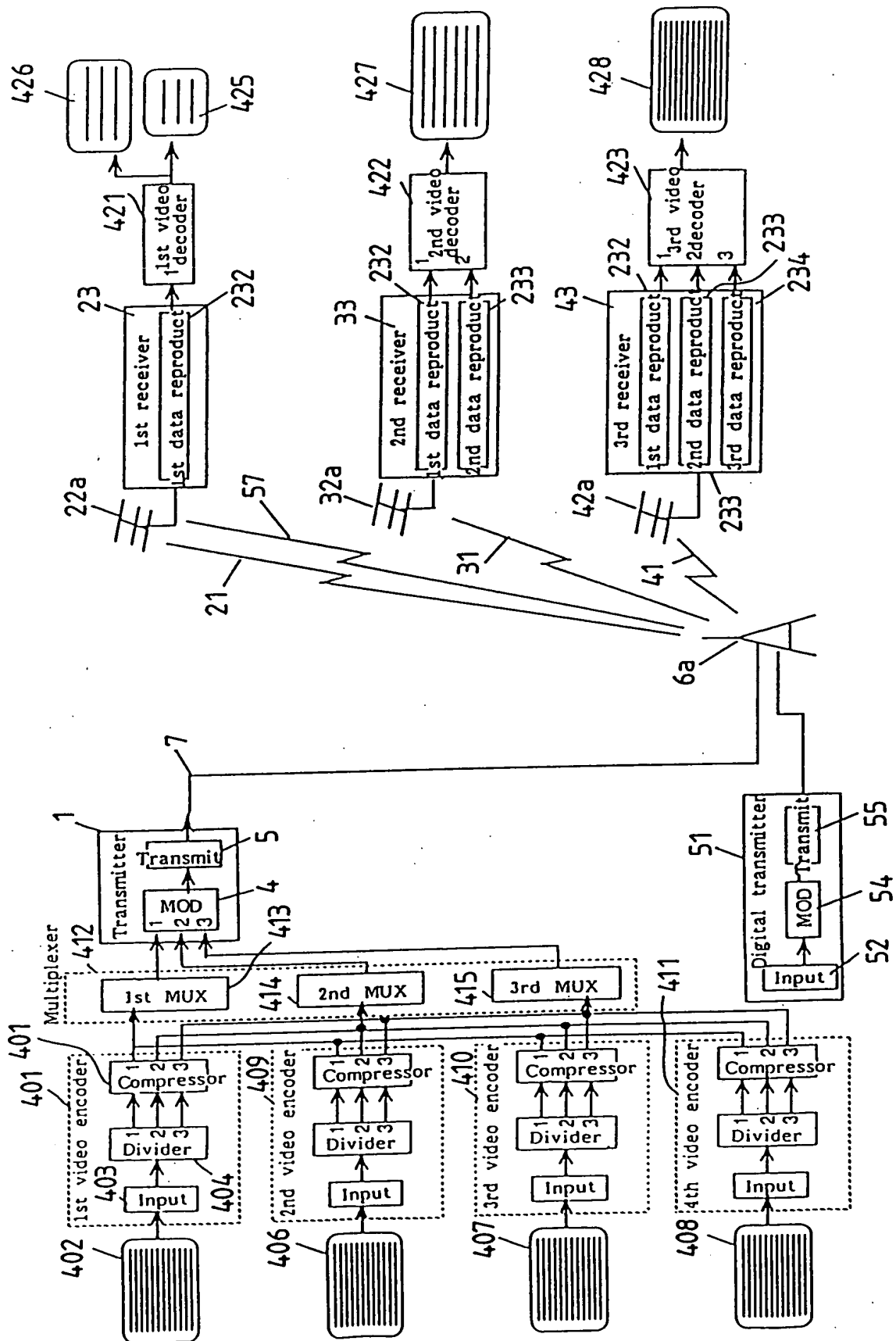


FIG. 37



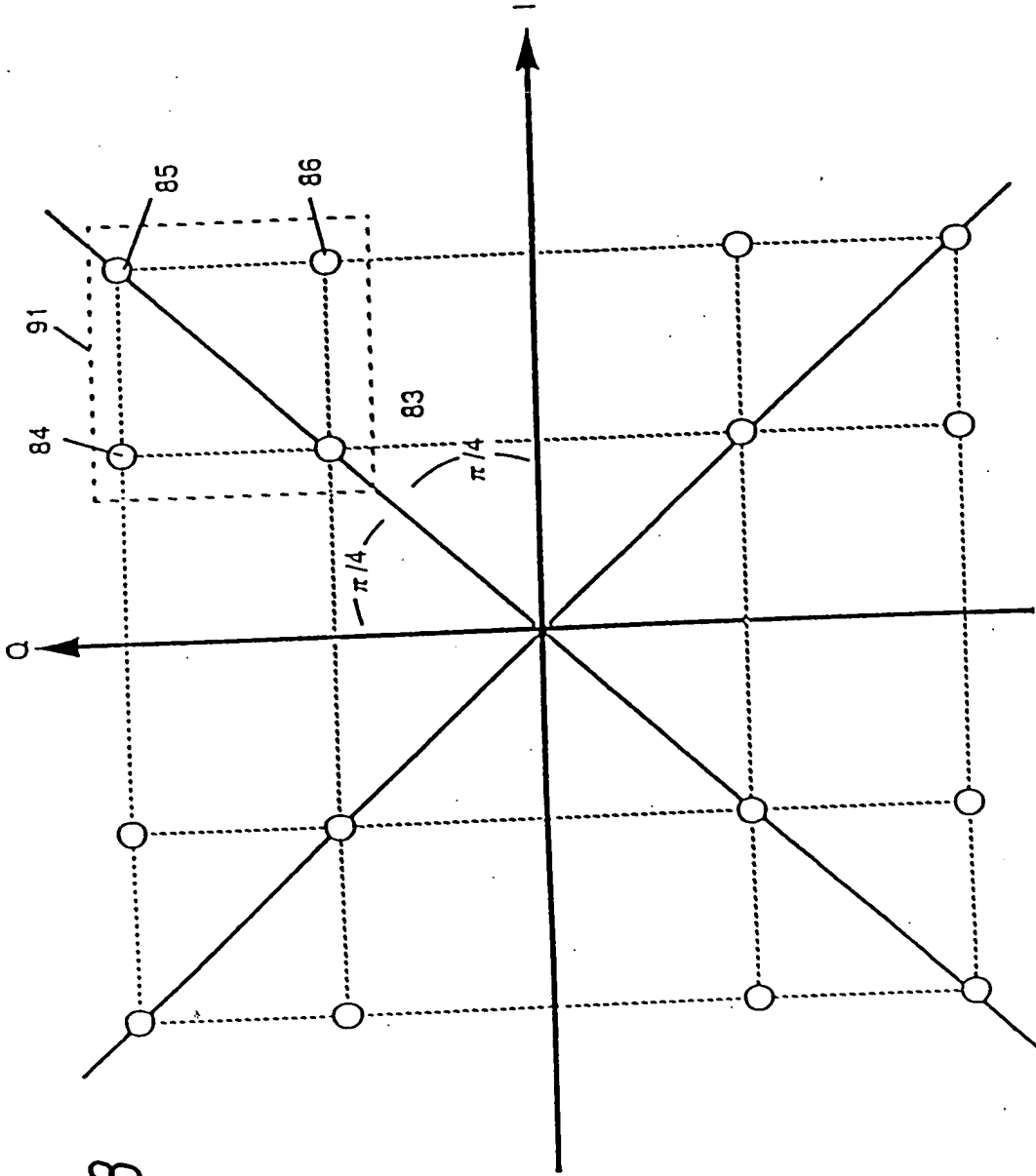
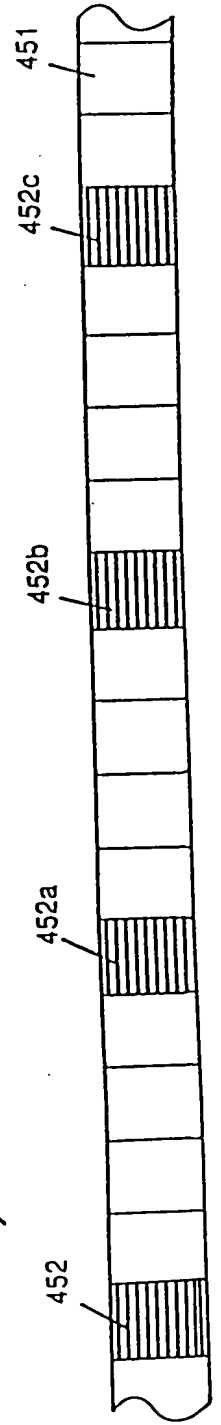
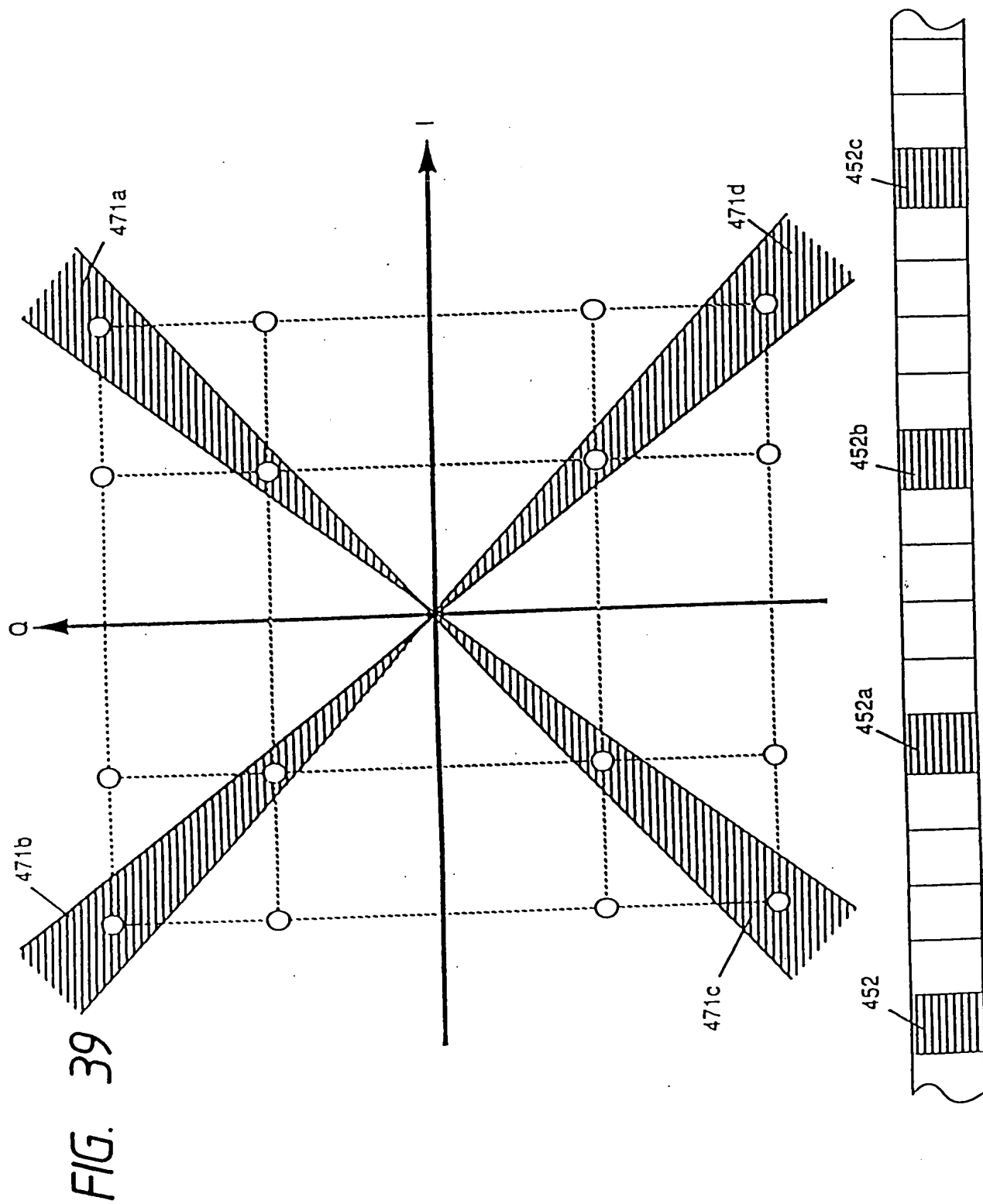


FIG. 38





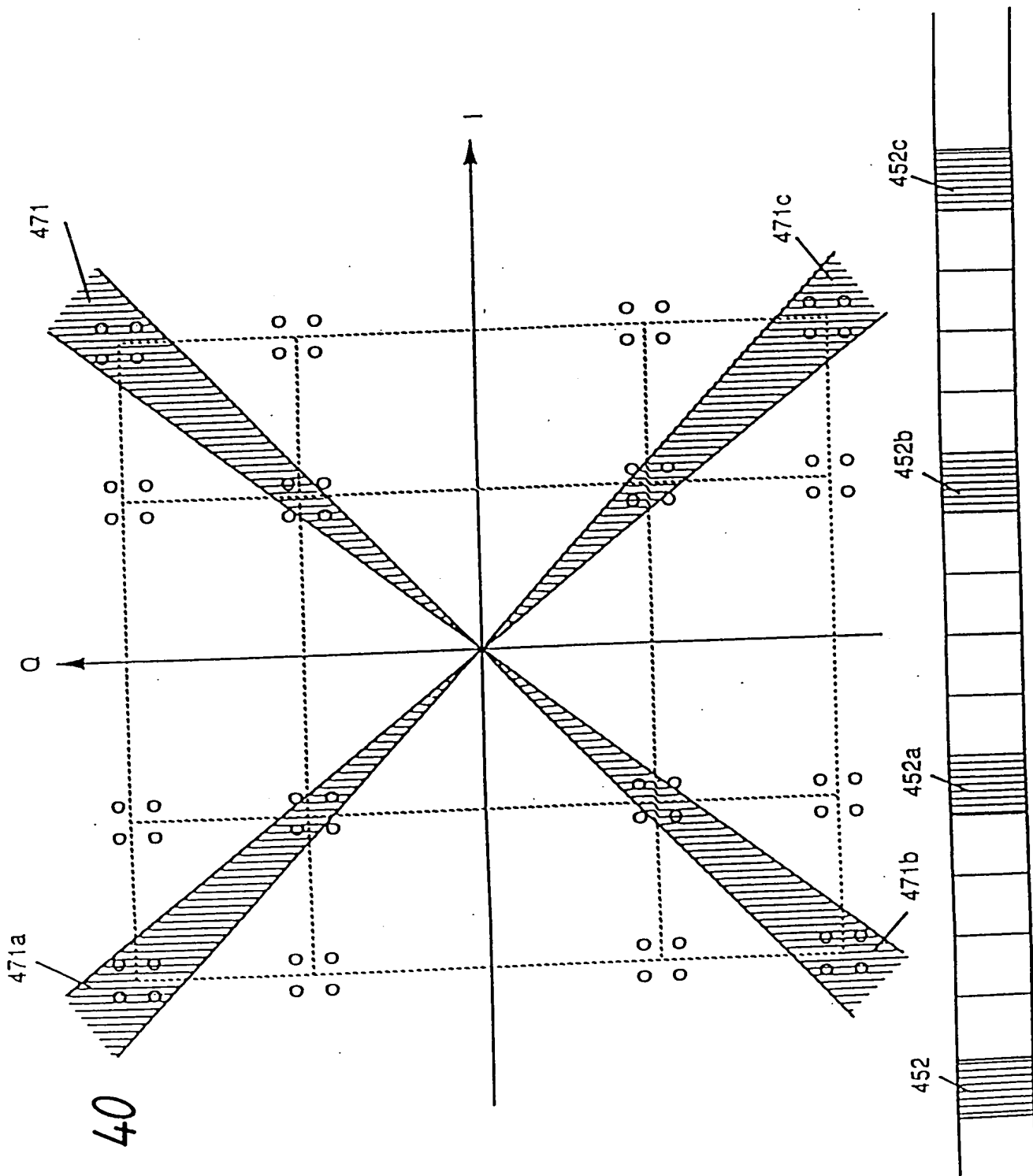


FIG. 41

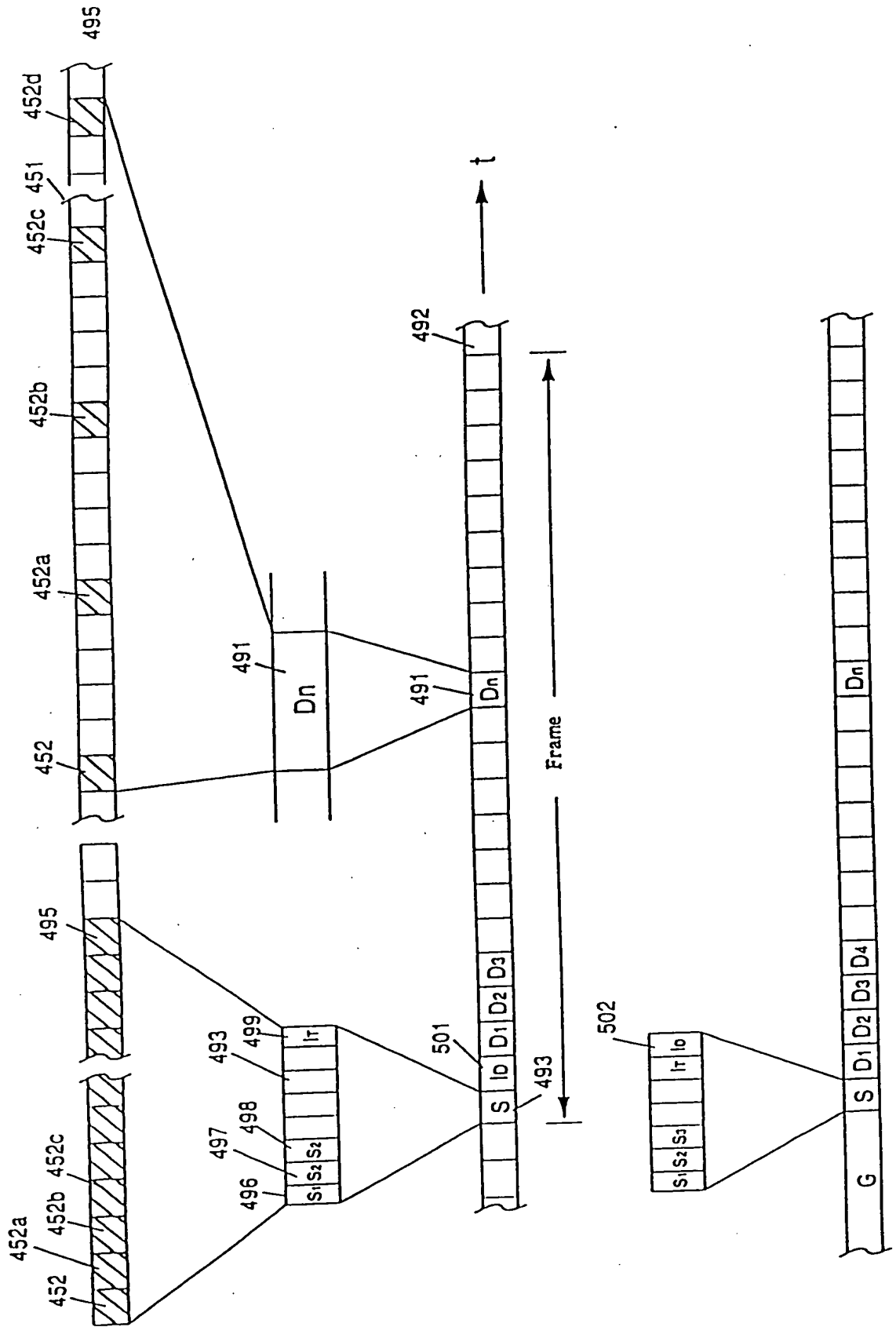


FIG. 43

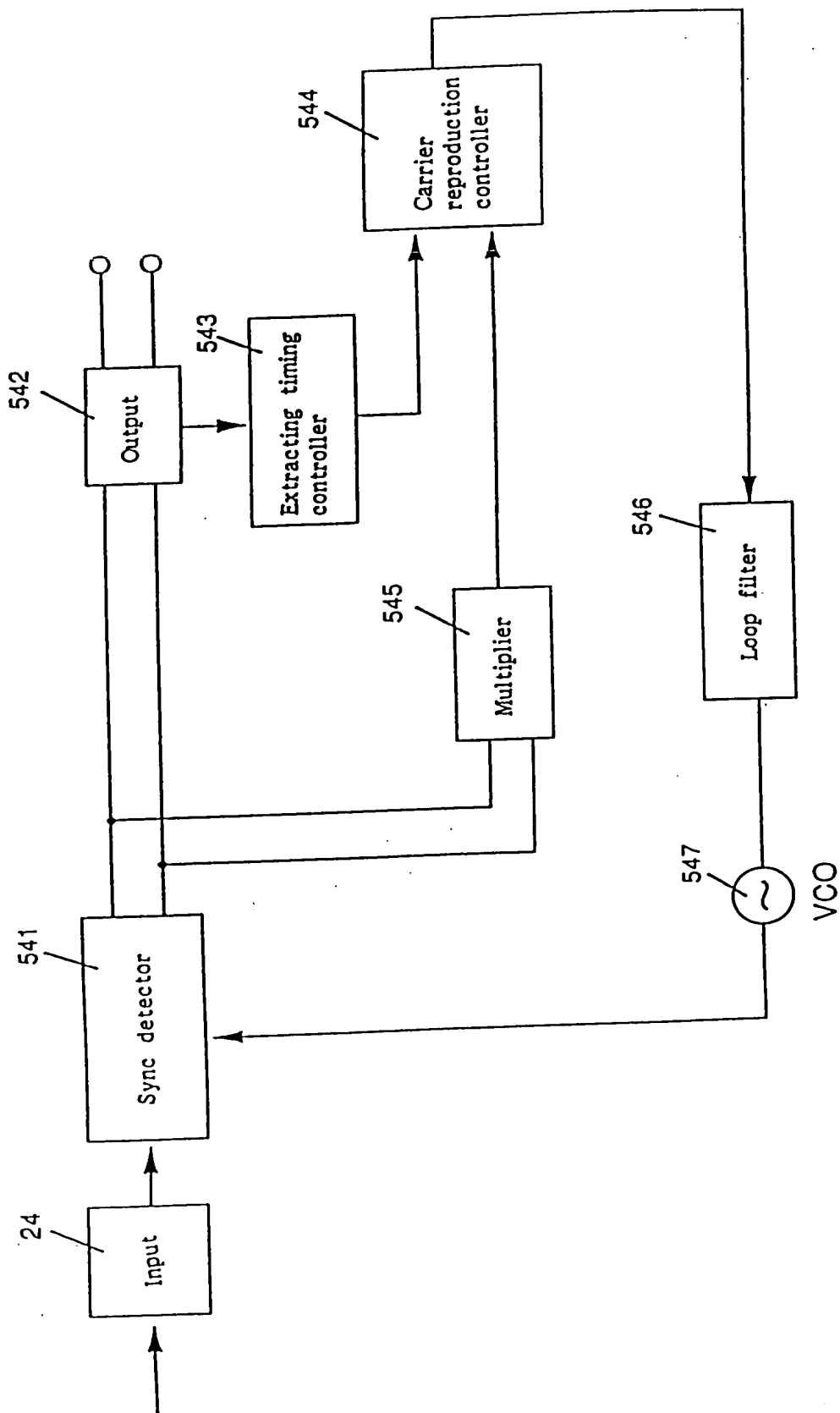


FIG. 44

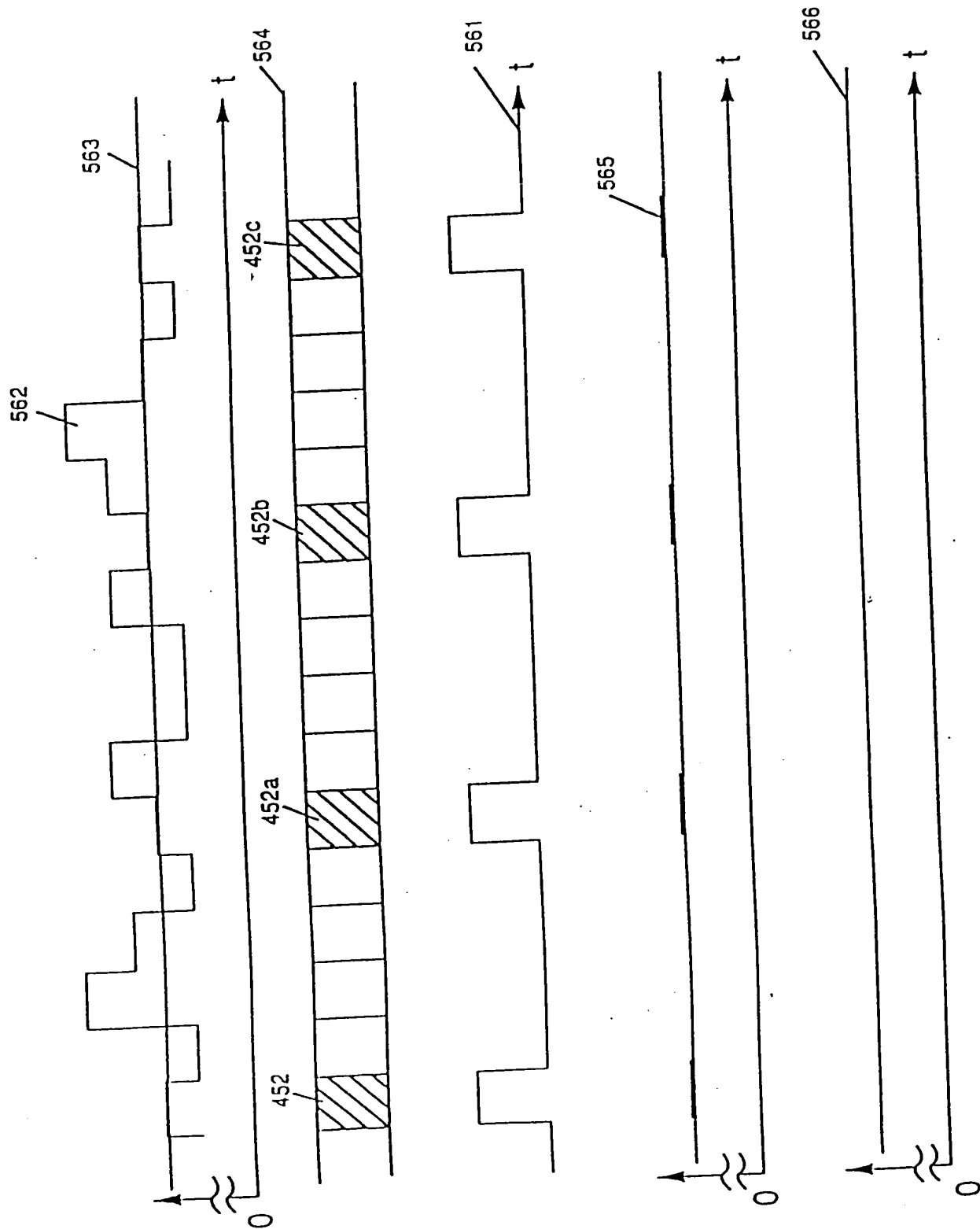


FIG. 45

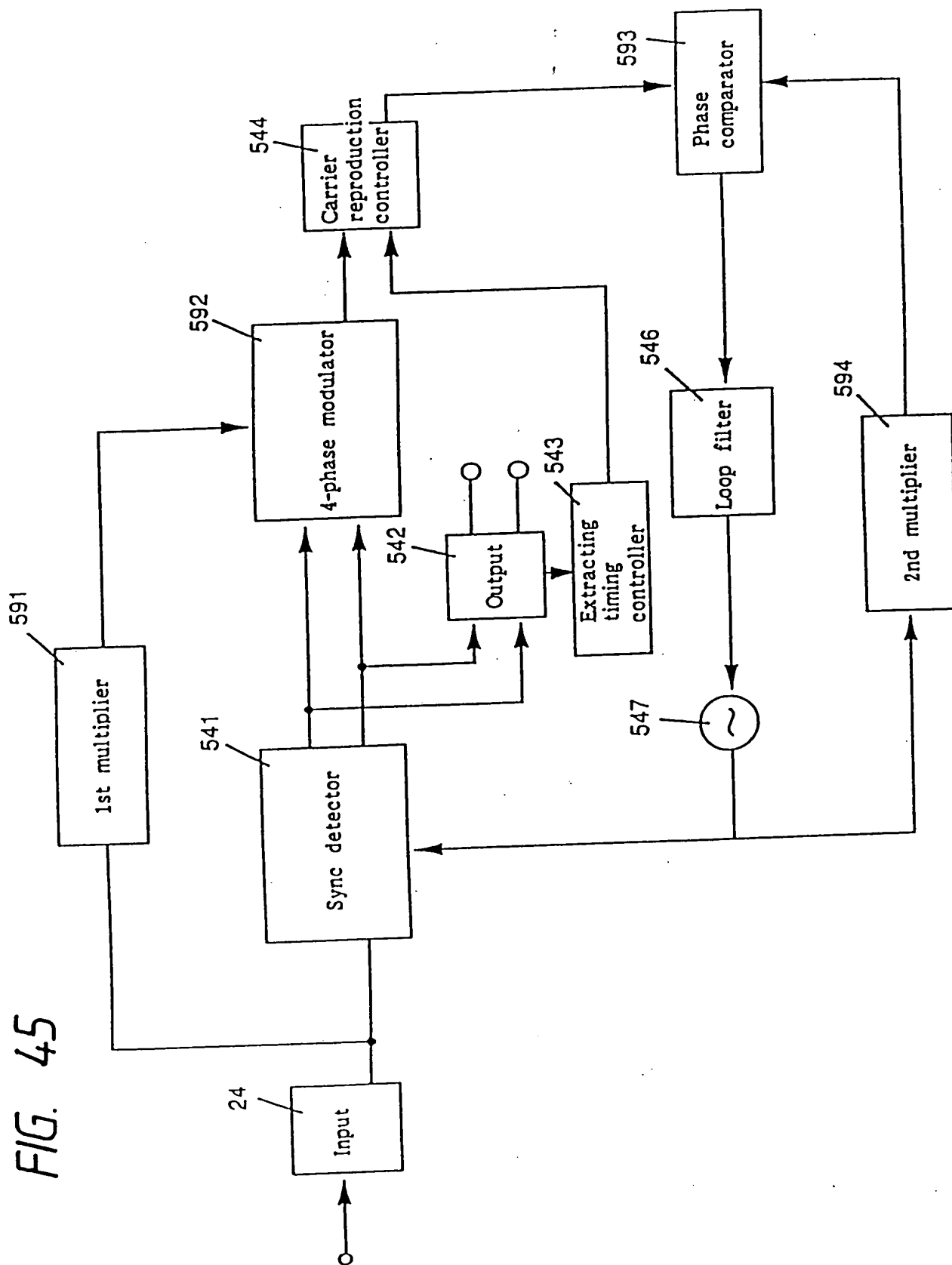


FIG. 46

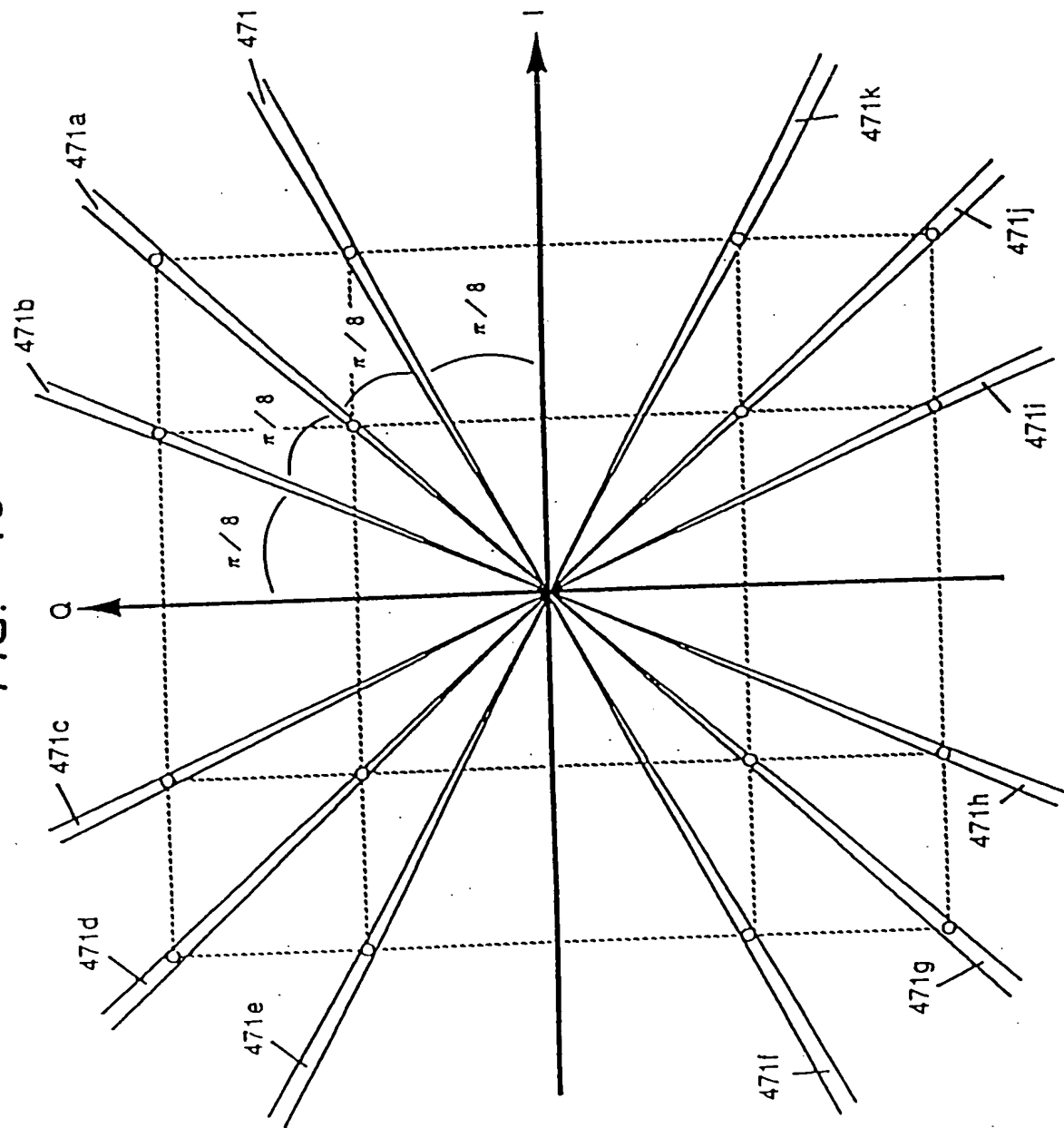


FIG. 47

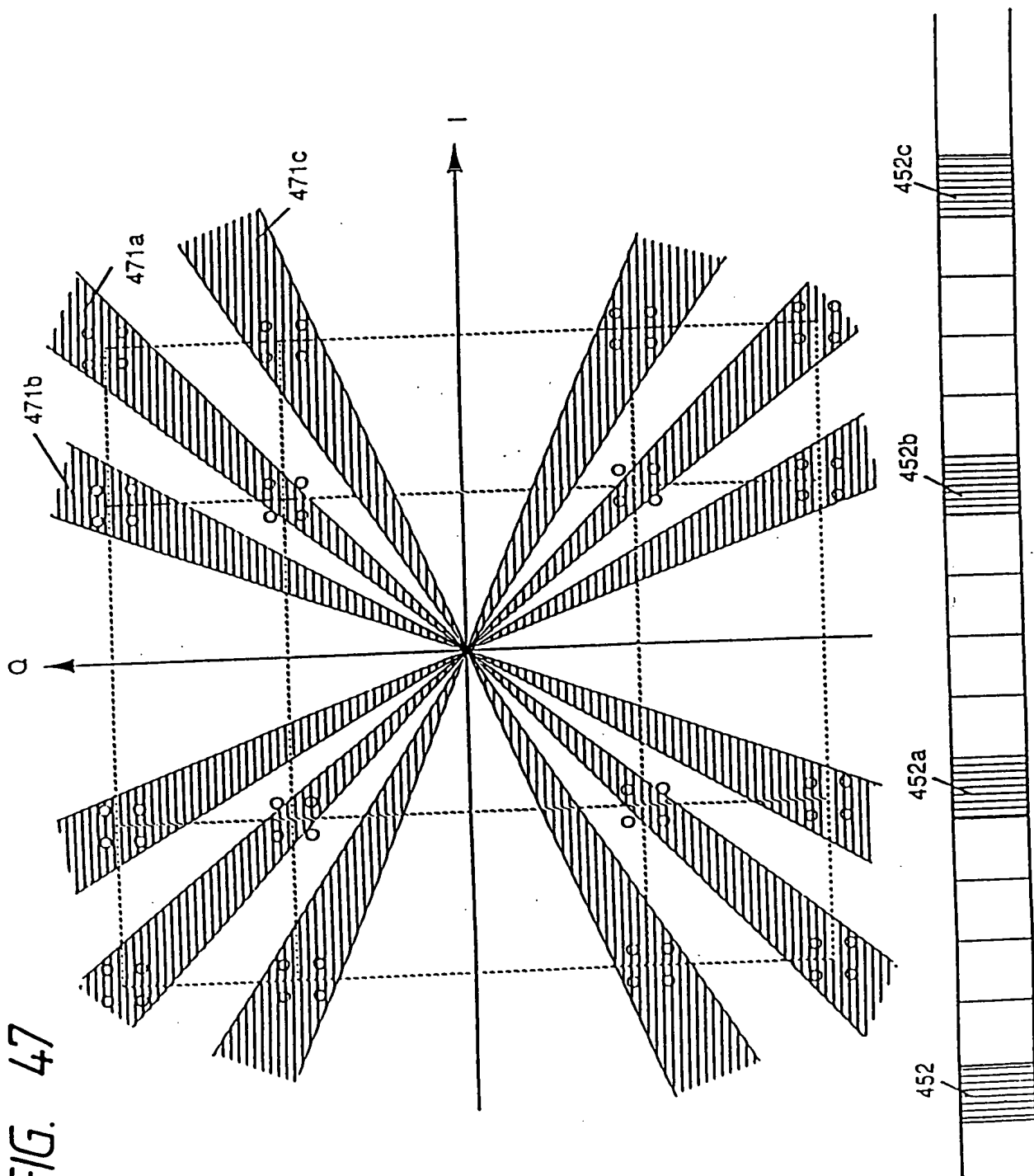


FIG. 48

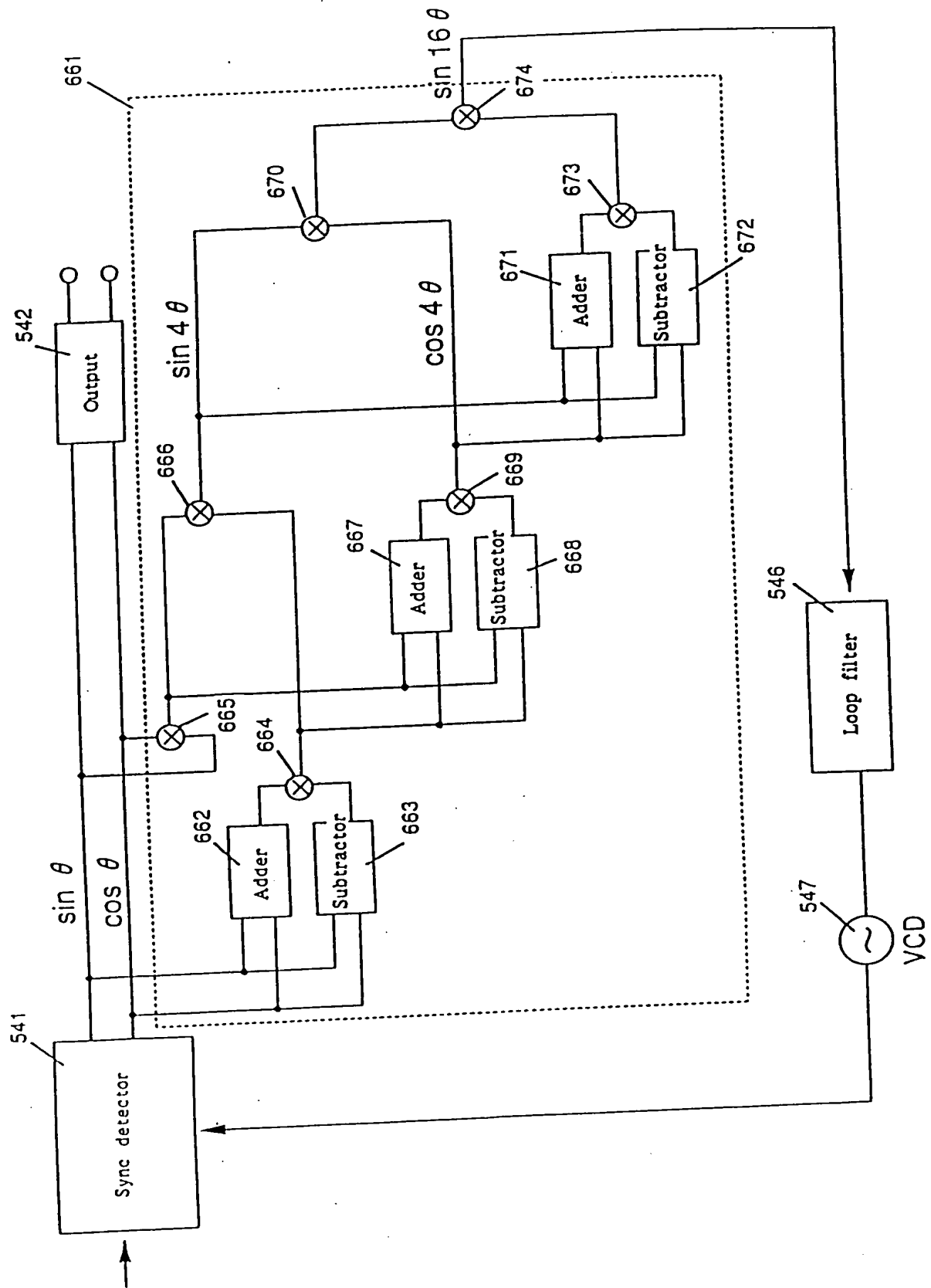
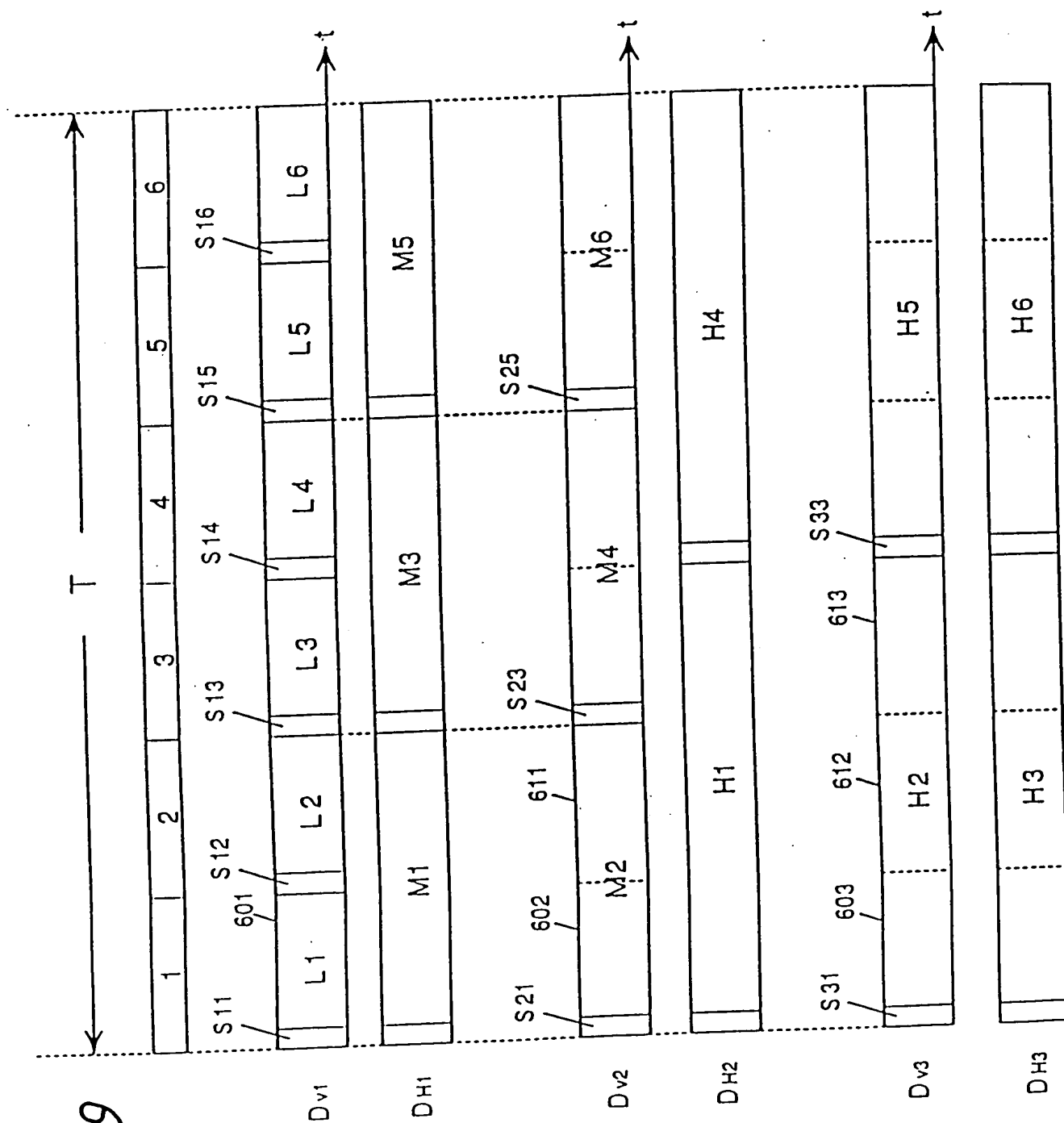
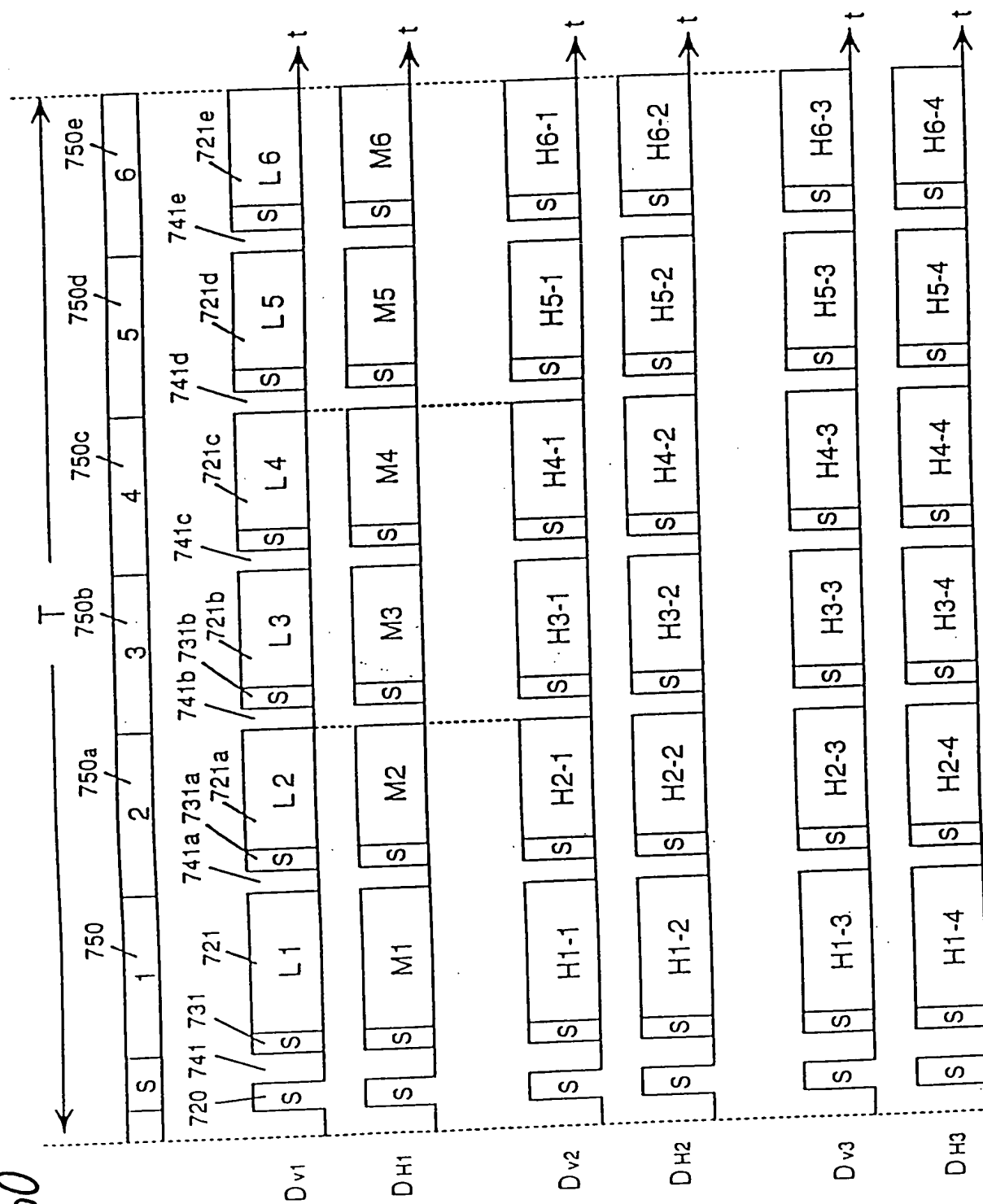


FIG. 67





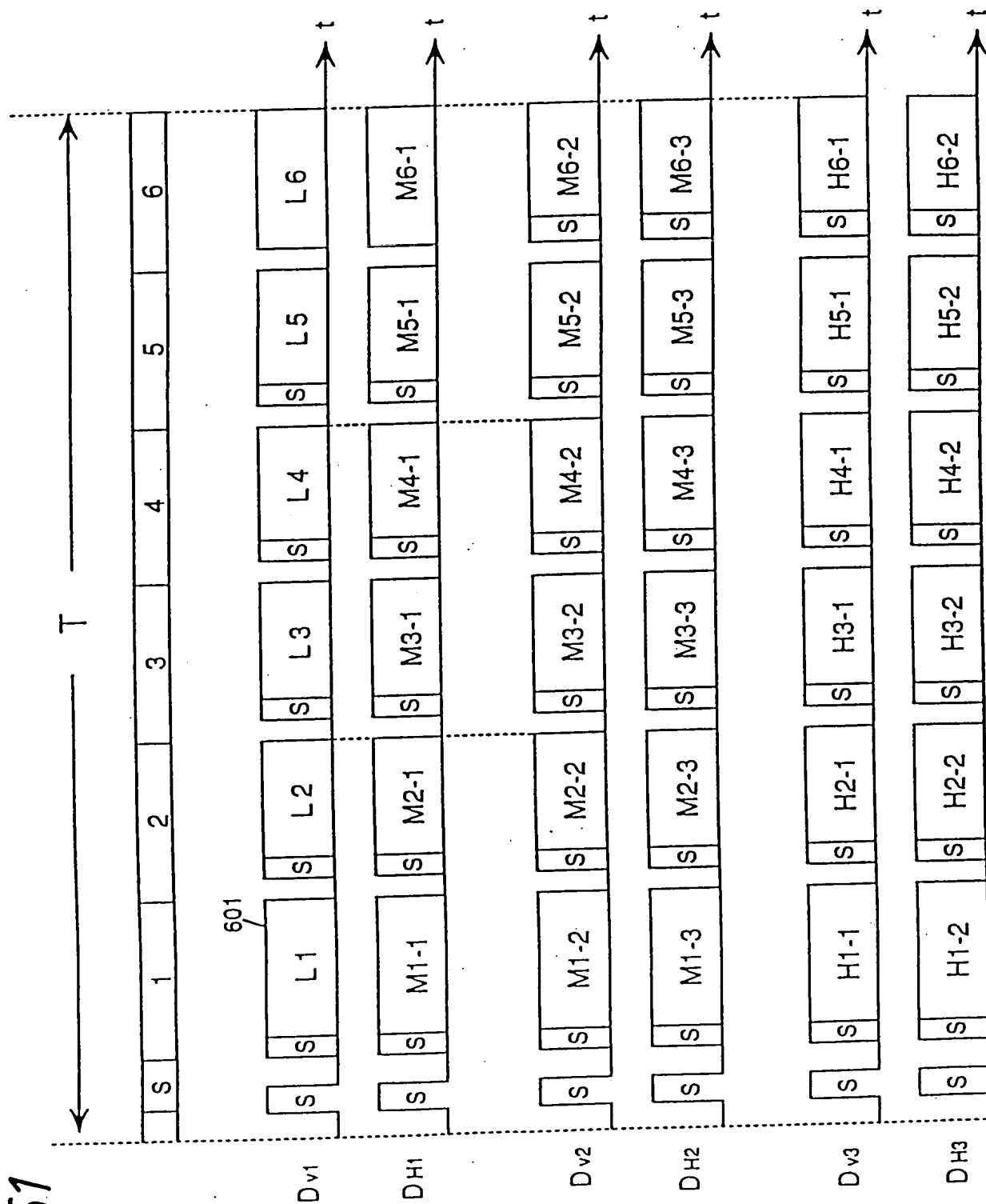


FIG. 52

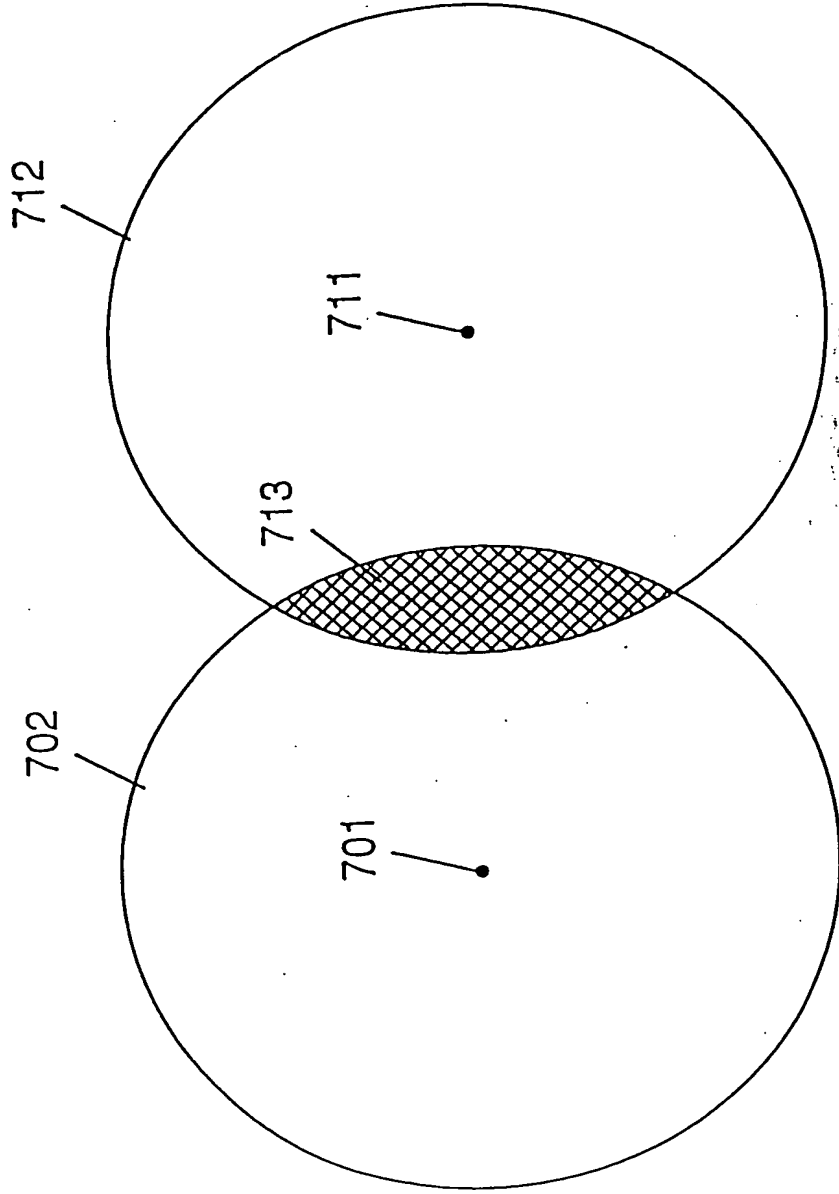


FIG. 53

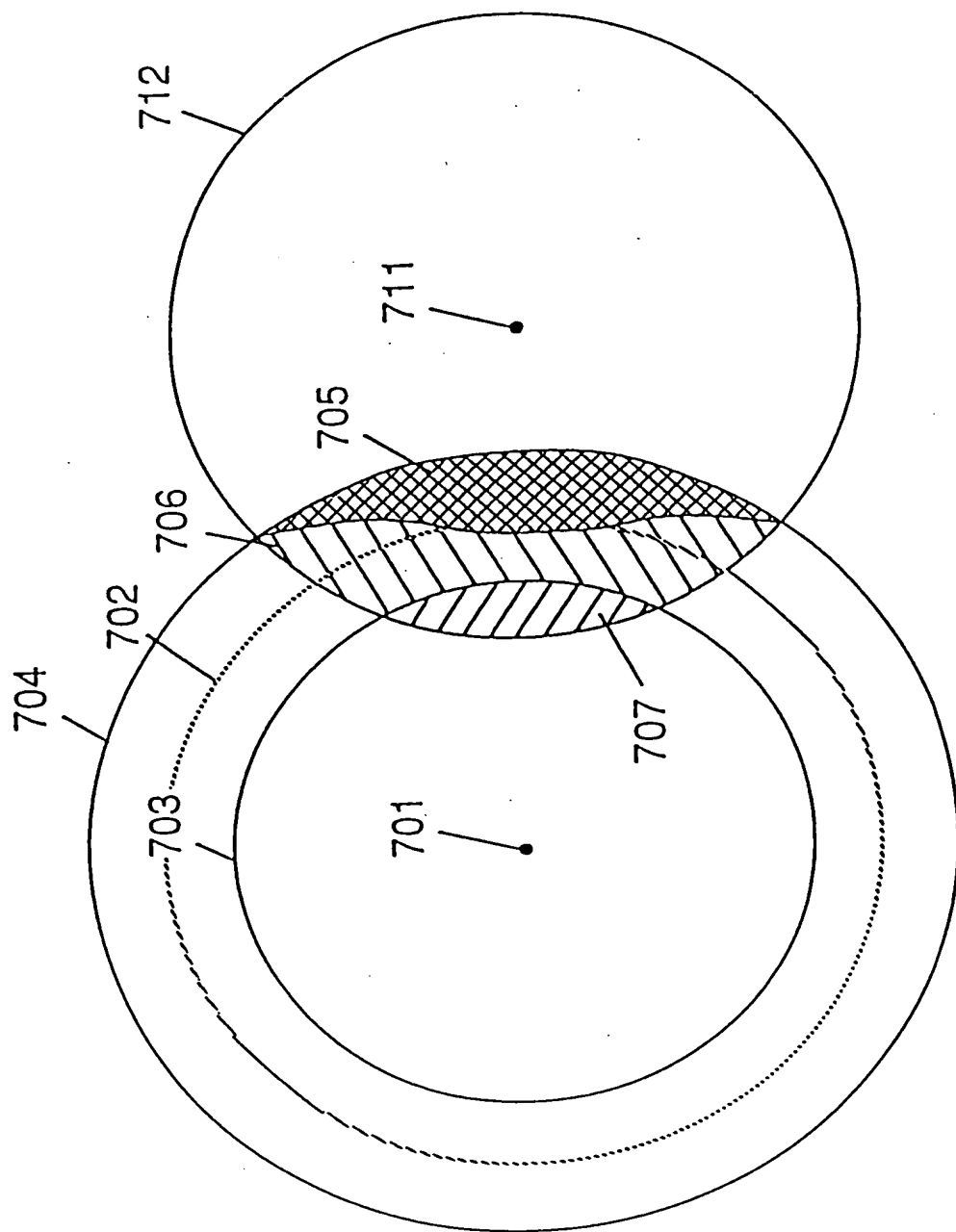


FIG. 54

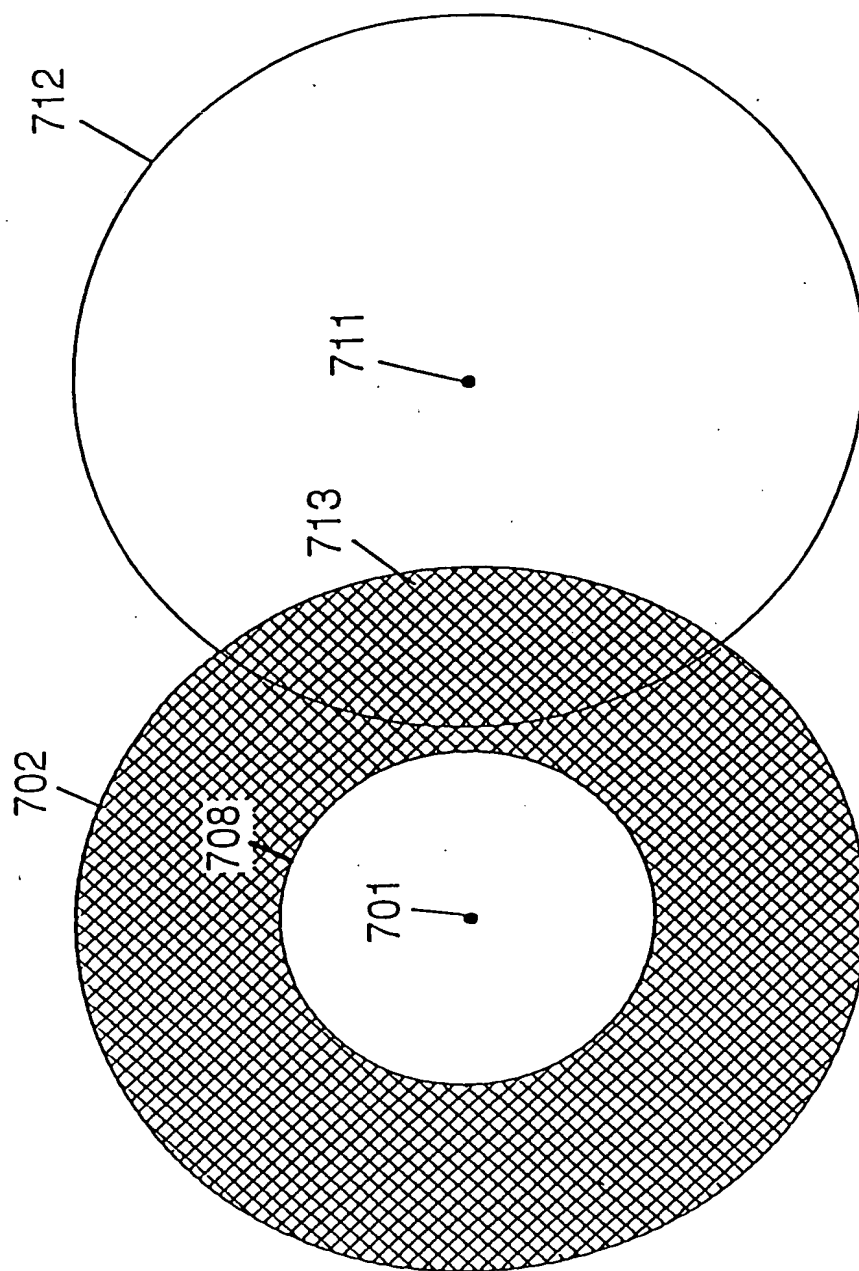


FIG. 55

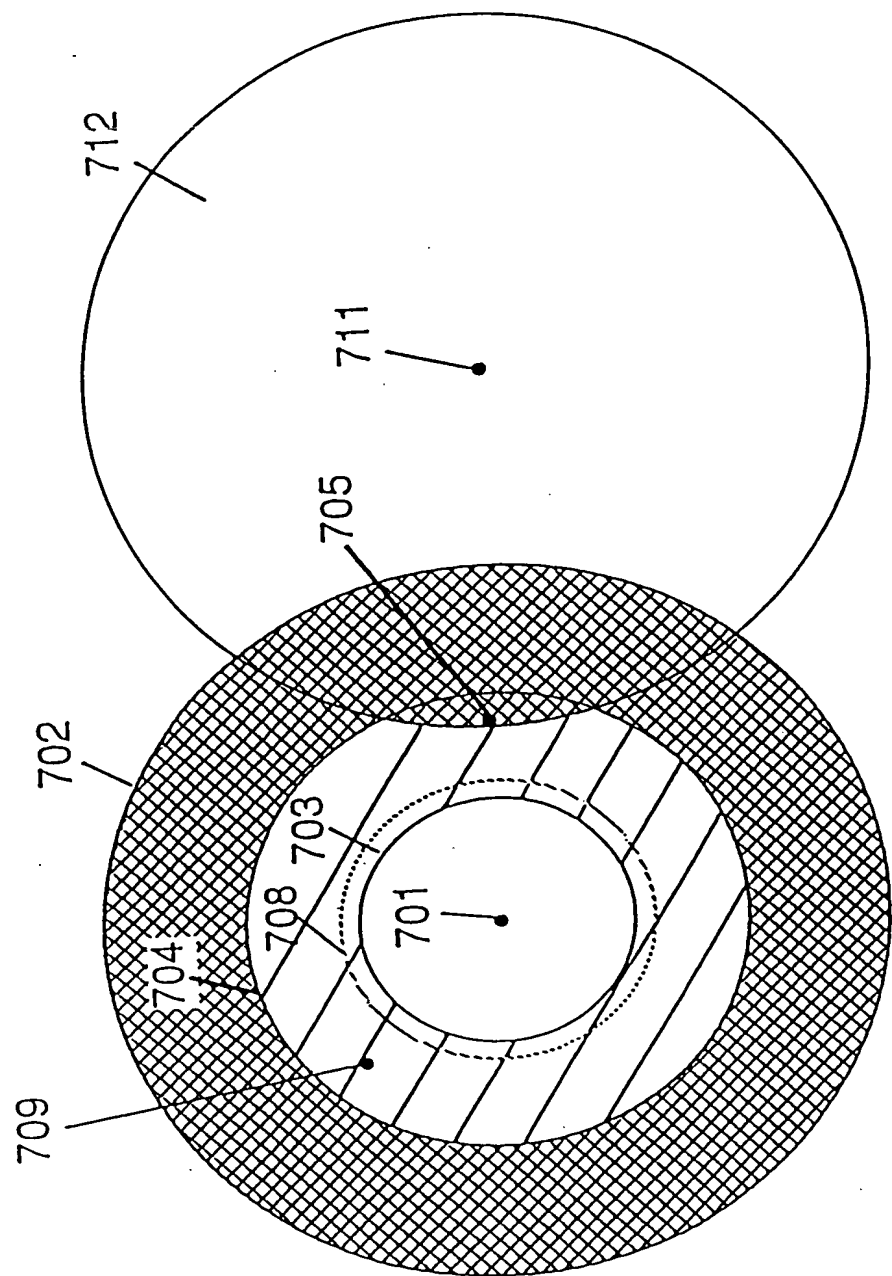


FIG. 56

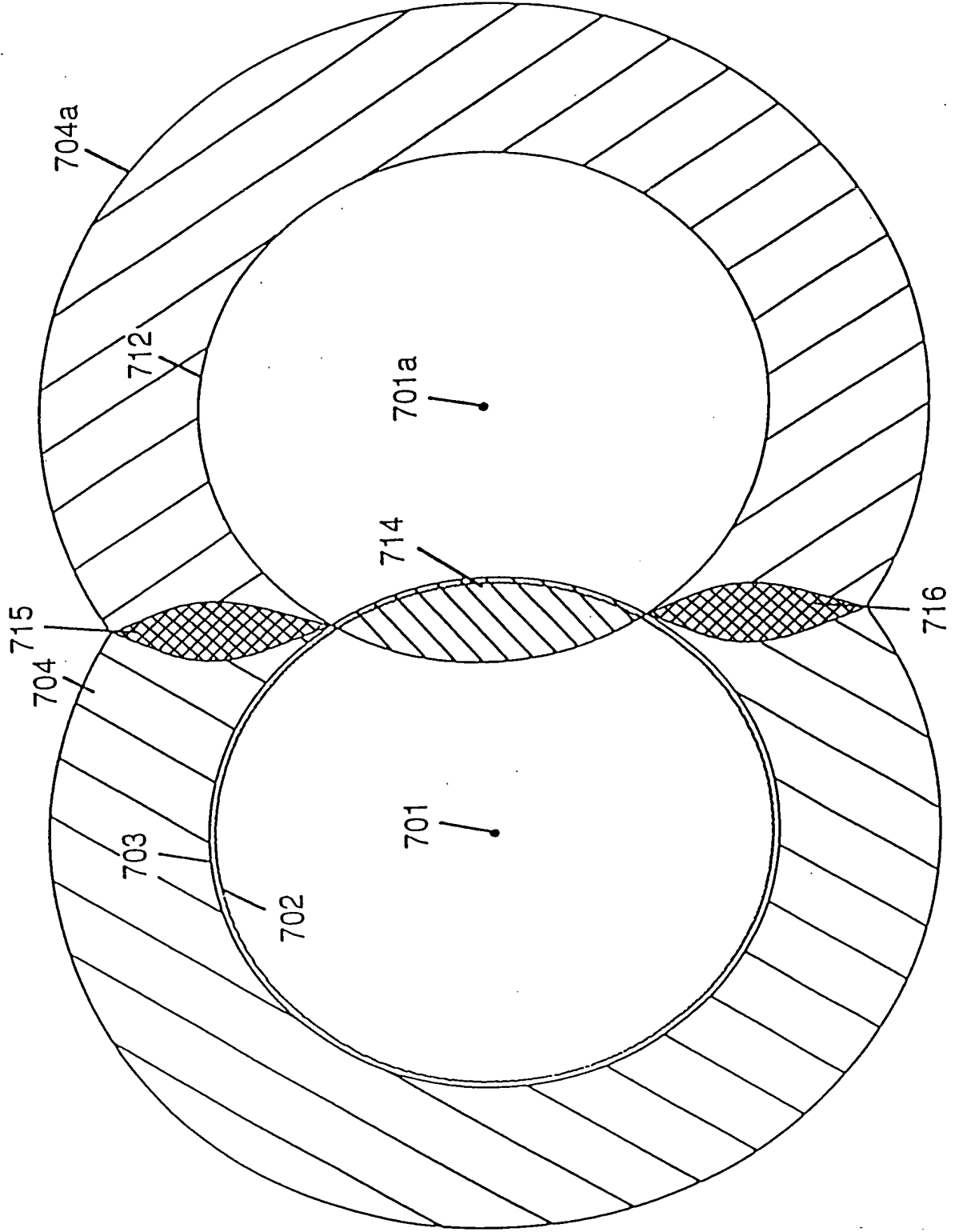


FIG. 57

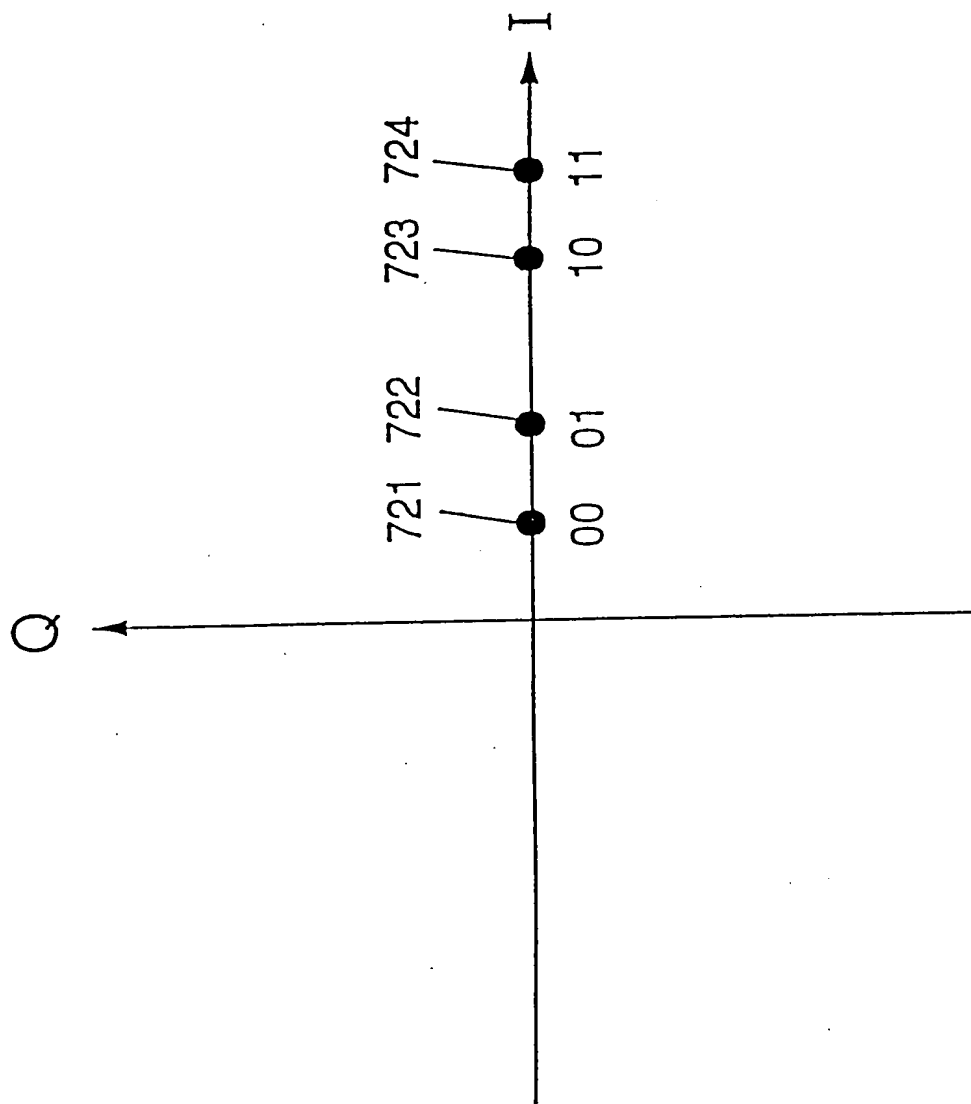


FIG. 58

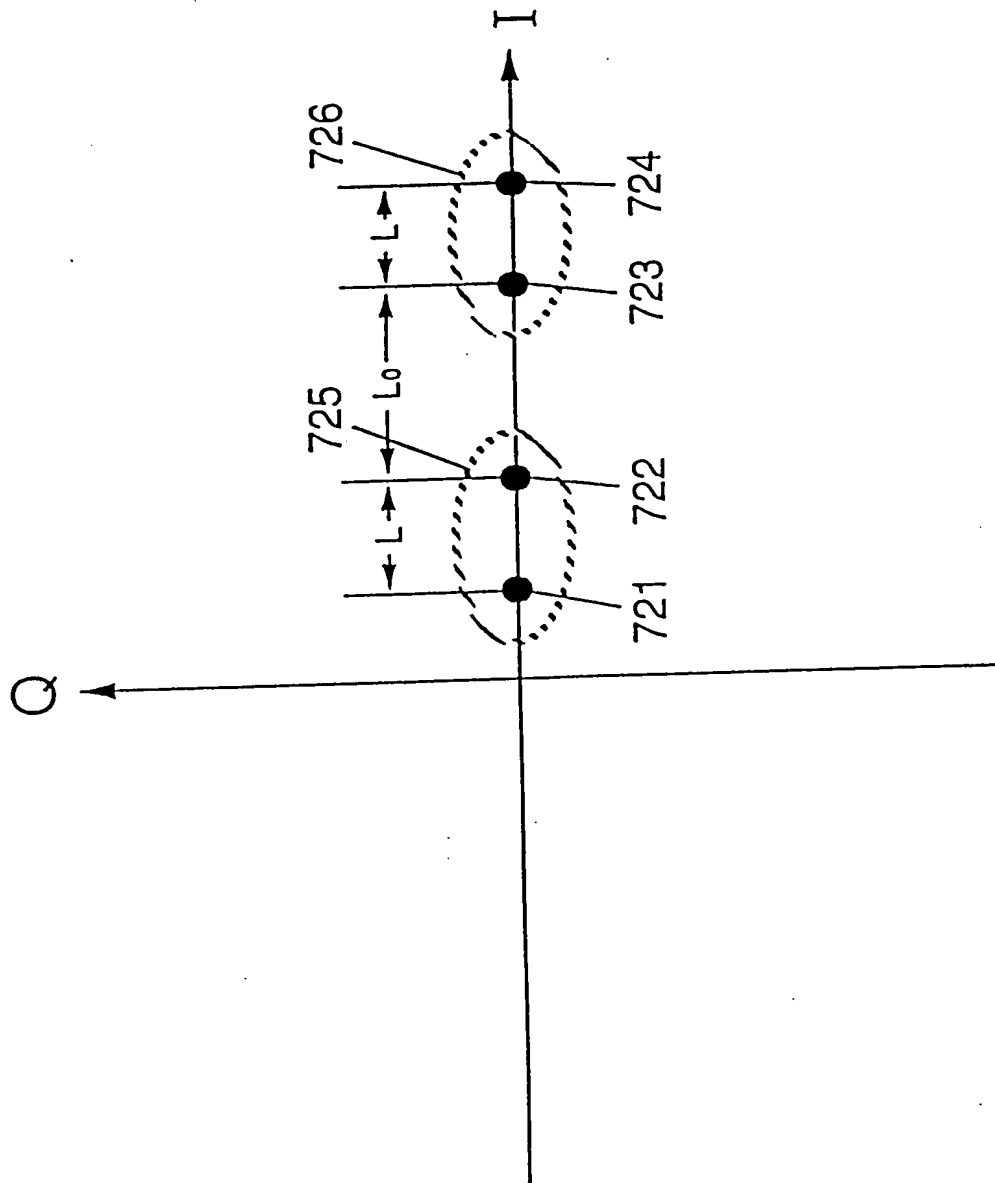


FIG. 59(a)

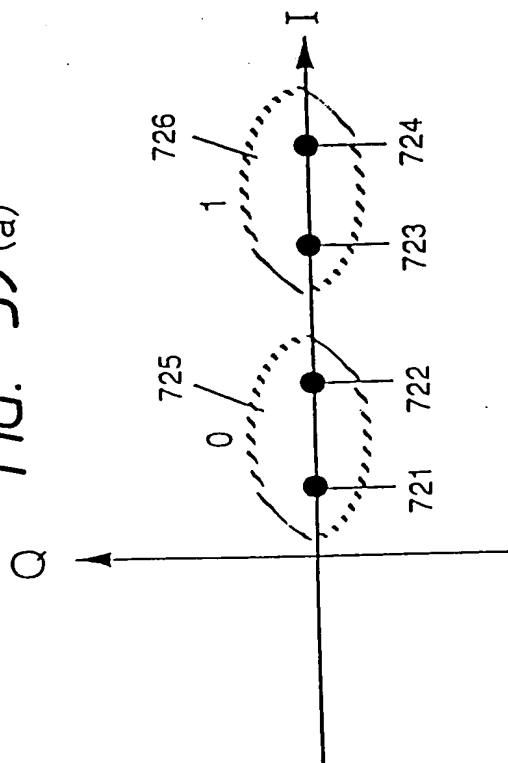


FIG. 59(b)

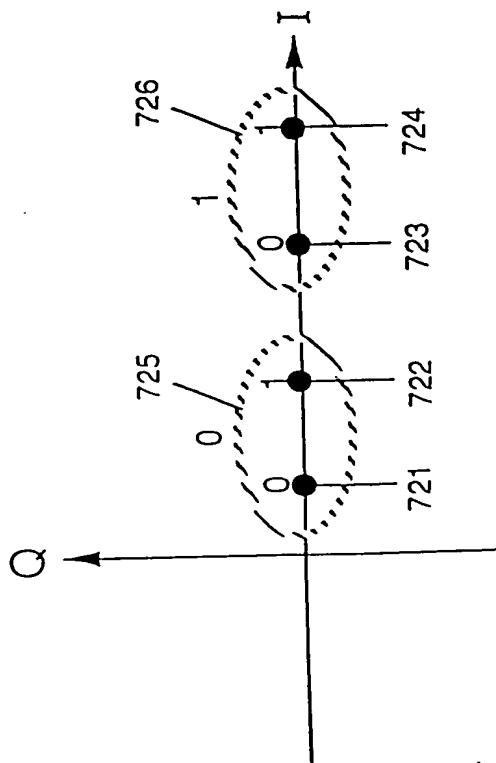


FIG. 59(c)

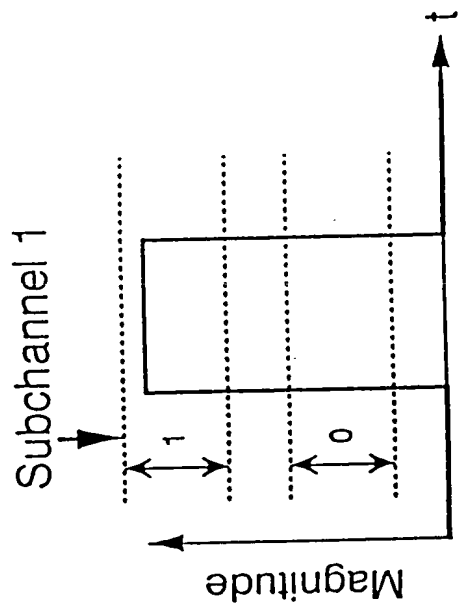


FIG. 59(d)

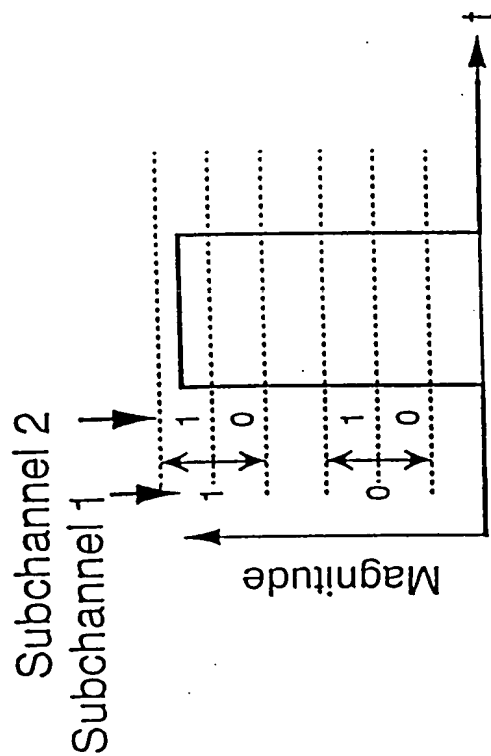


FIG. 60

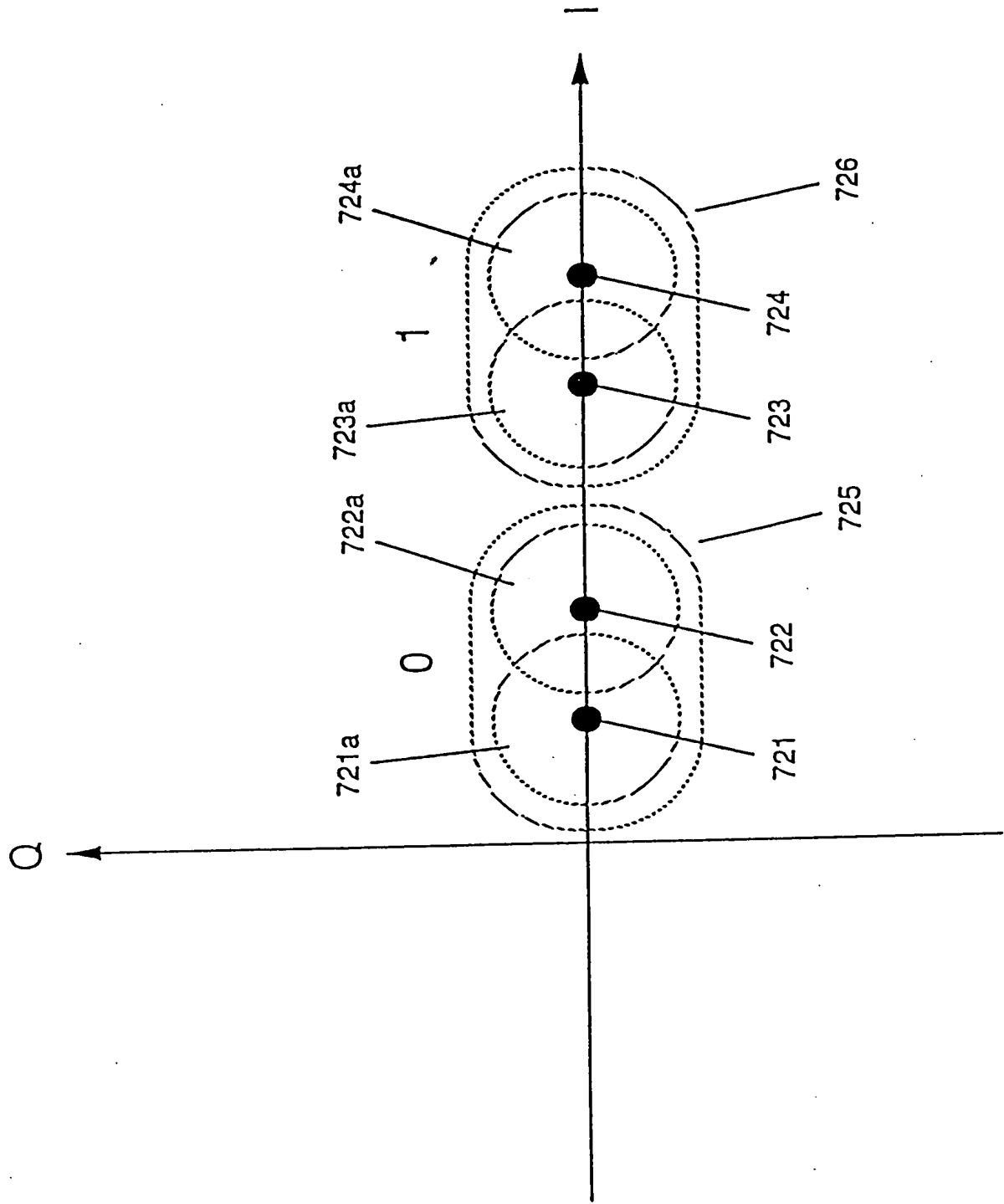


FIG. 61

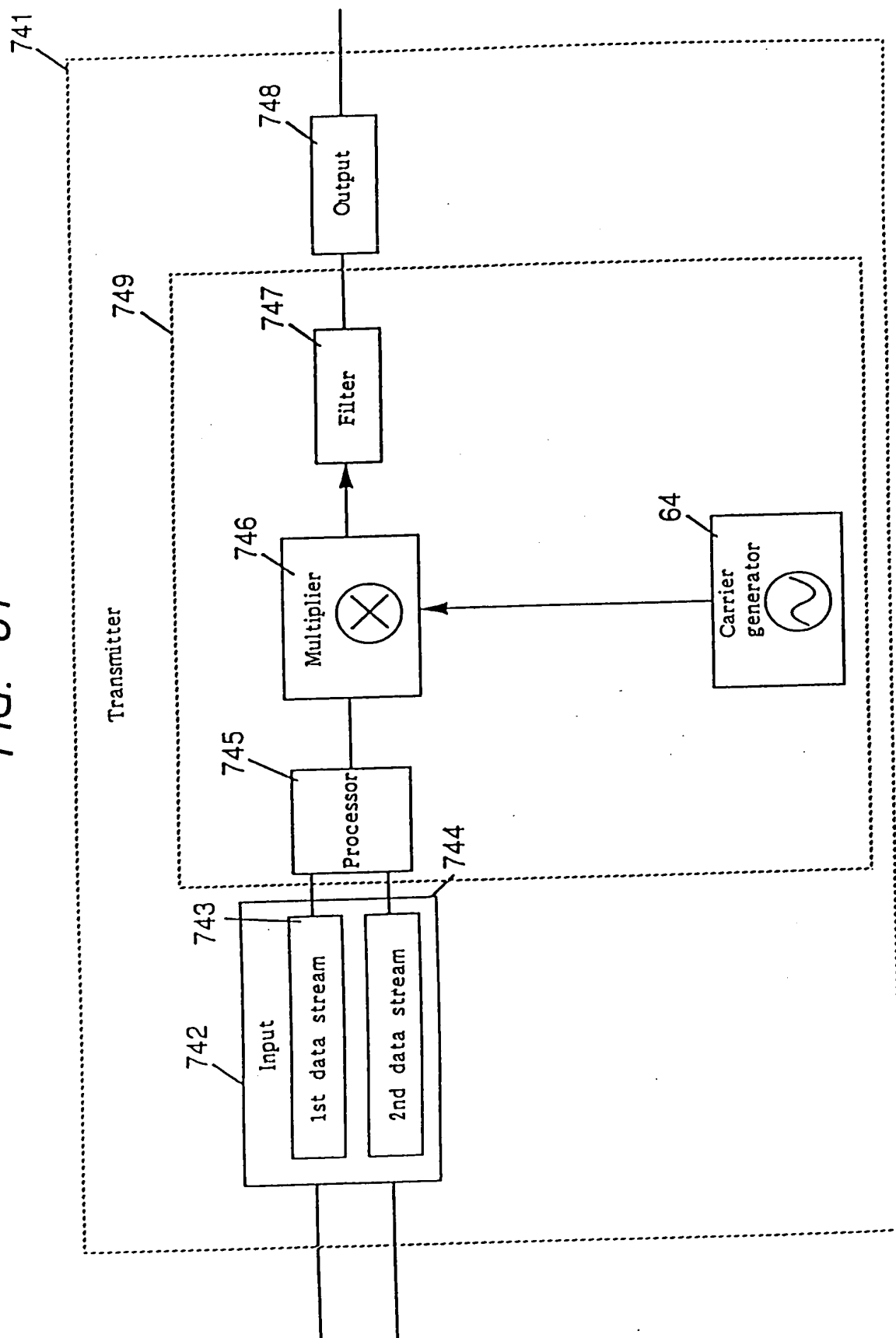


FIG. 62

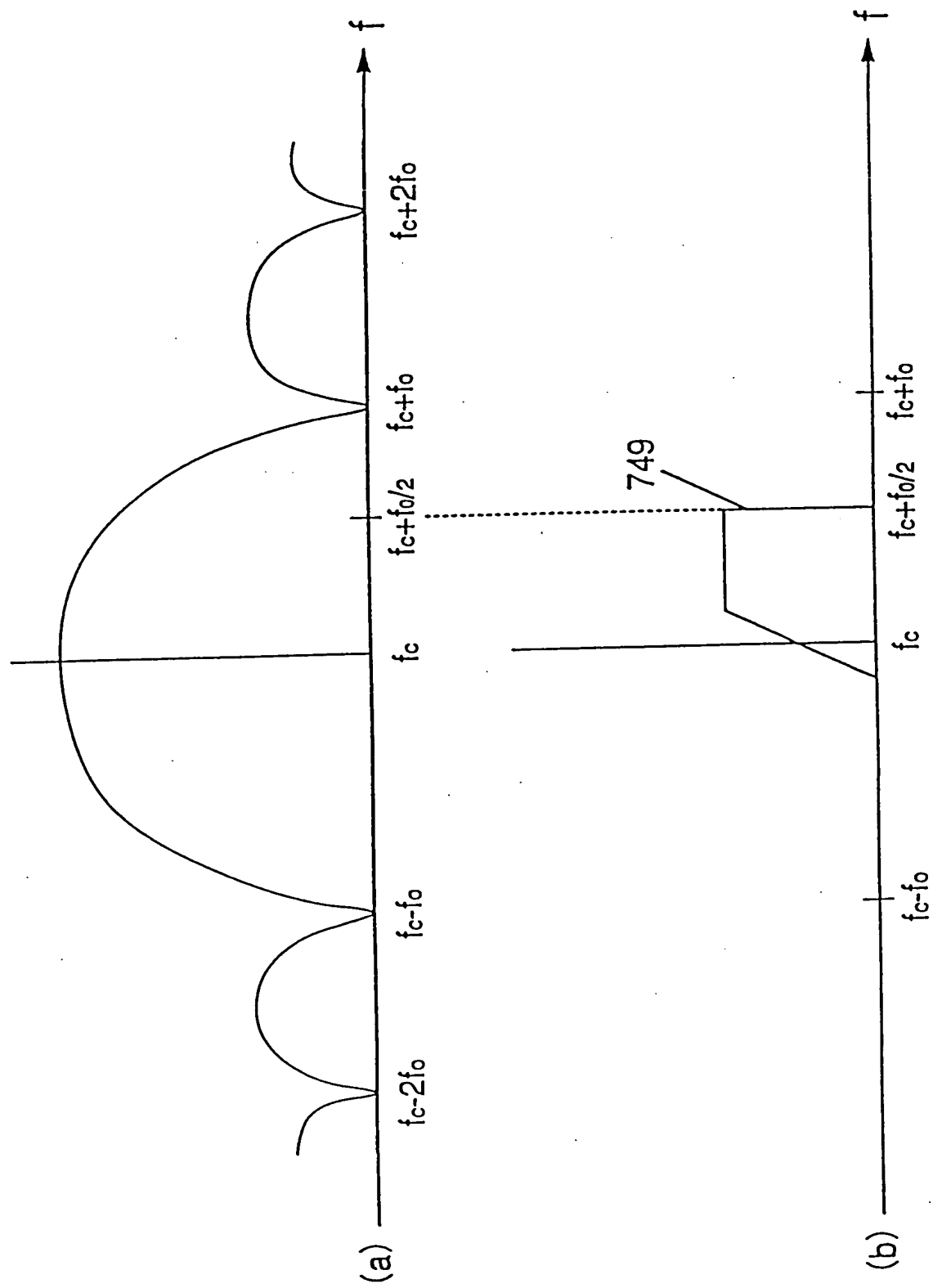


FIG. 64

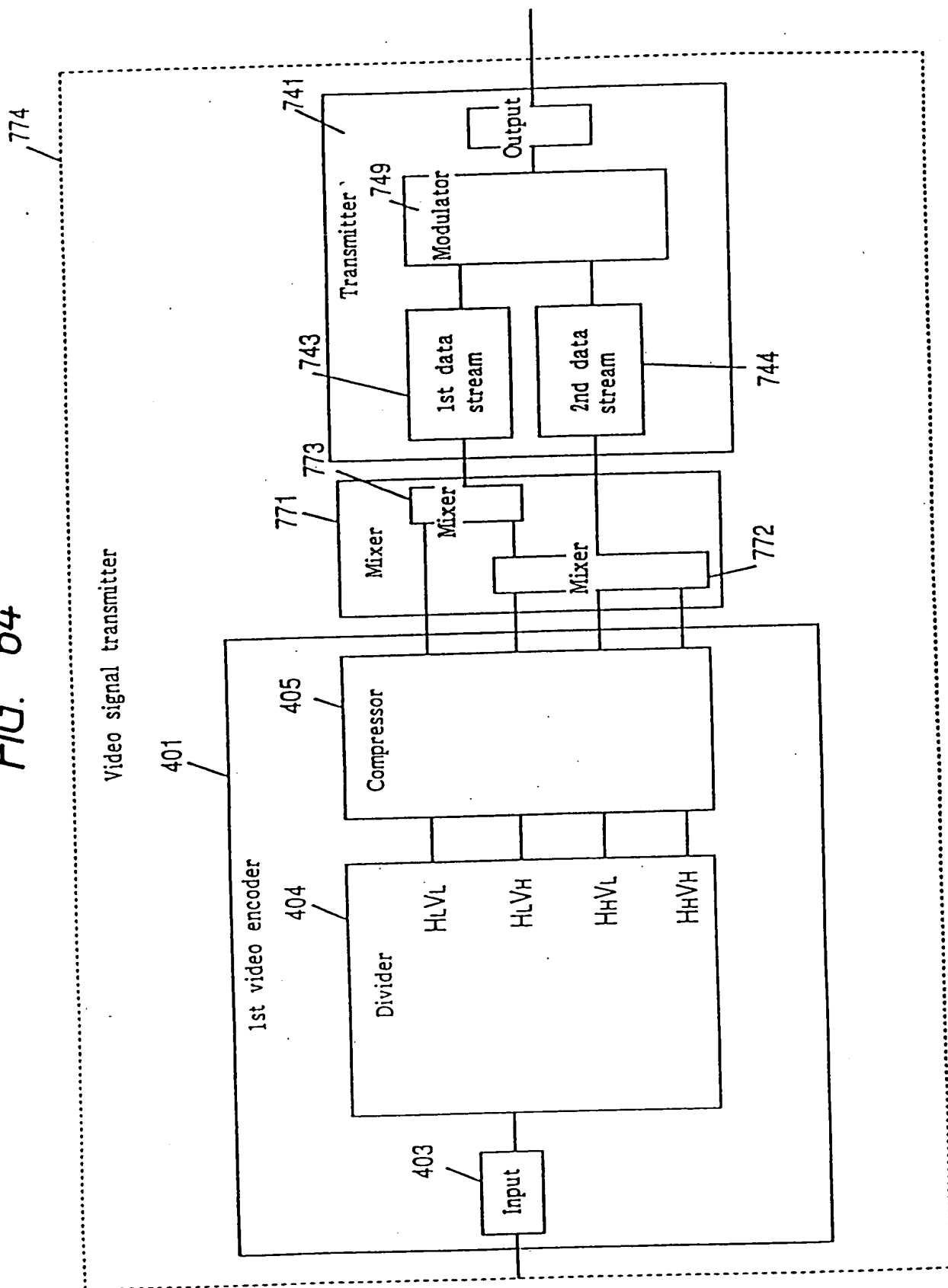


FIG. 66

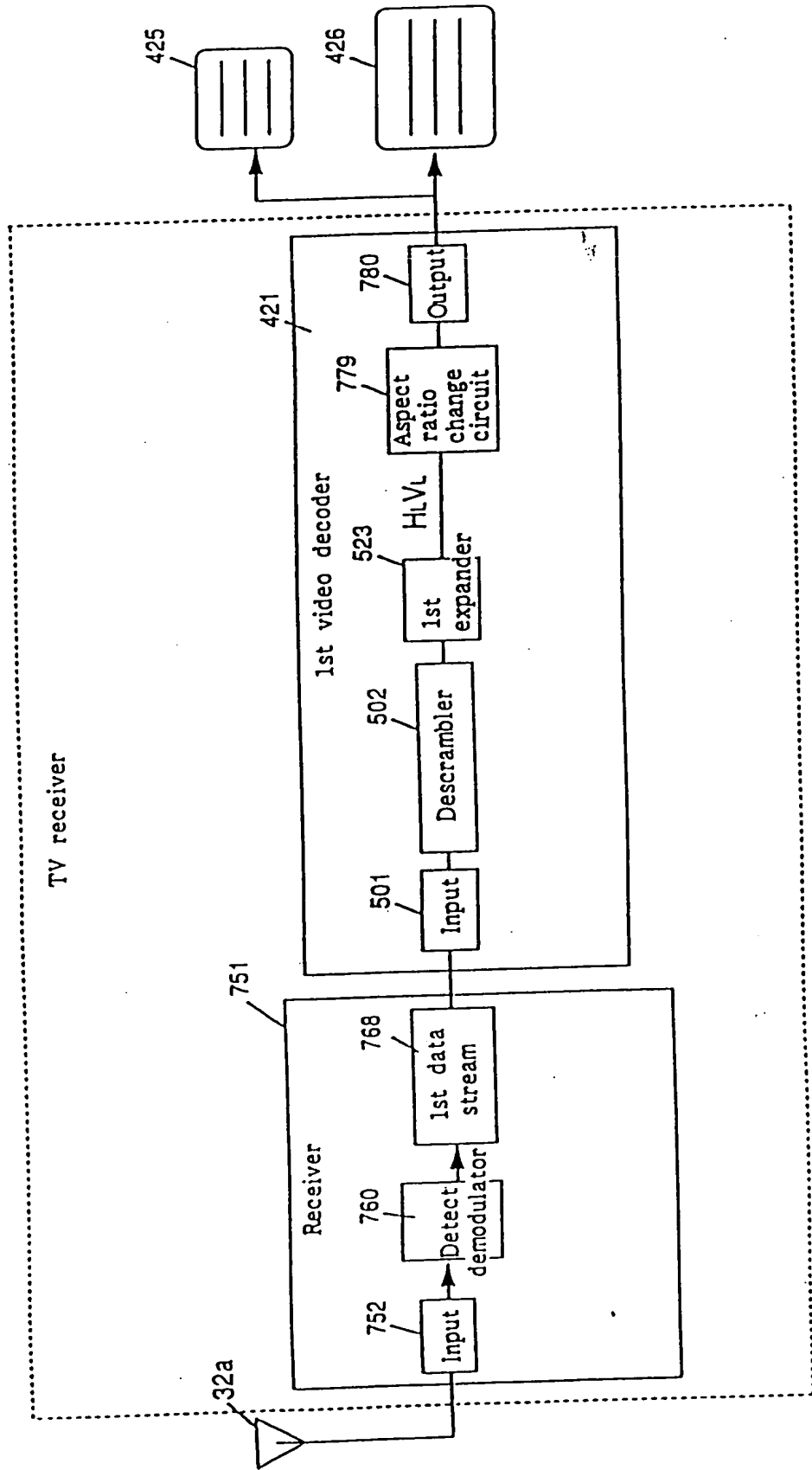


FIG. 67

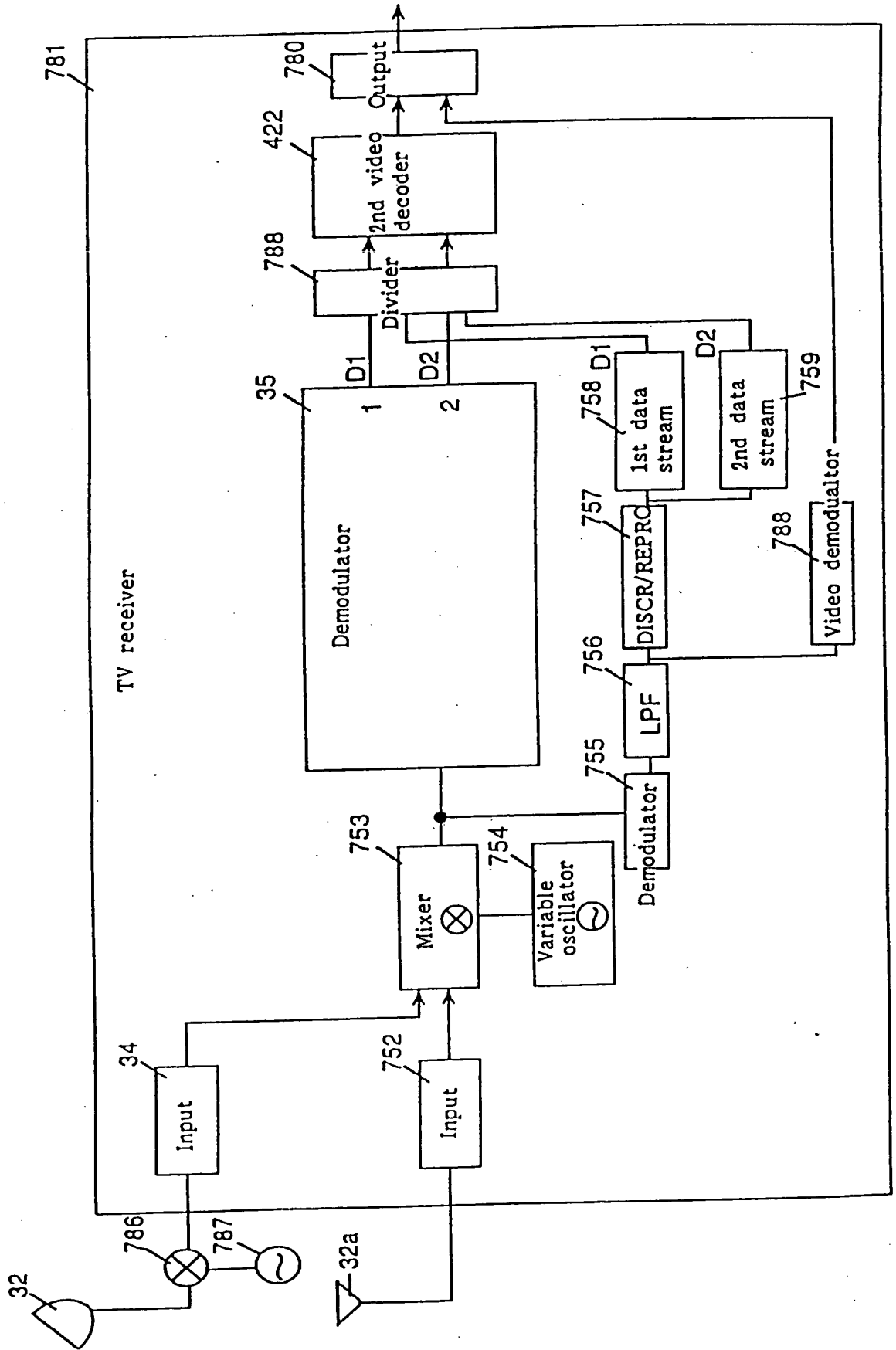


FIG. 68

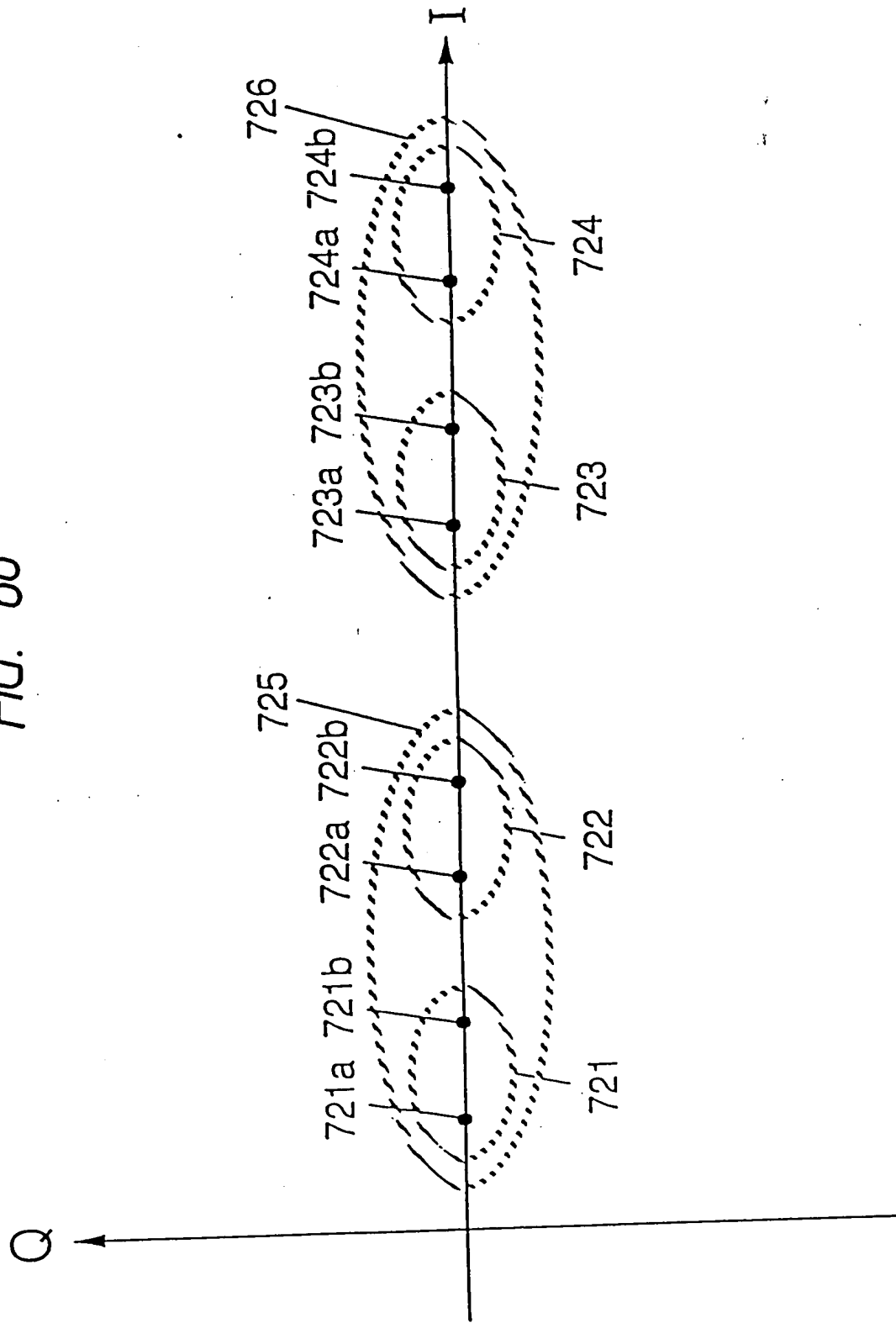


FIG. 69

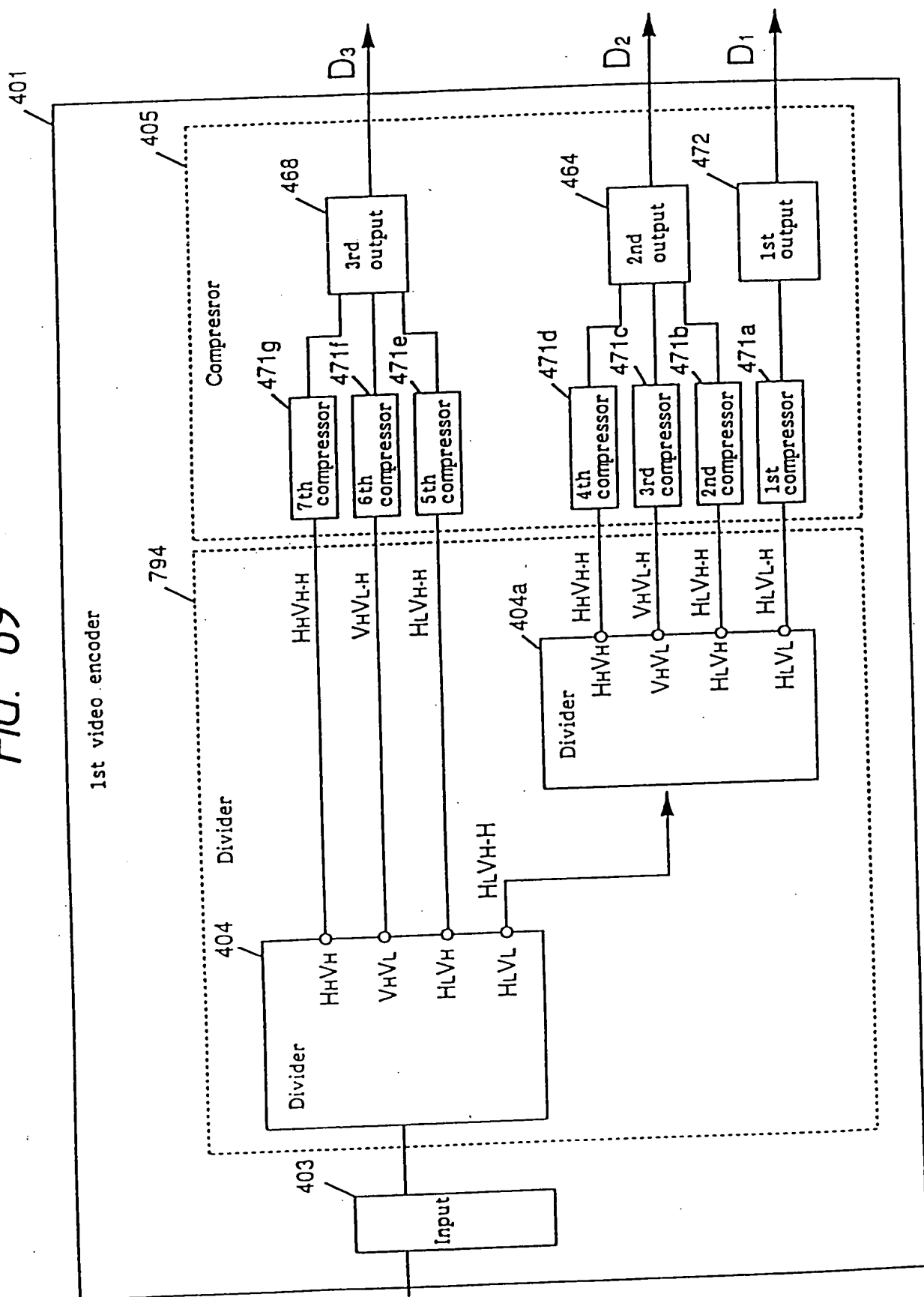


FIG. 70

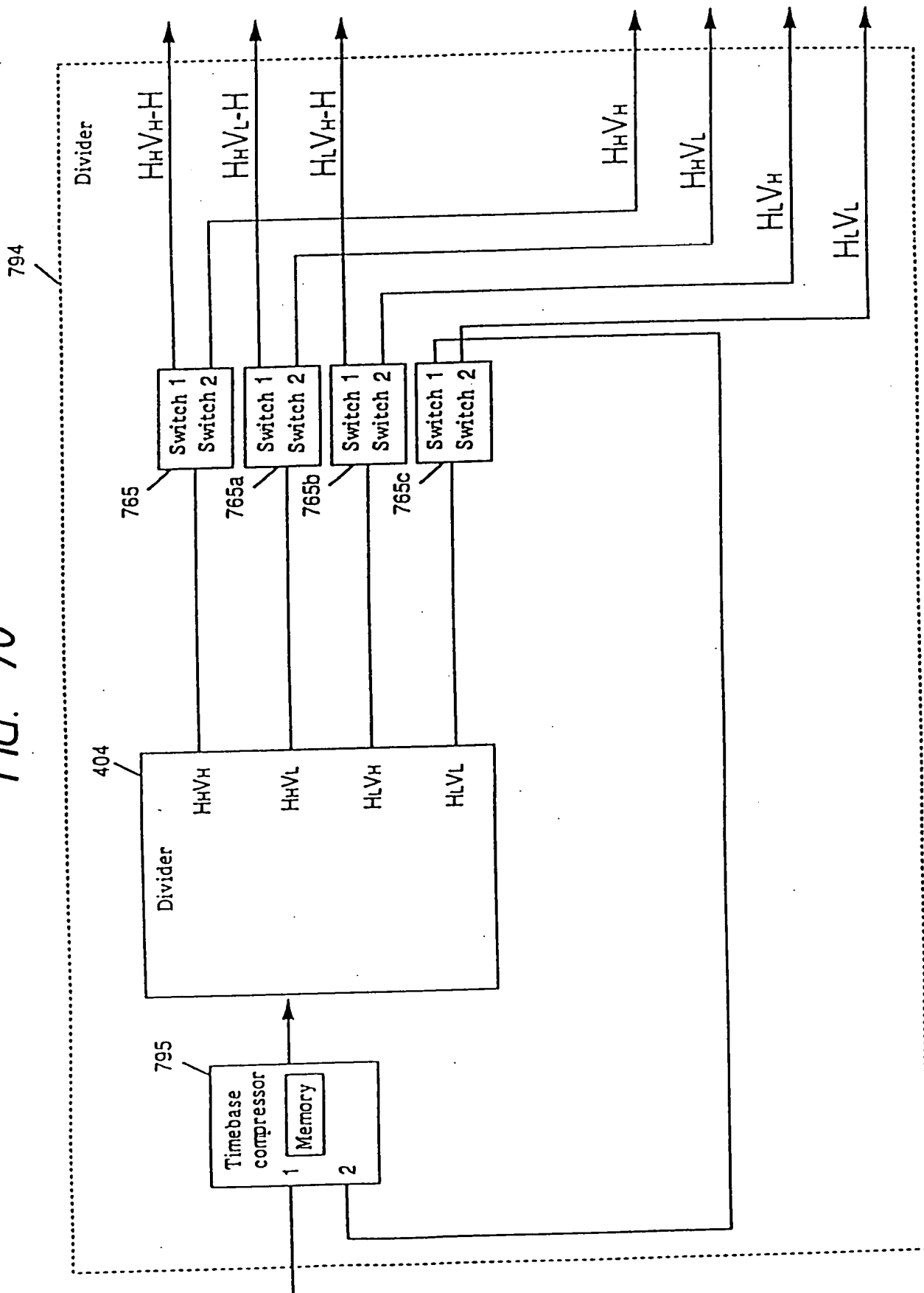


FIG. 71

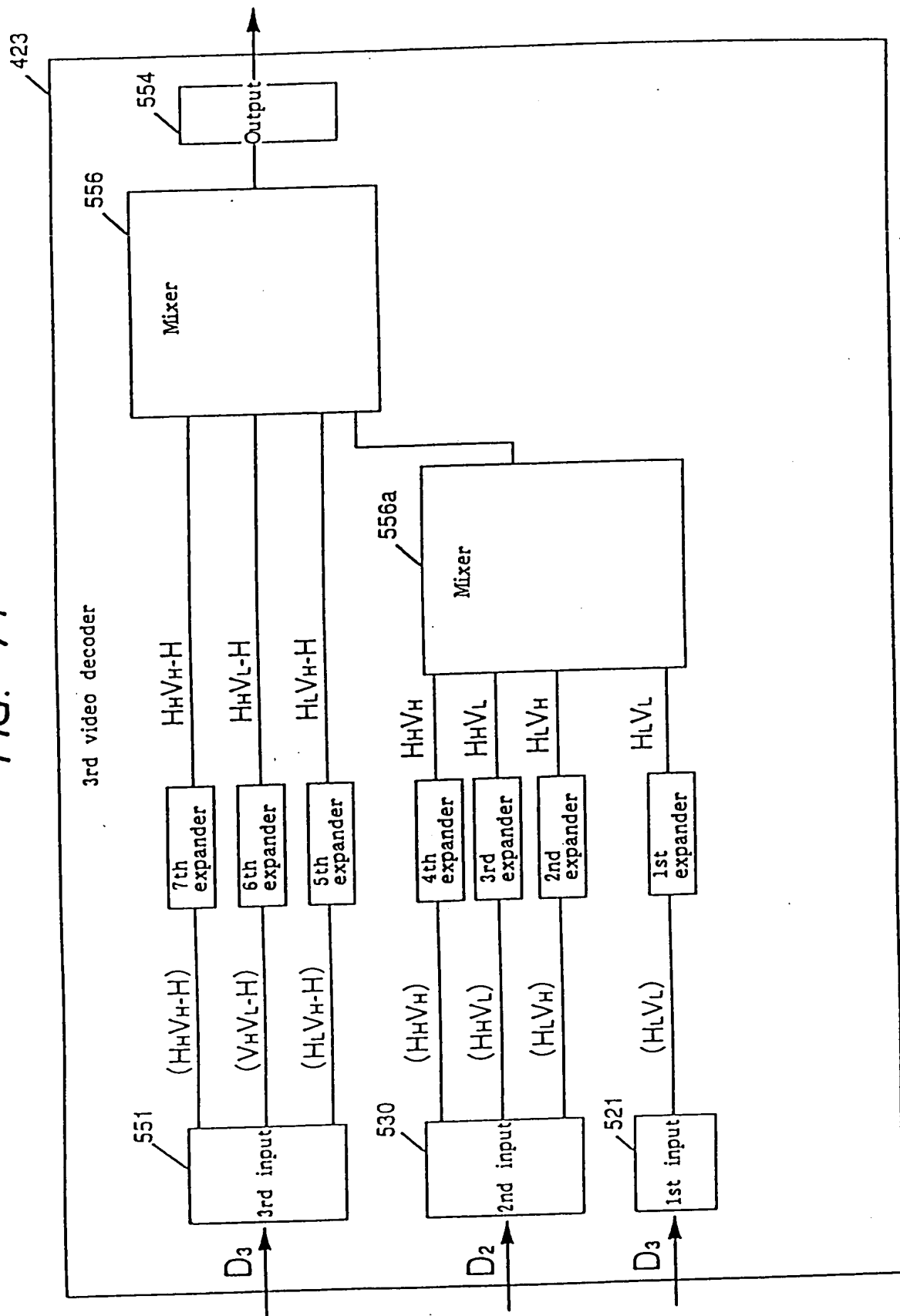


FIG. 72

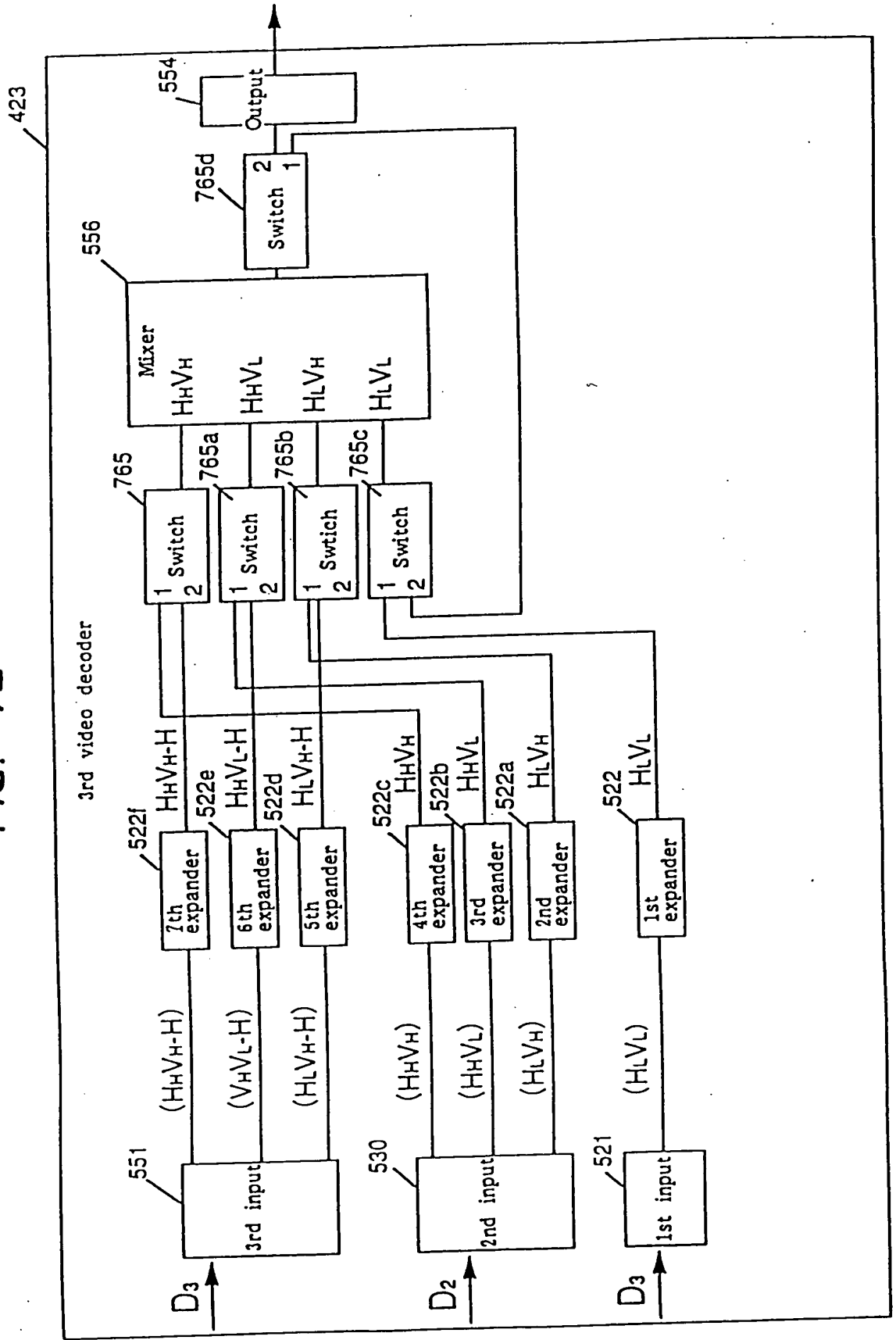


FIG. 73

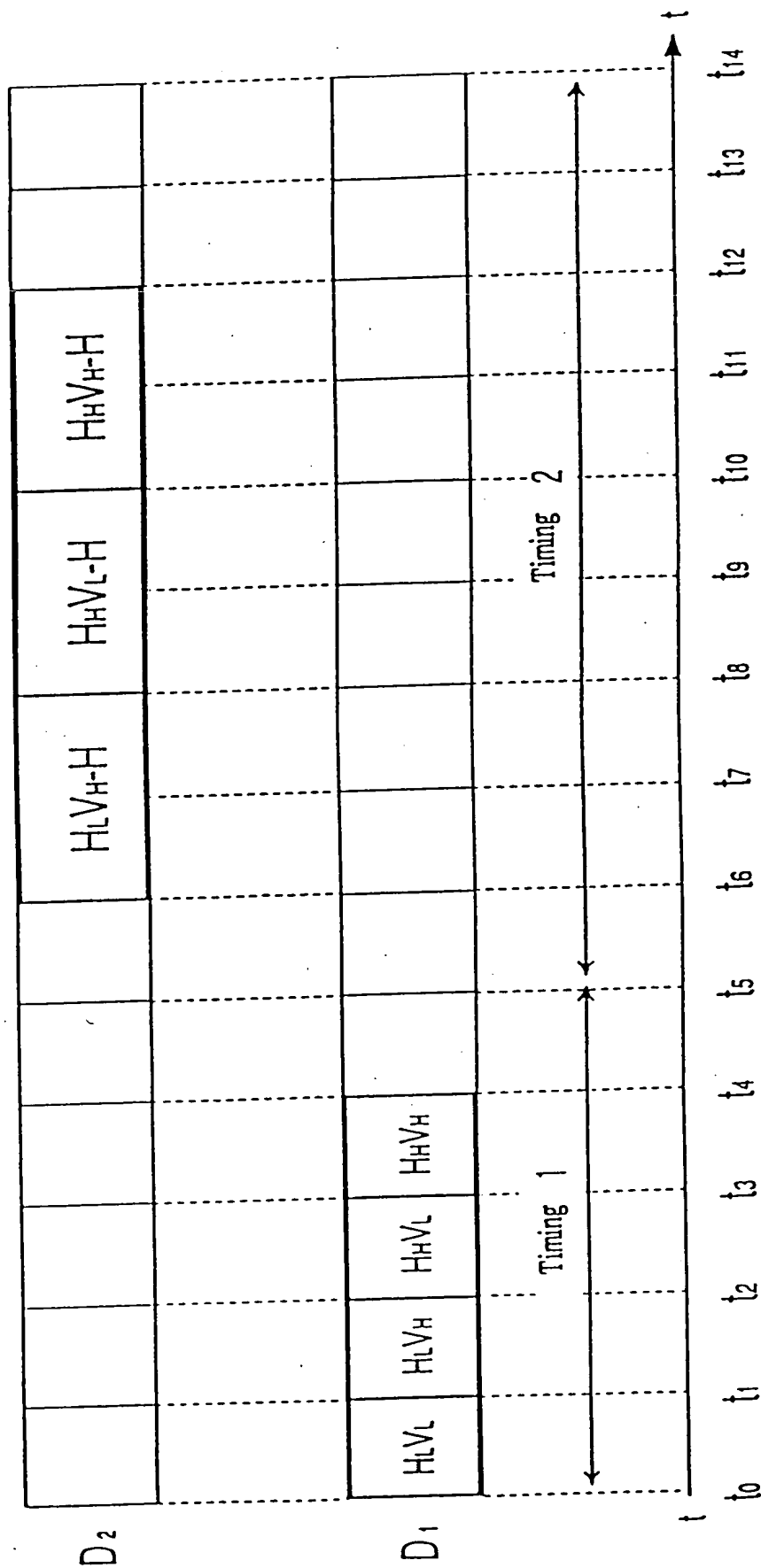


FIG. 74

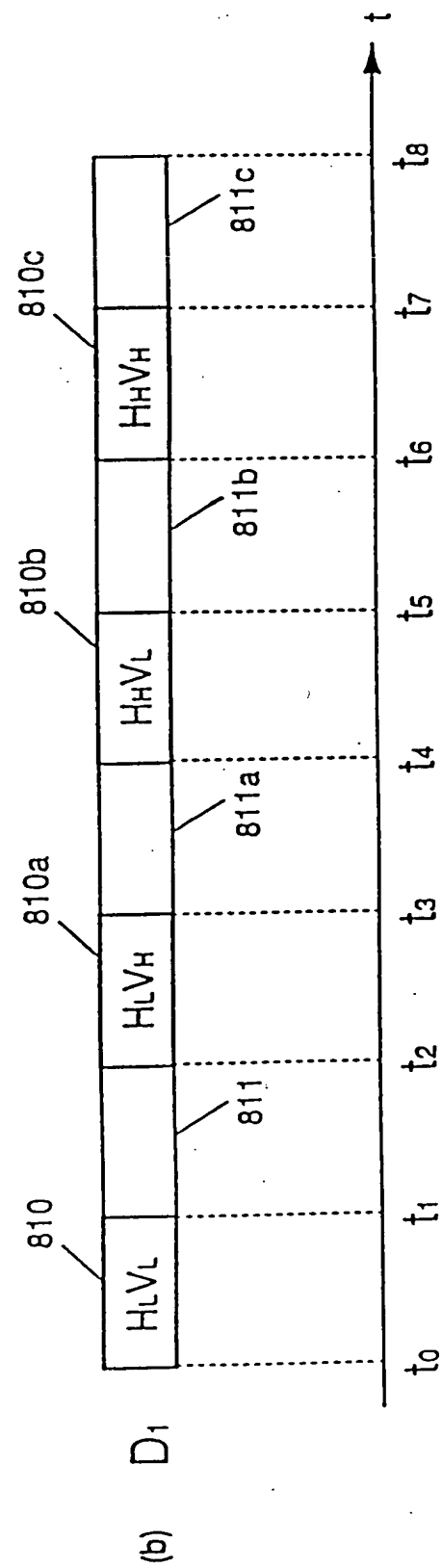
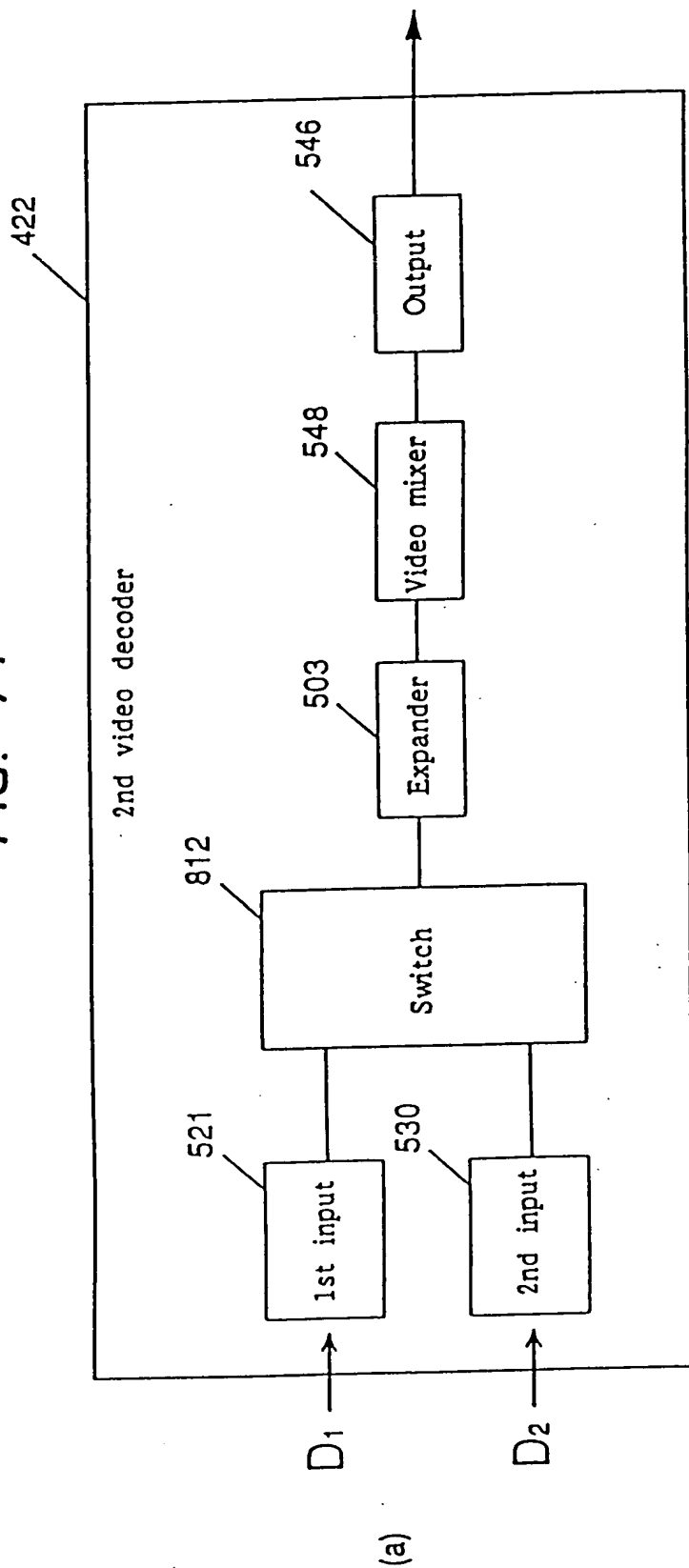


FIG. 75

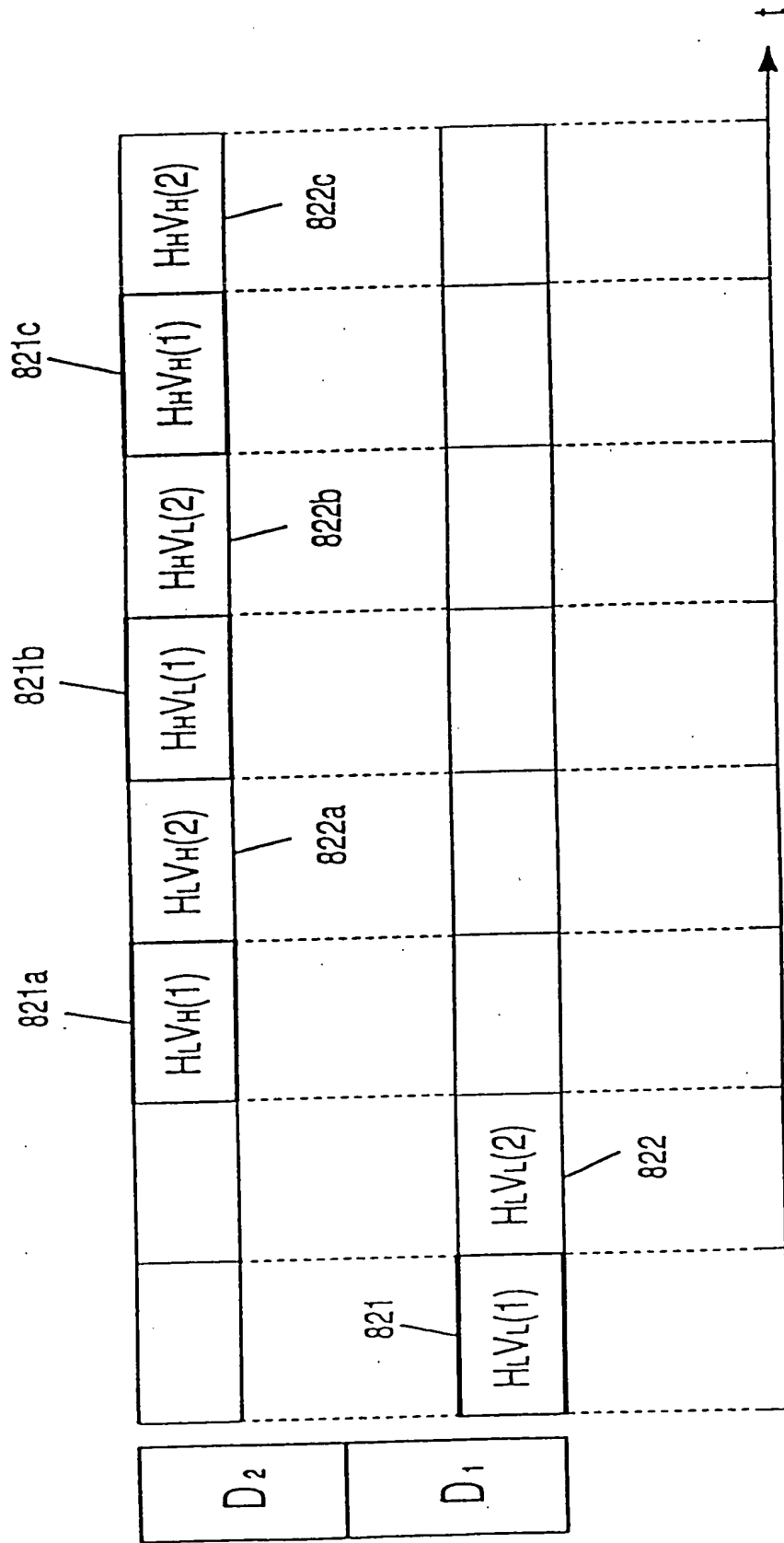


FIG. 77

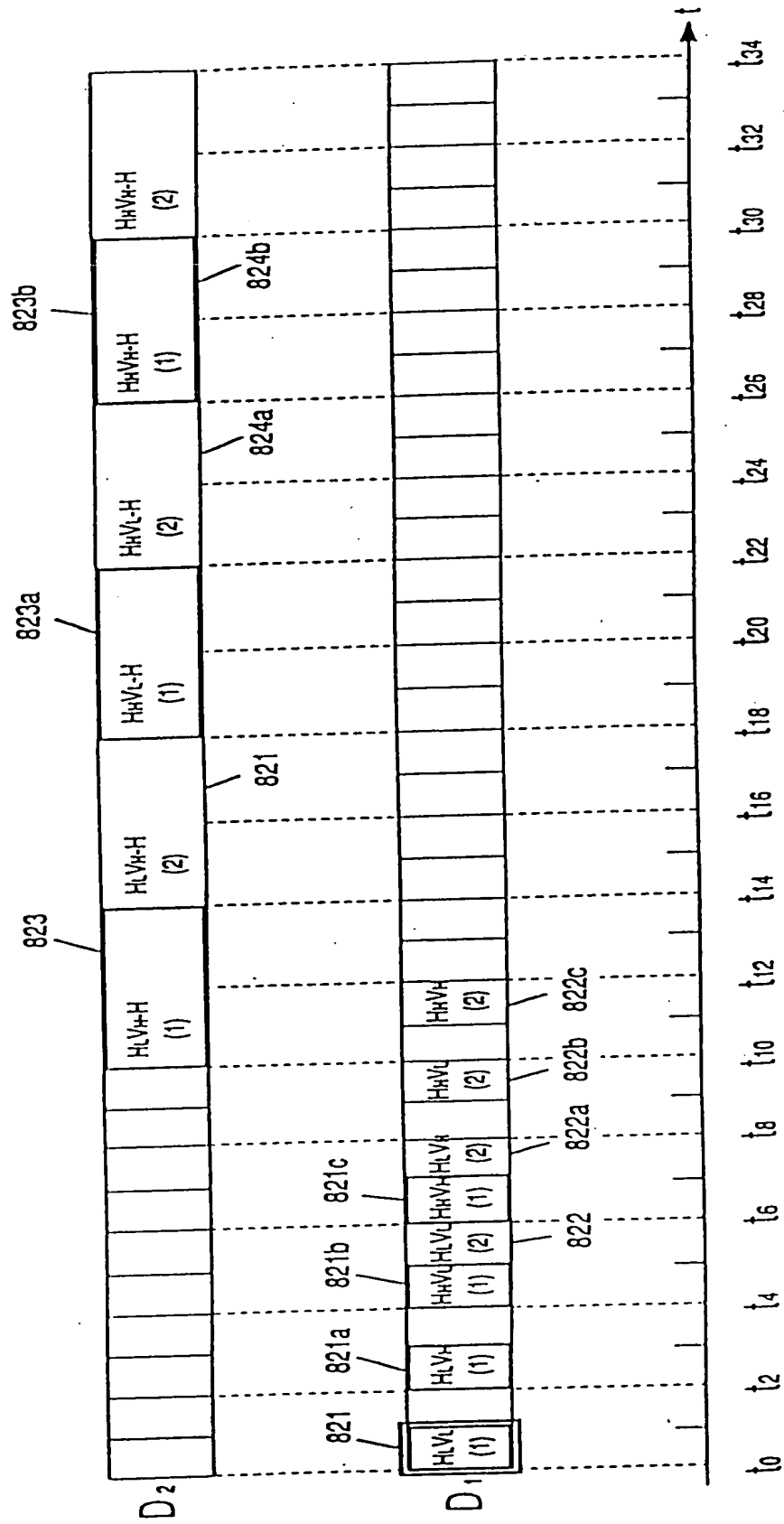


FIG. 78

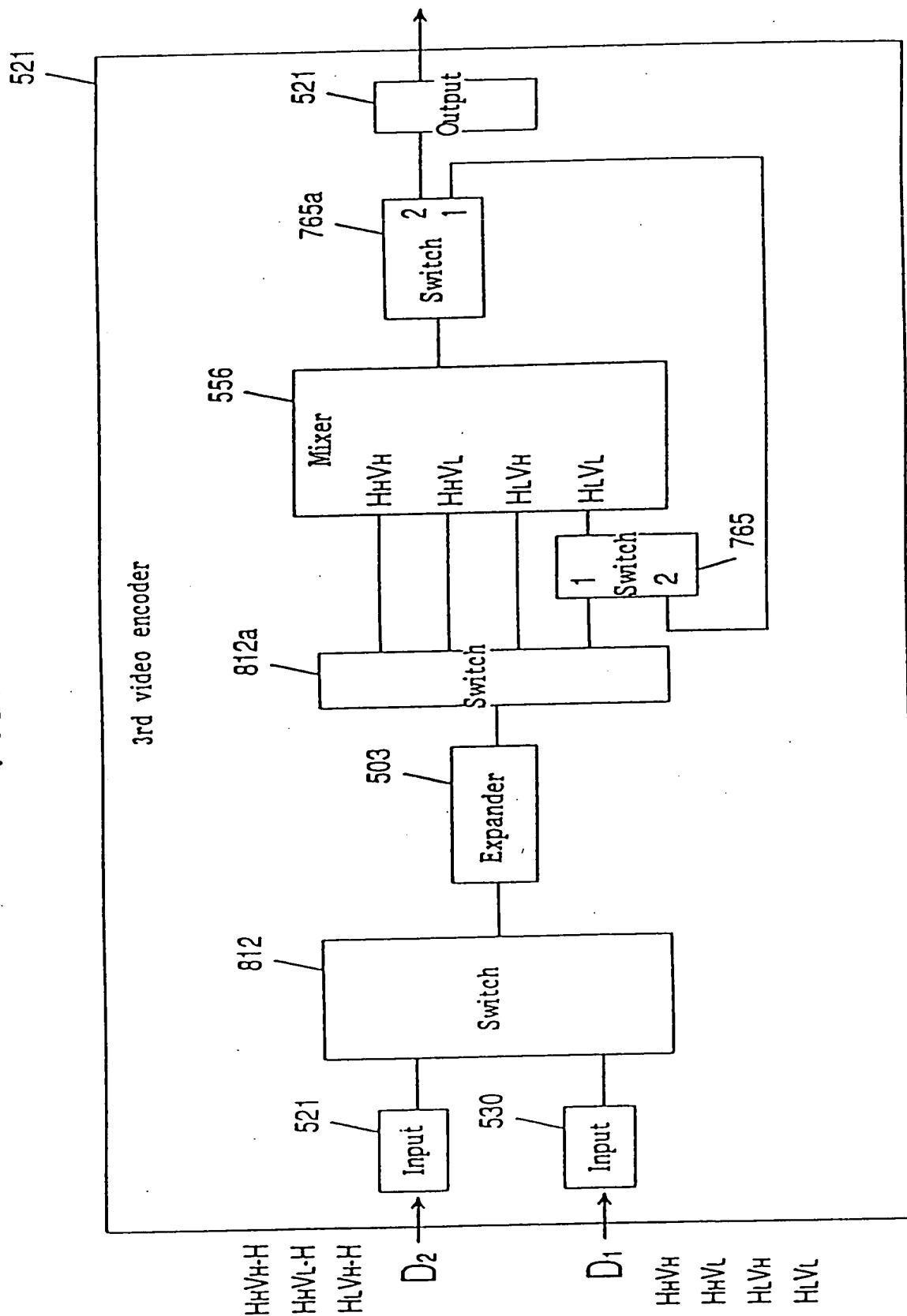


FIG. 79

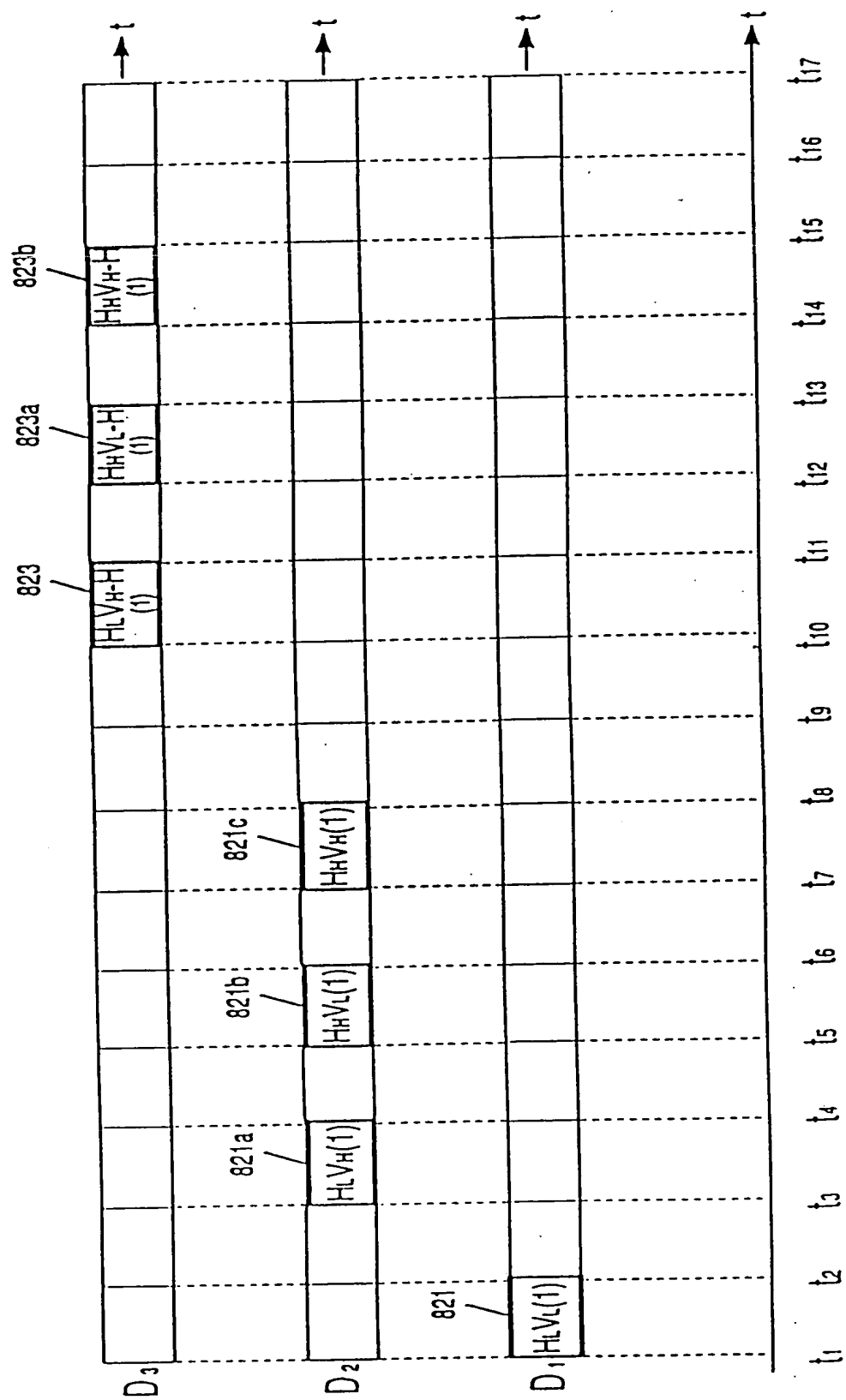


FIG. 80

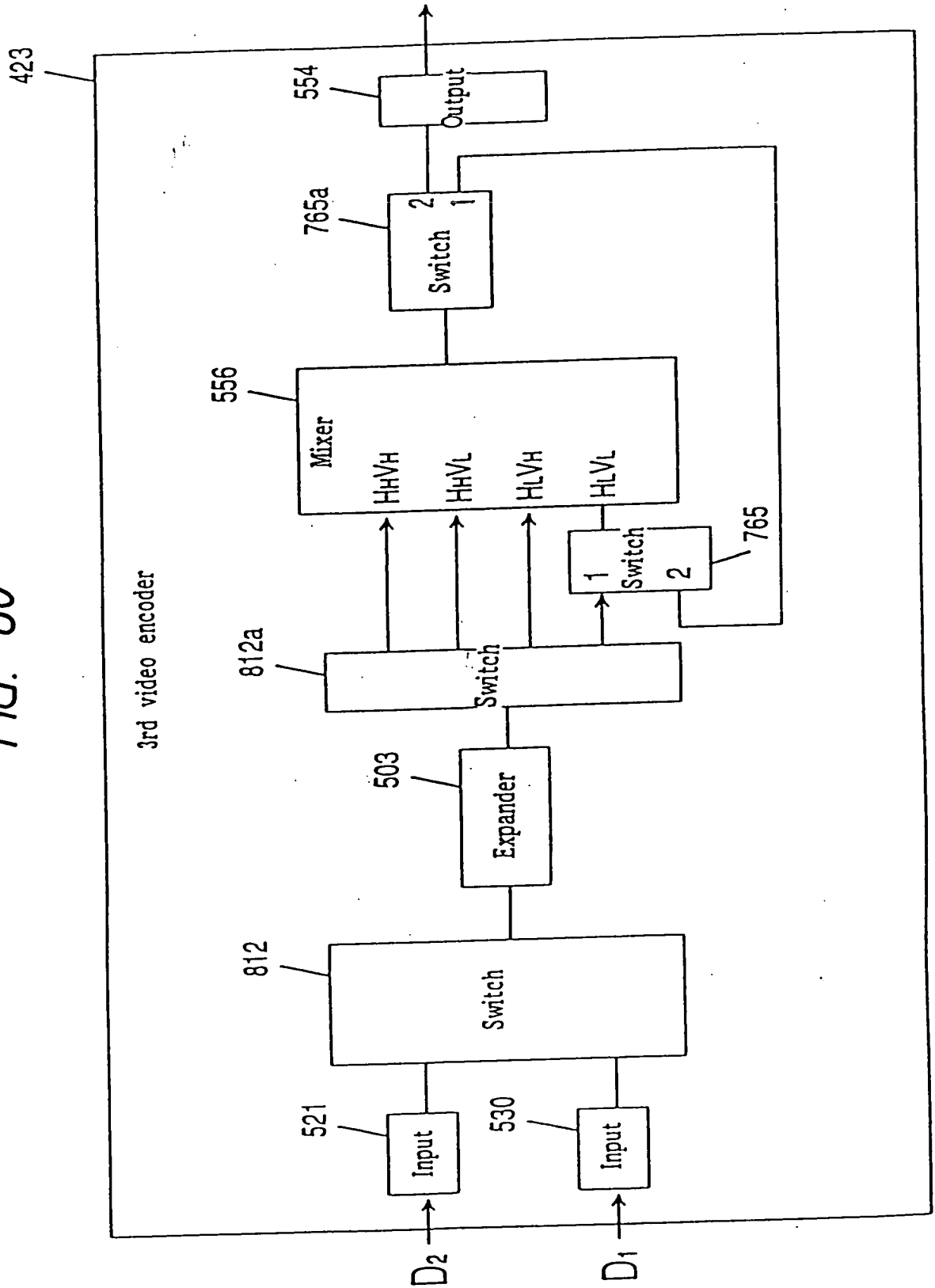


FIG. 81

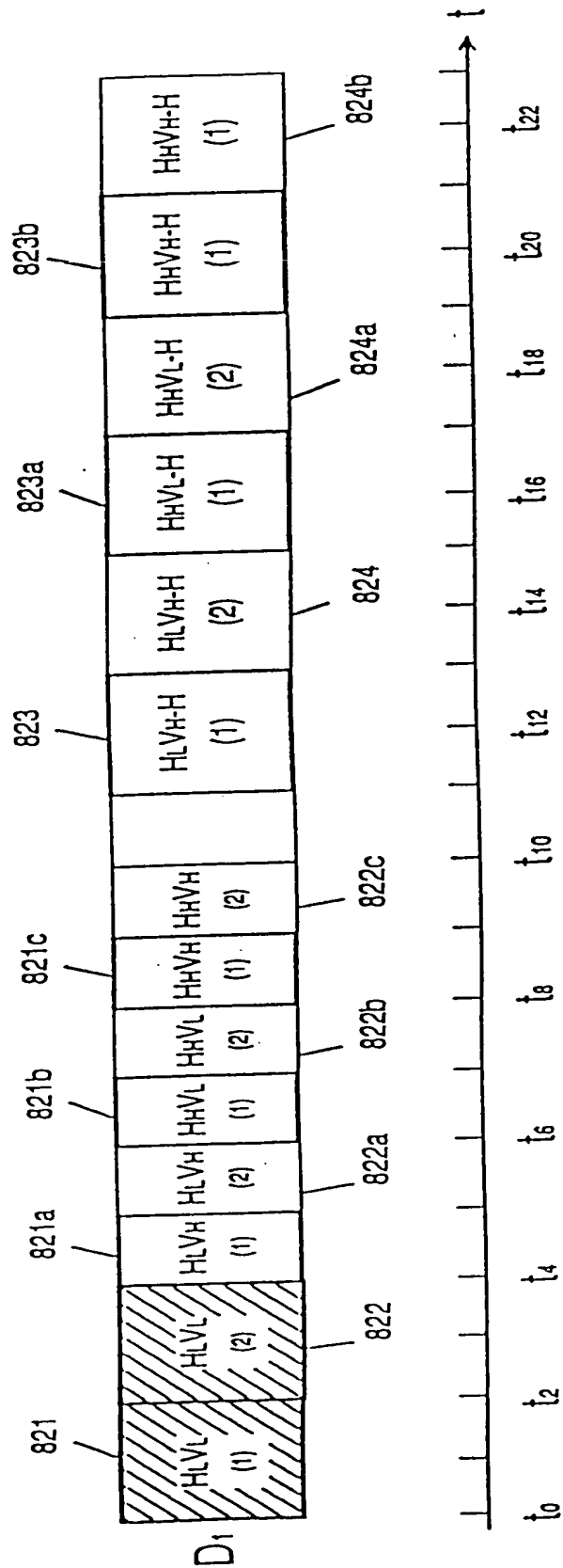


FIG. 82

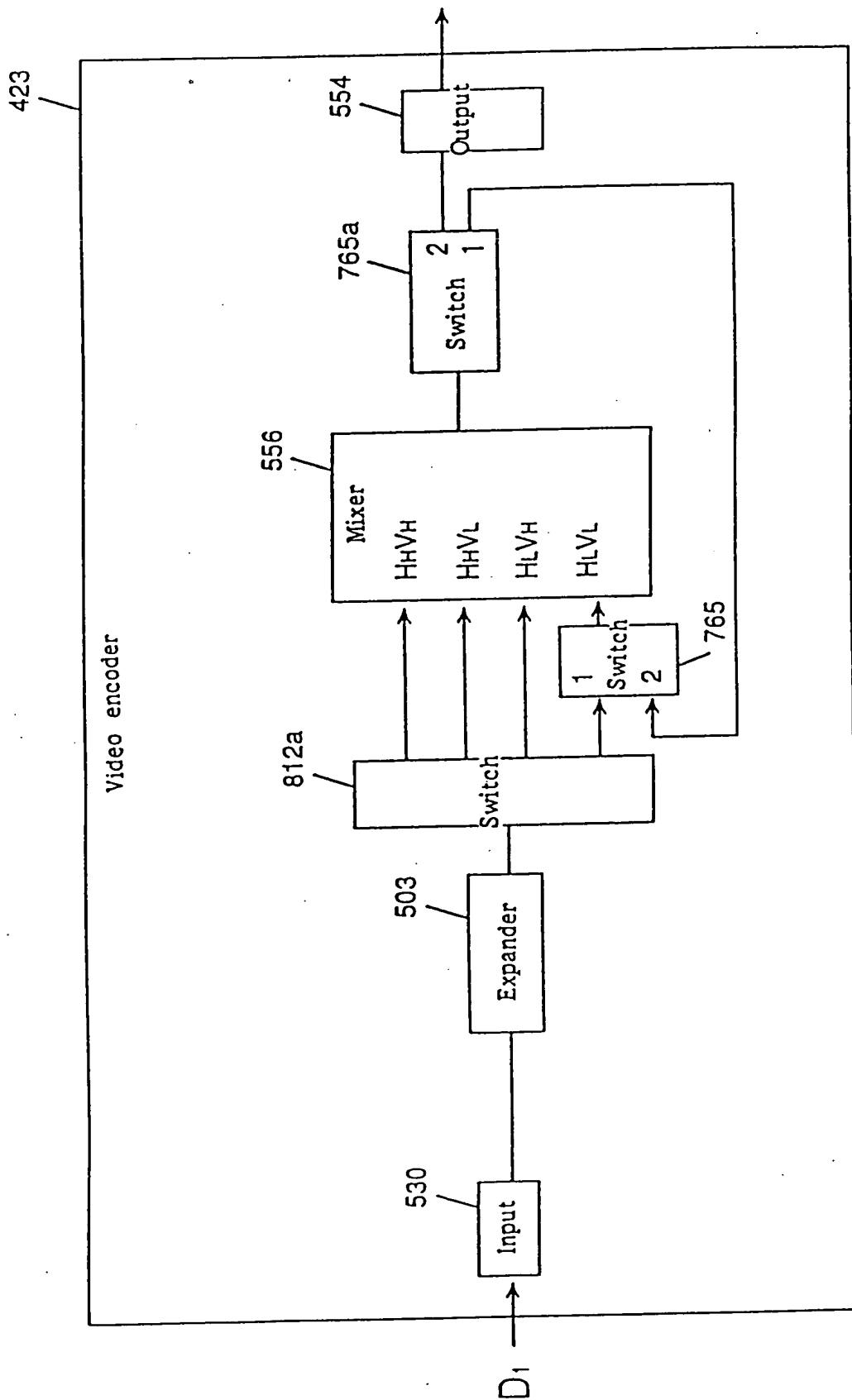
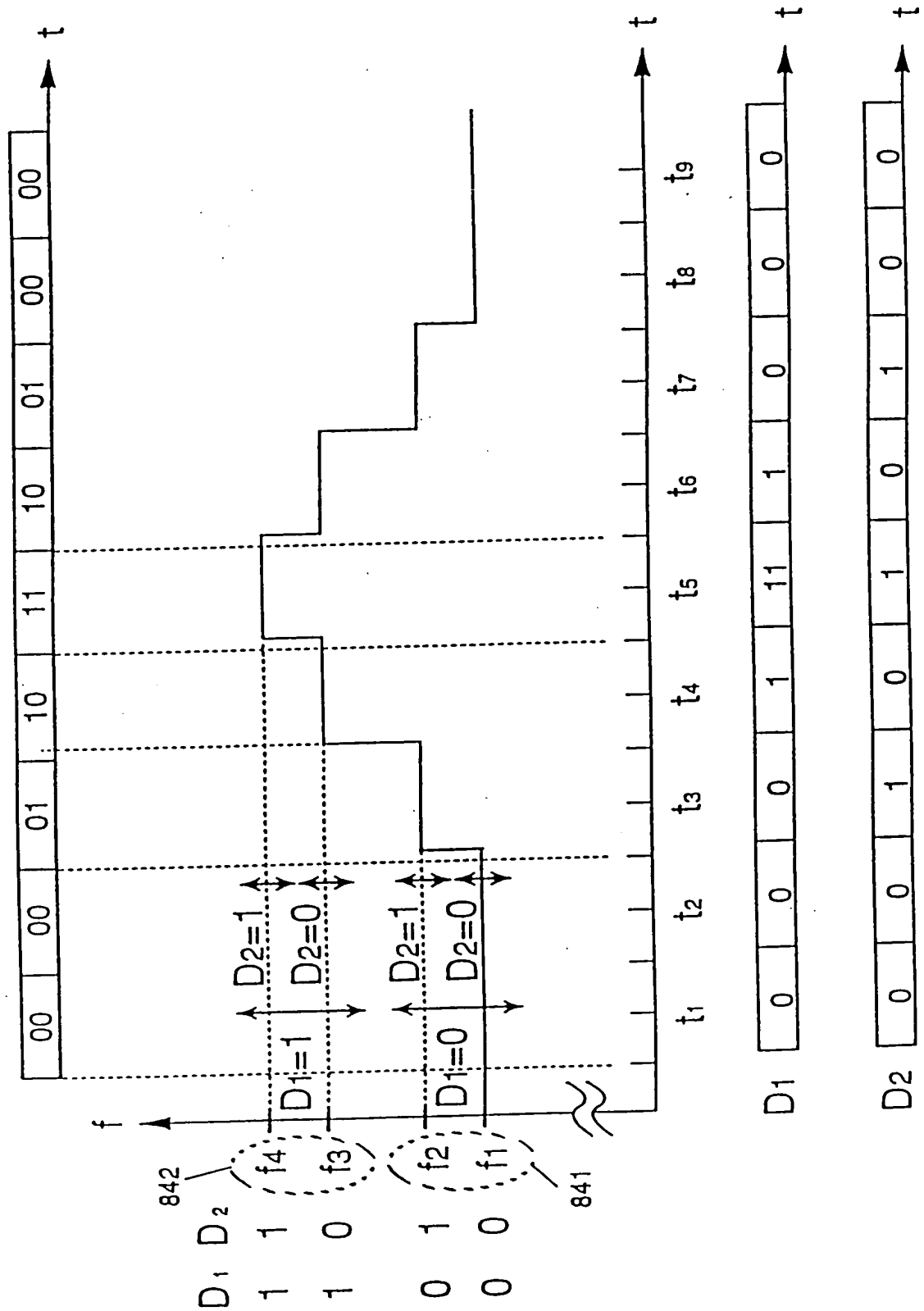


FIG. 83



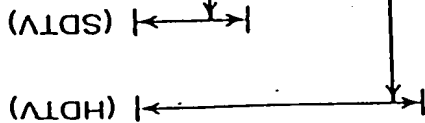


FIG. 85

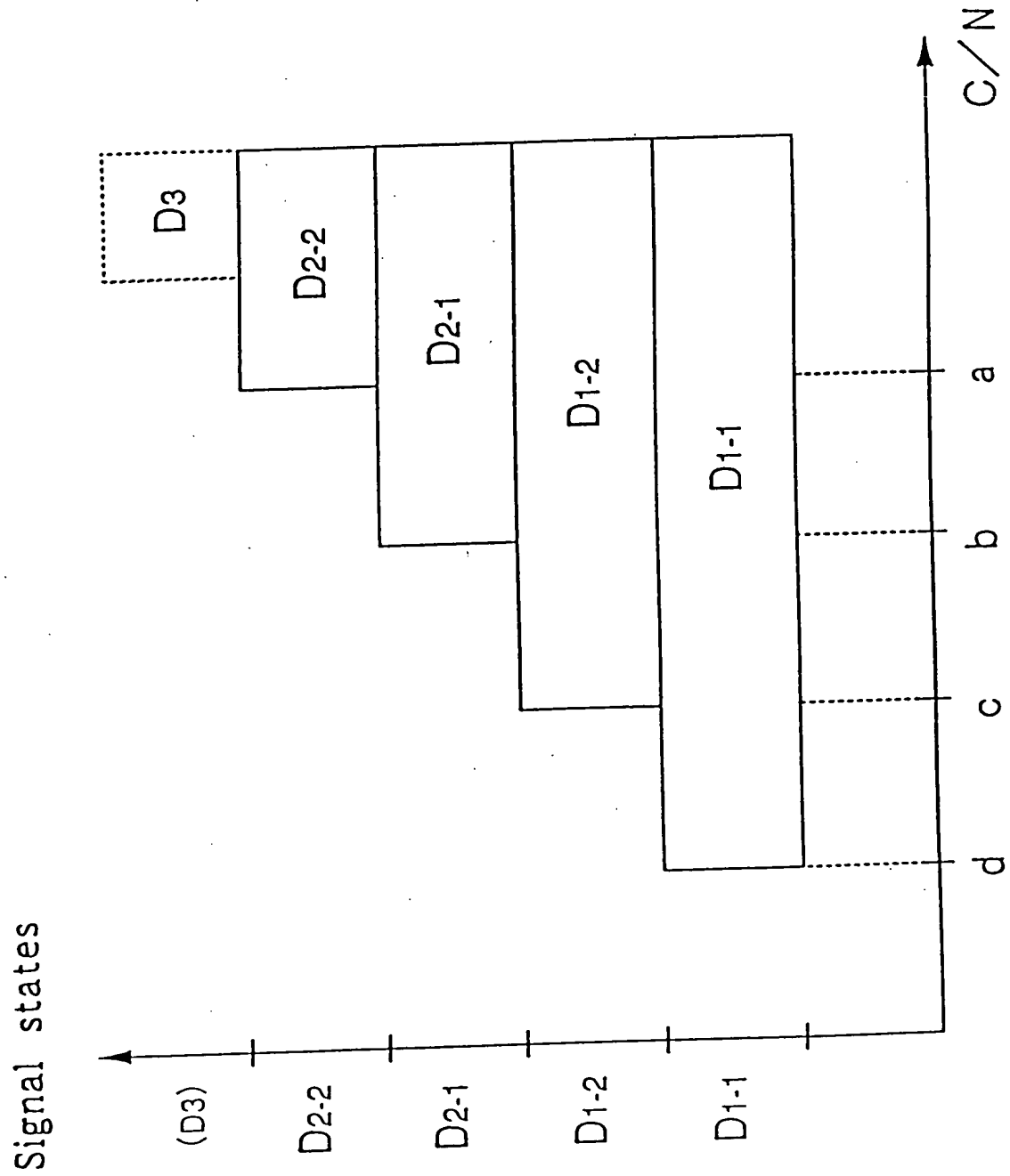


FIG. 86

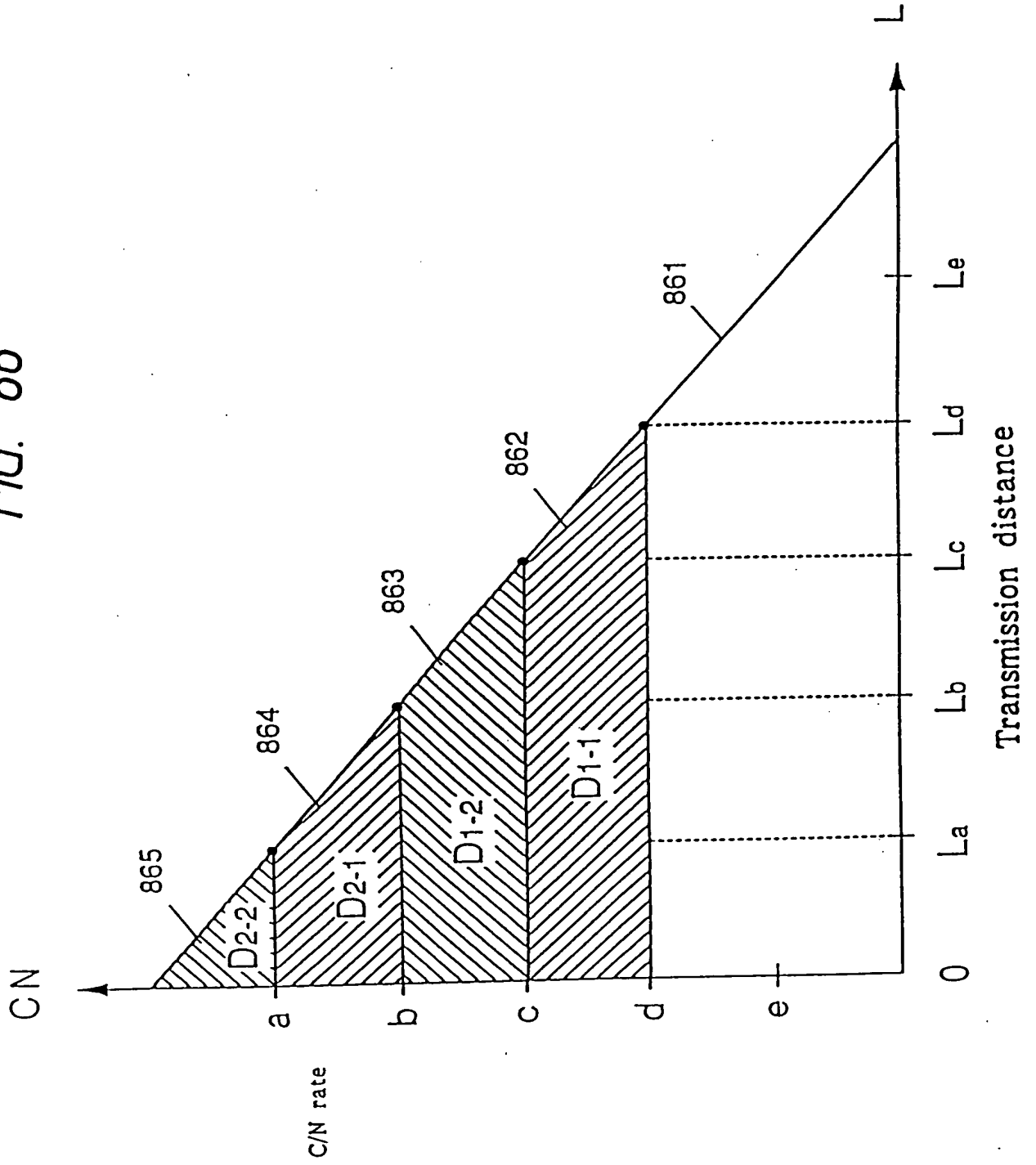


FIG. 87

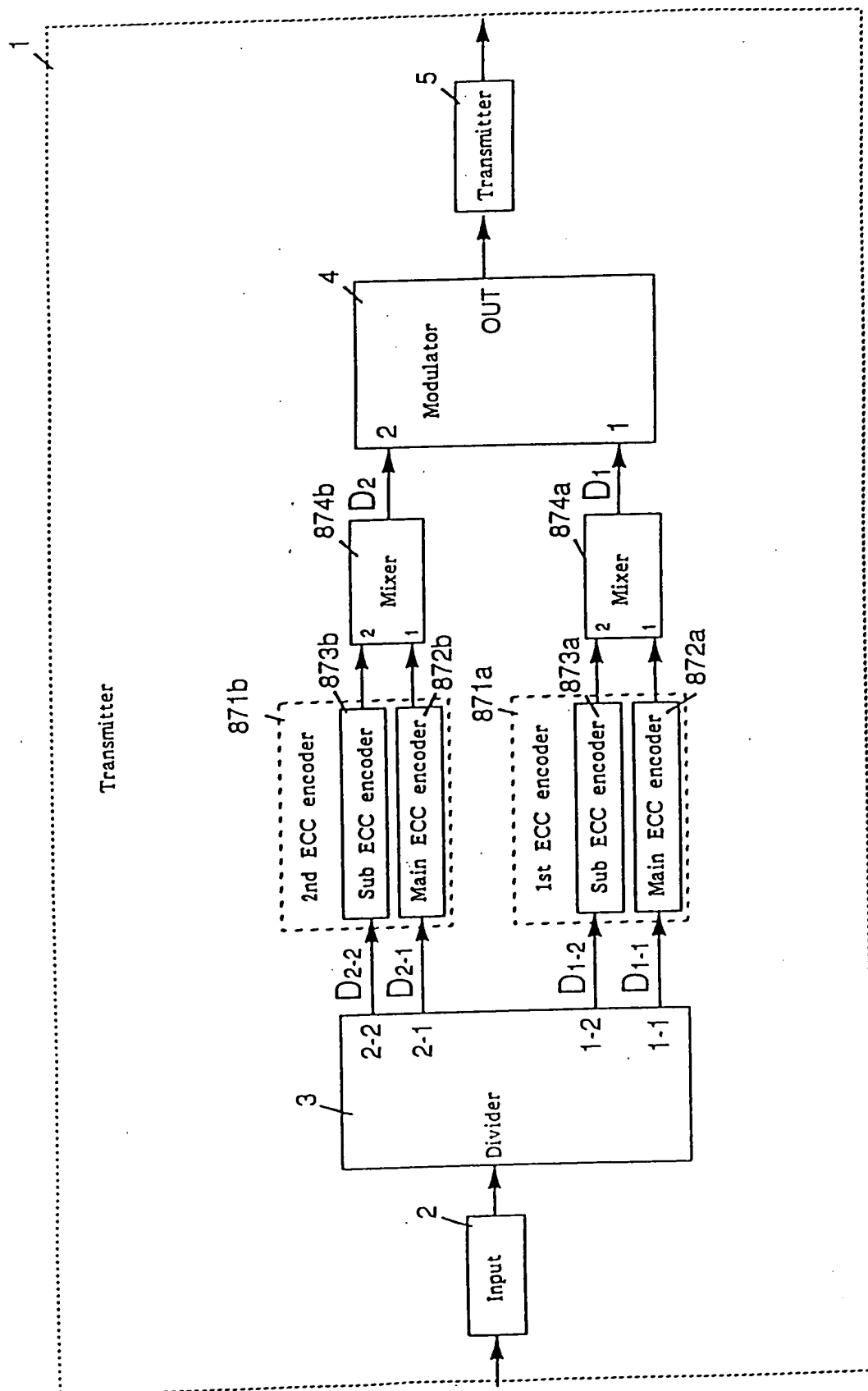


FIG. 88

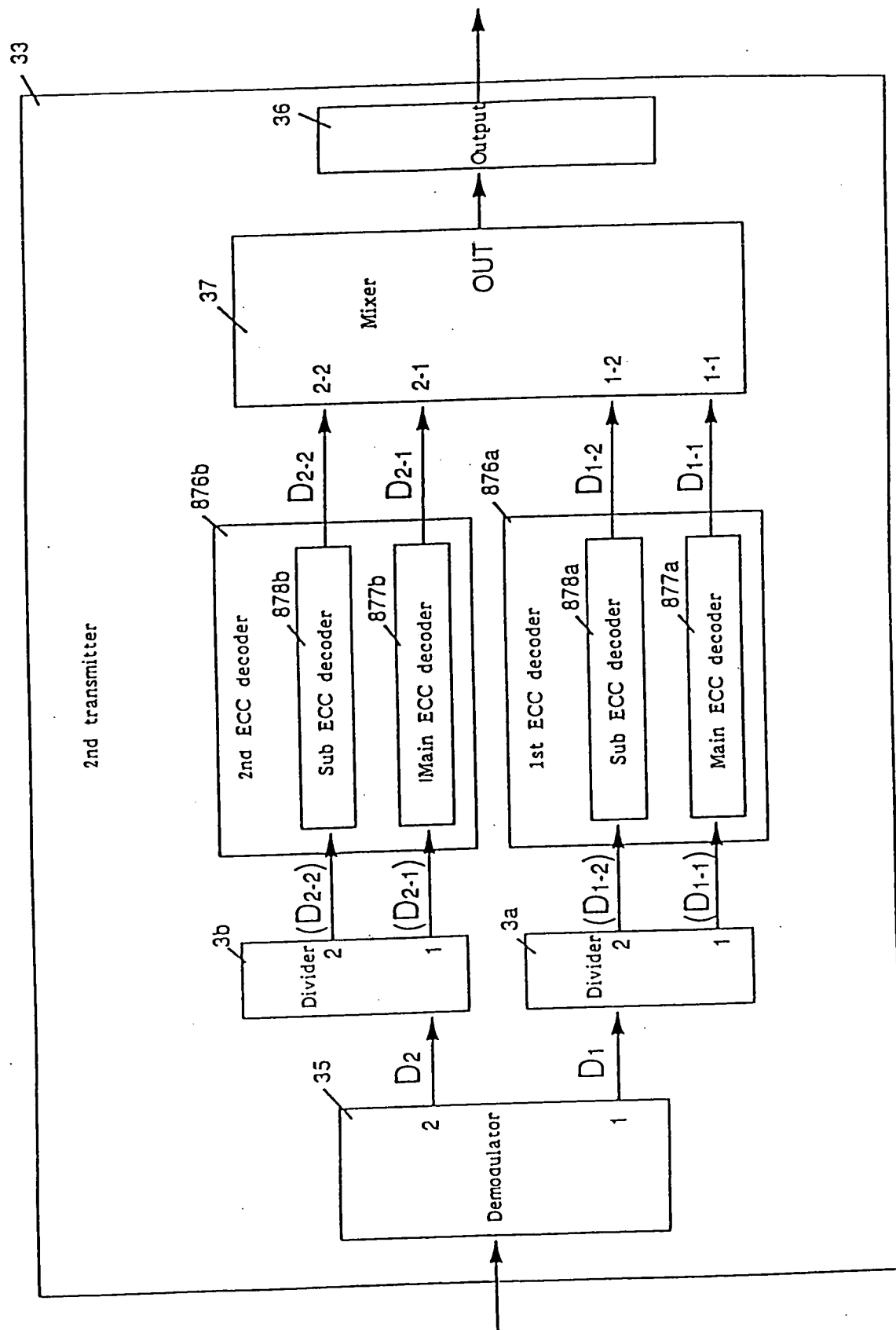
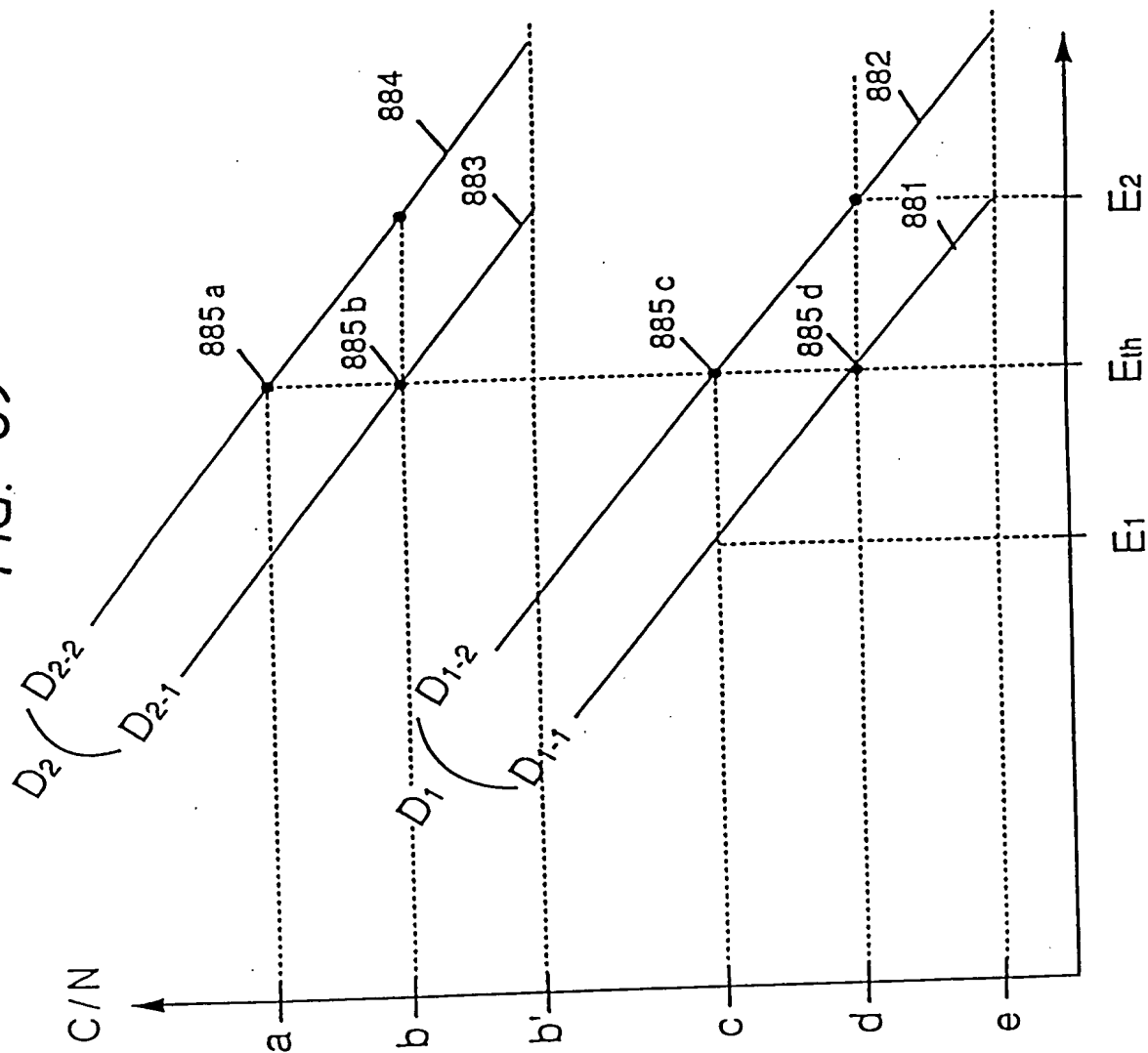


FIG. 89



Error rate after error correction

FIG. 90

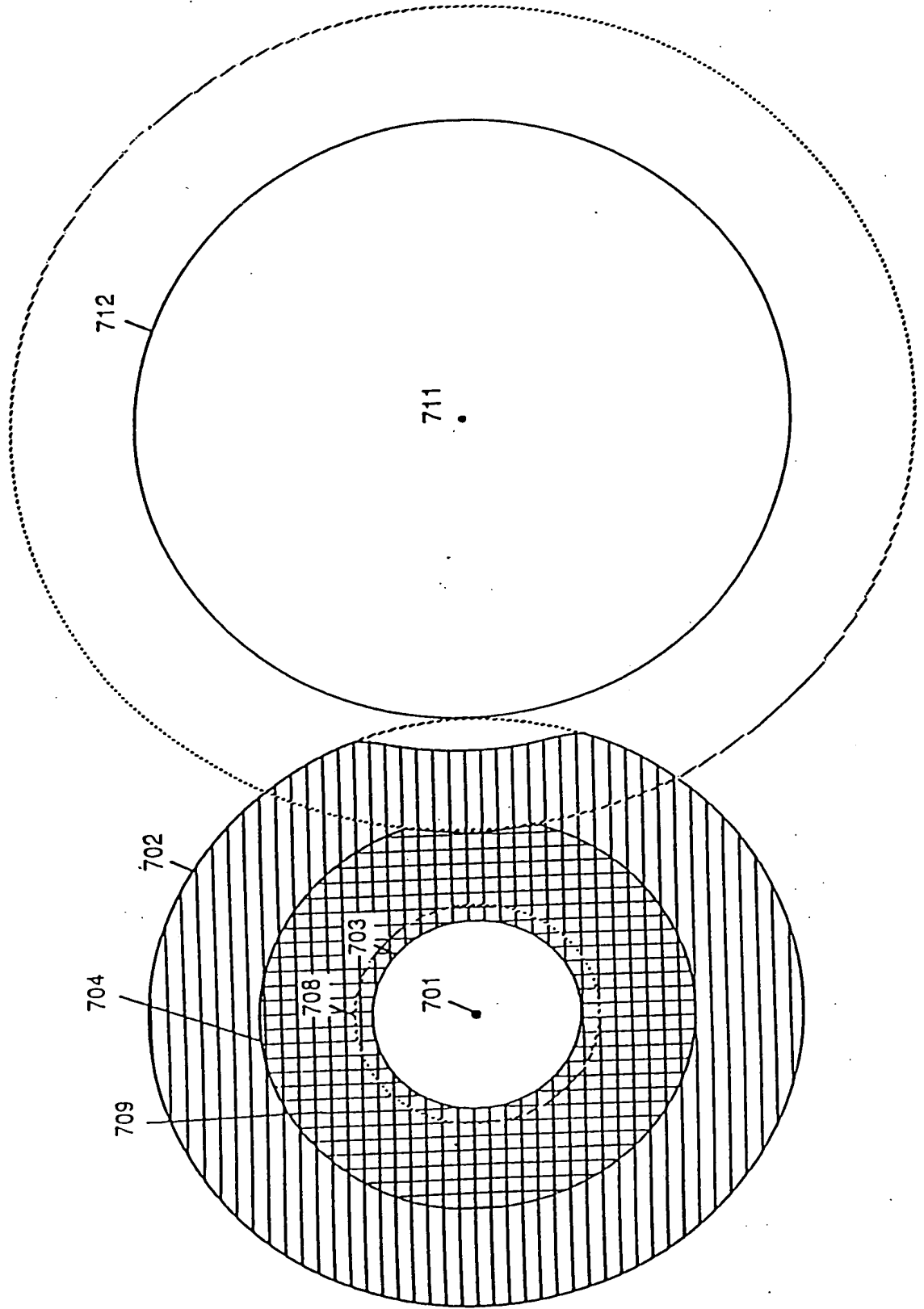


FIG. 91

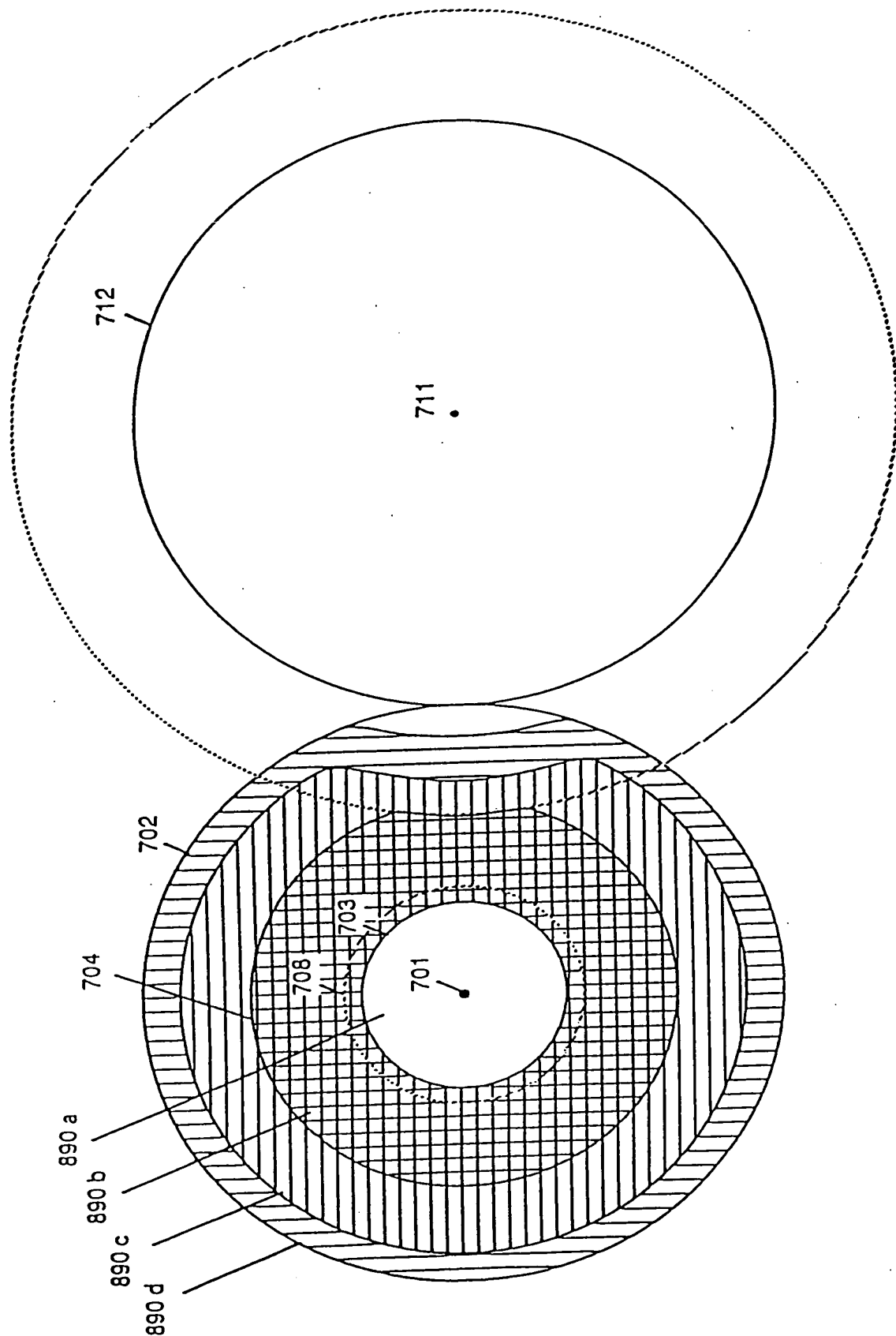


FIG. 92

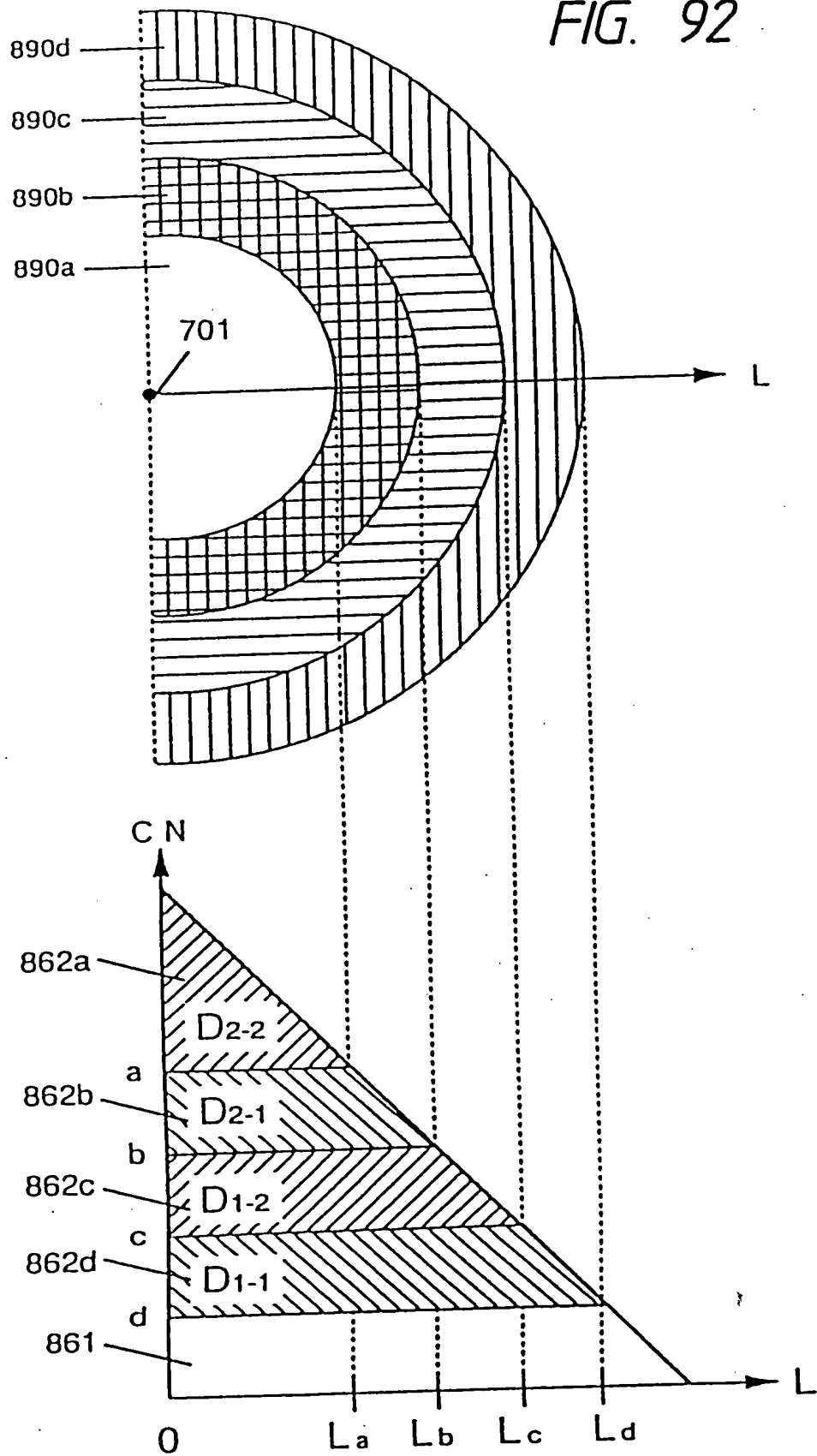


FIG. 93

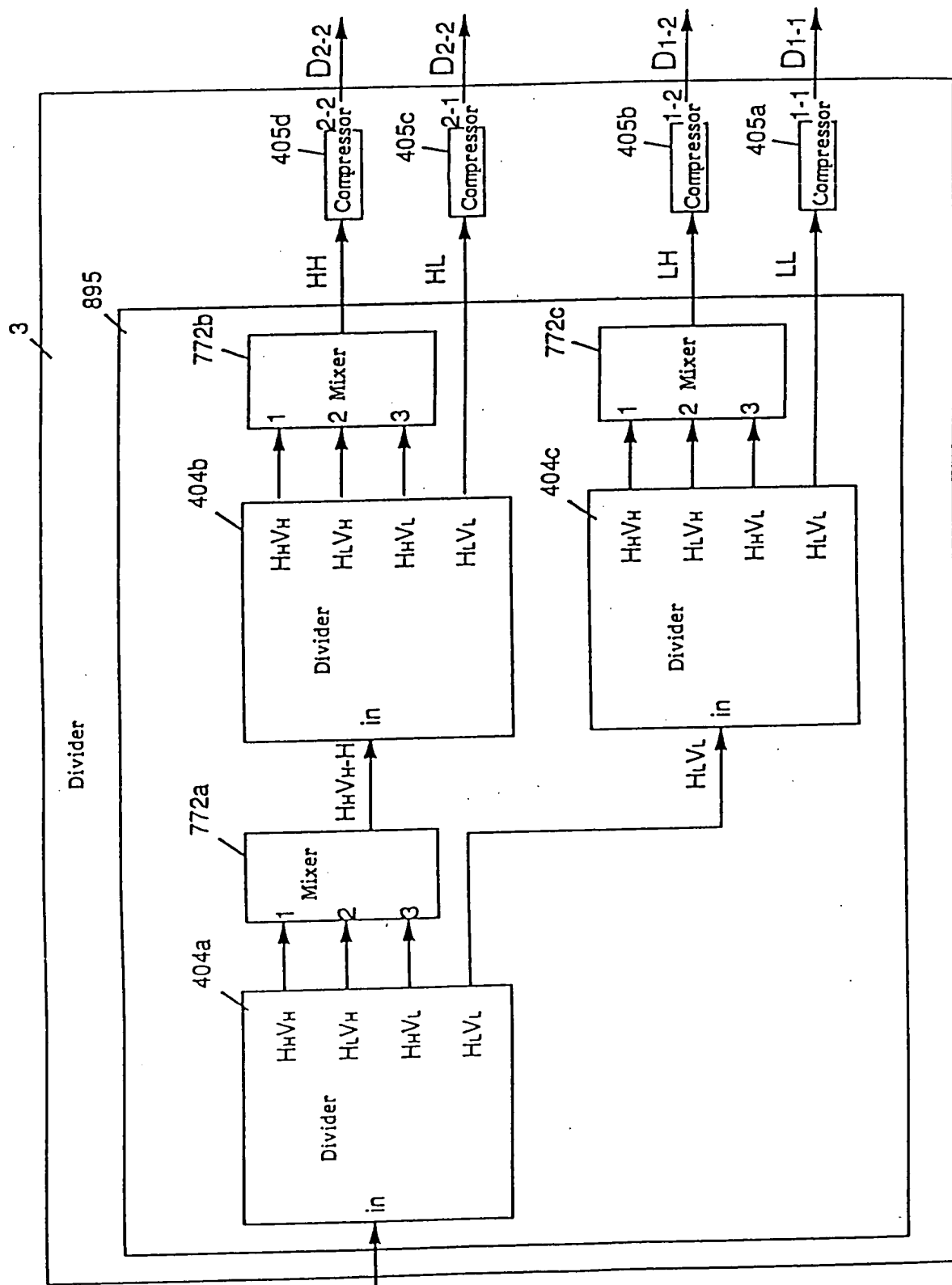


FIG. 94

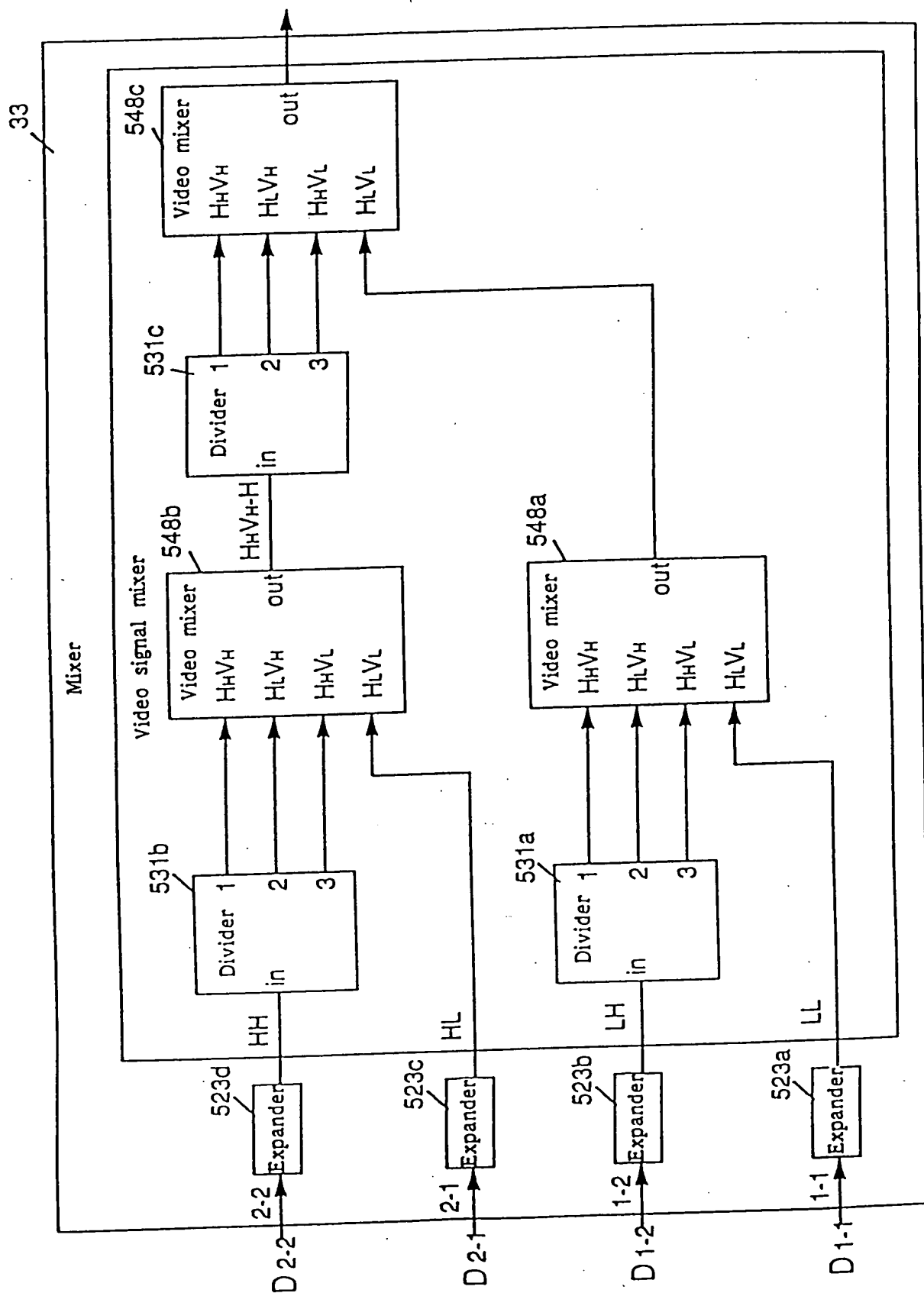


FIG. 95

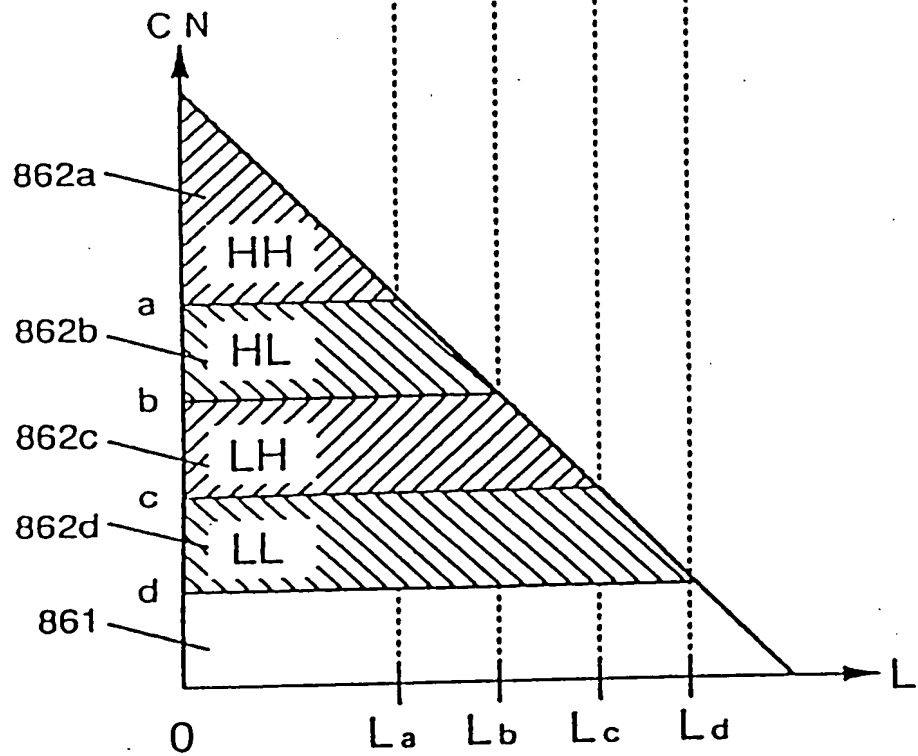
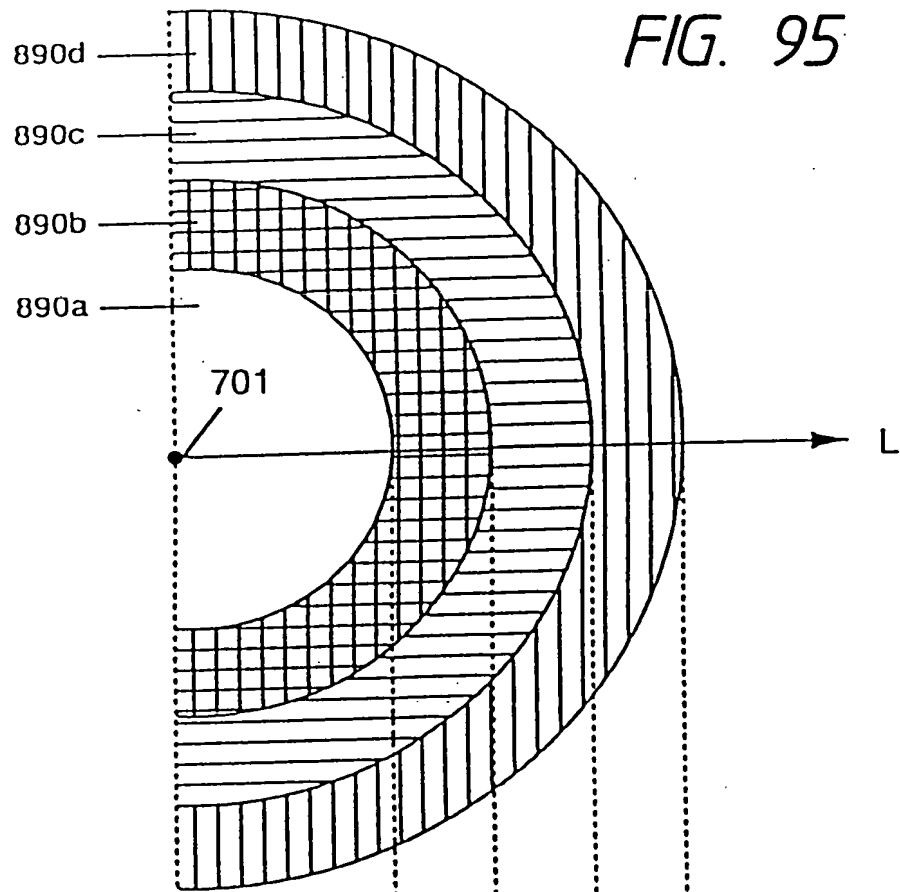


FIG. 96

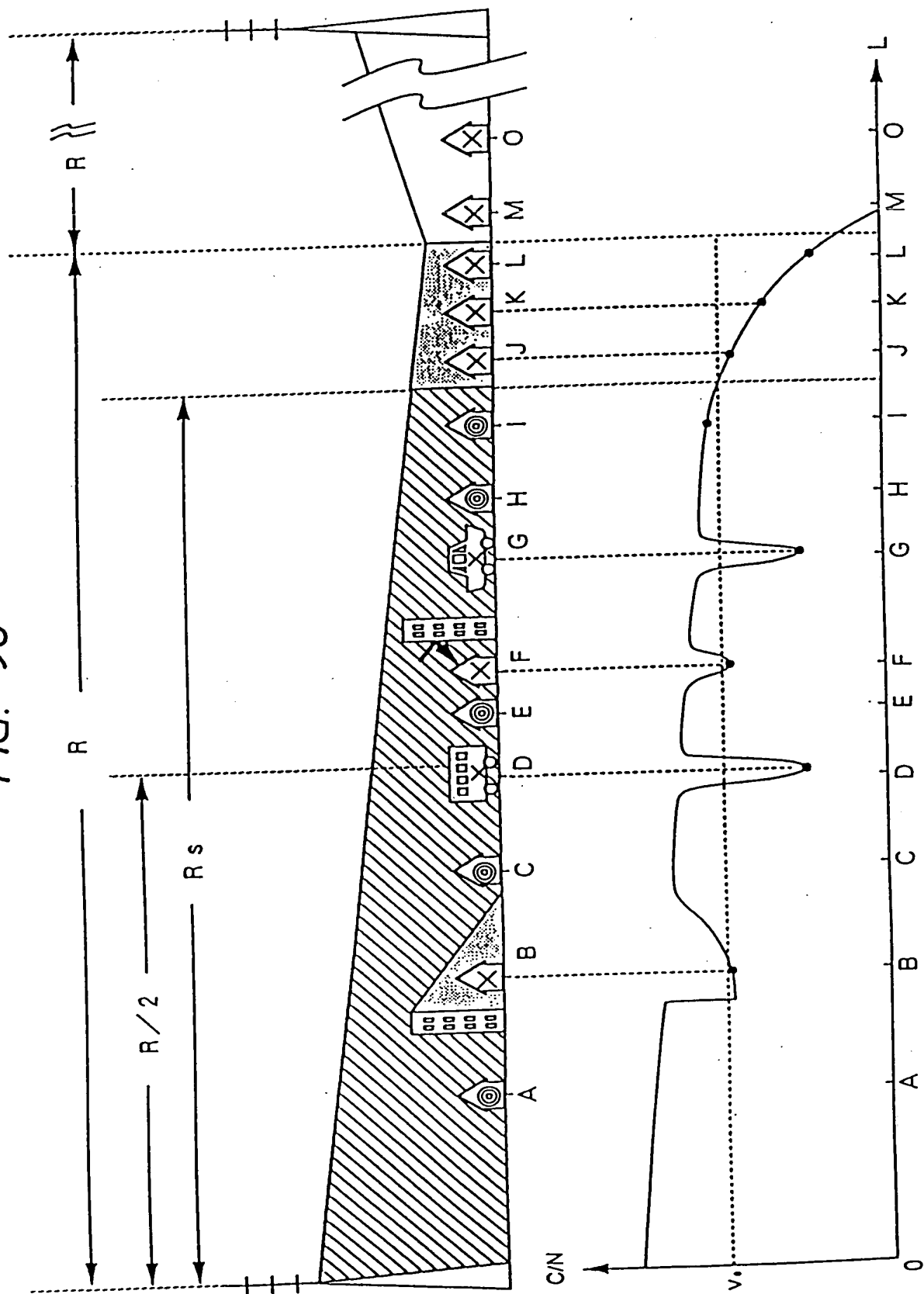


FIG. 97

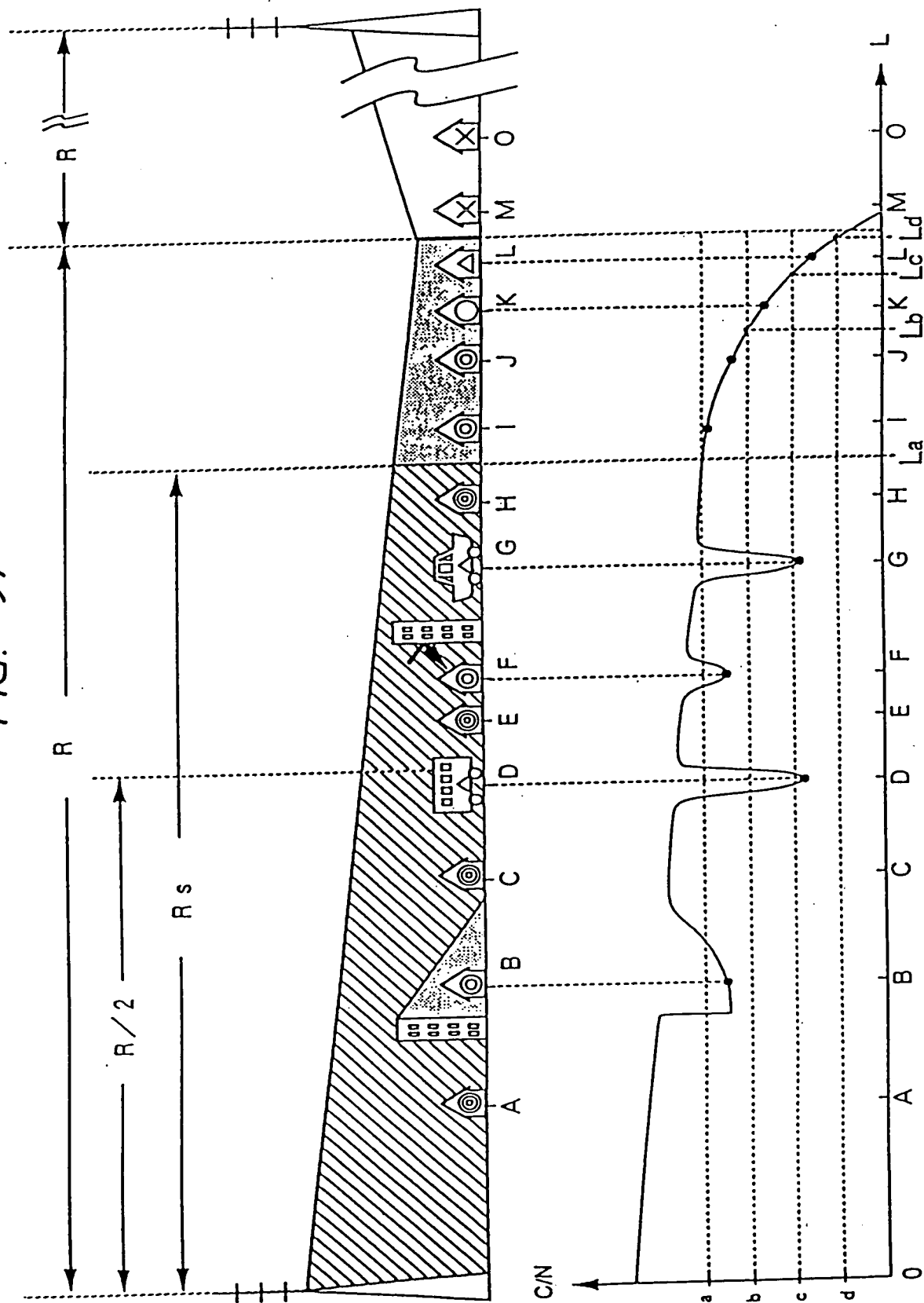


FIG. 98

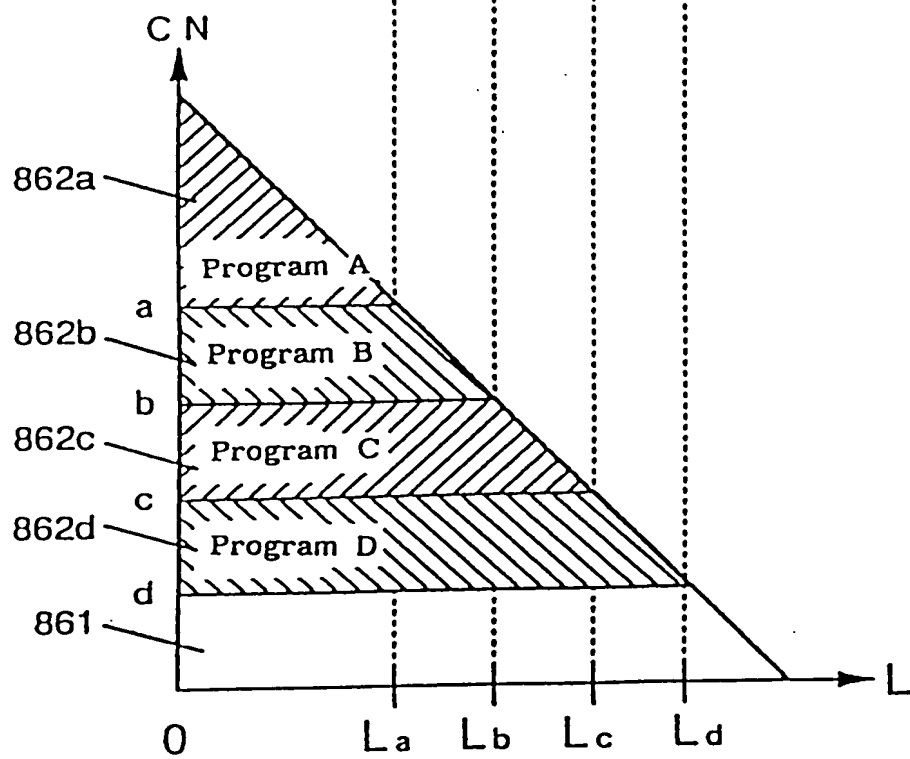
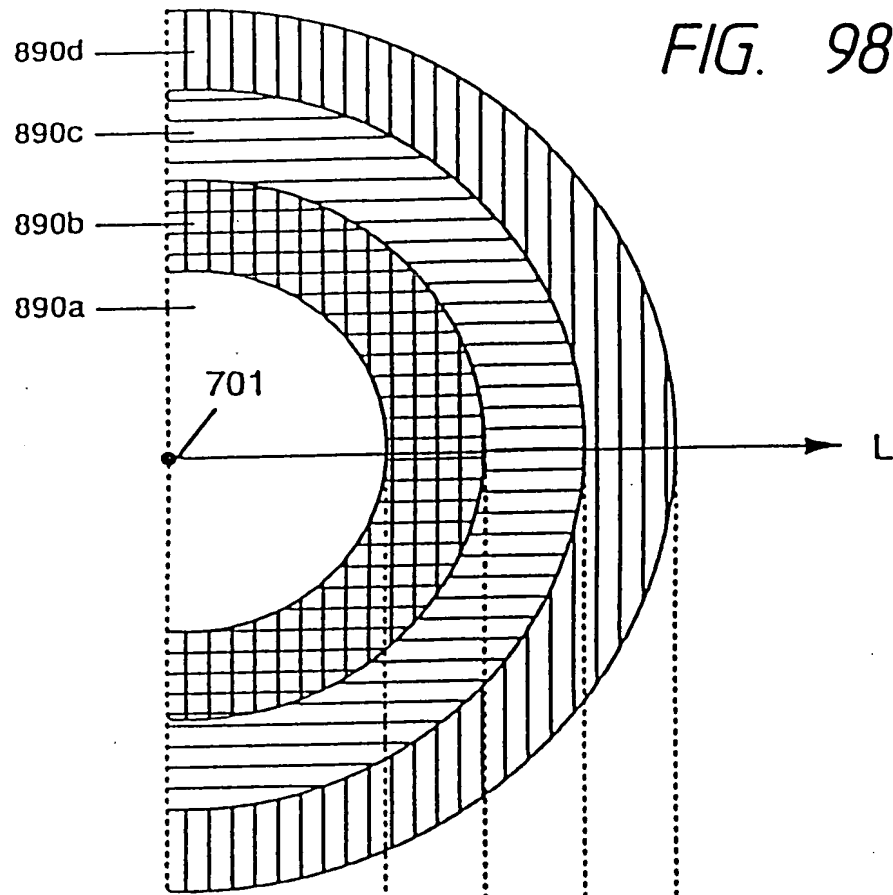


FIG. 99

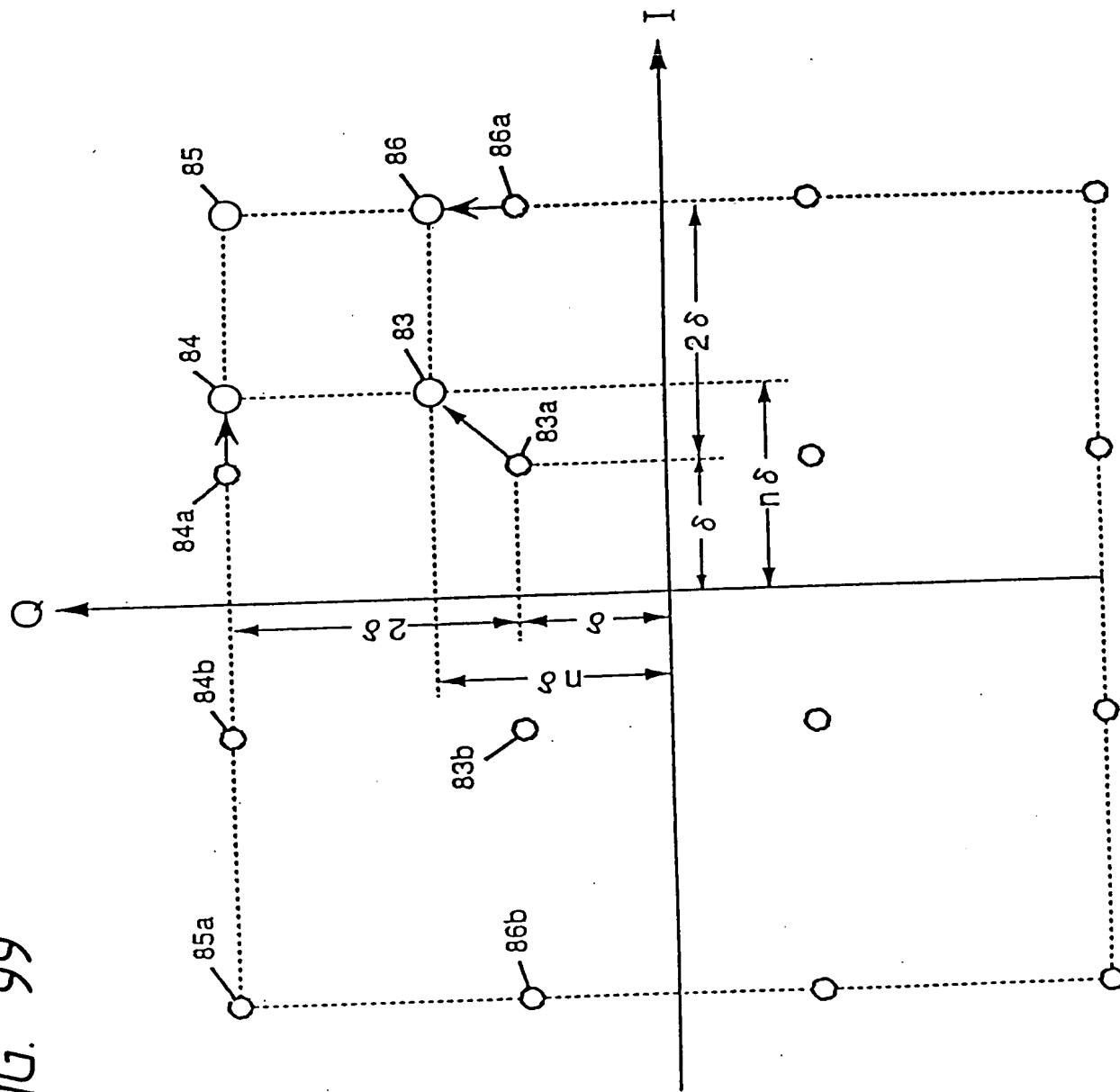
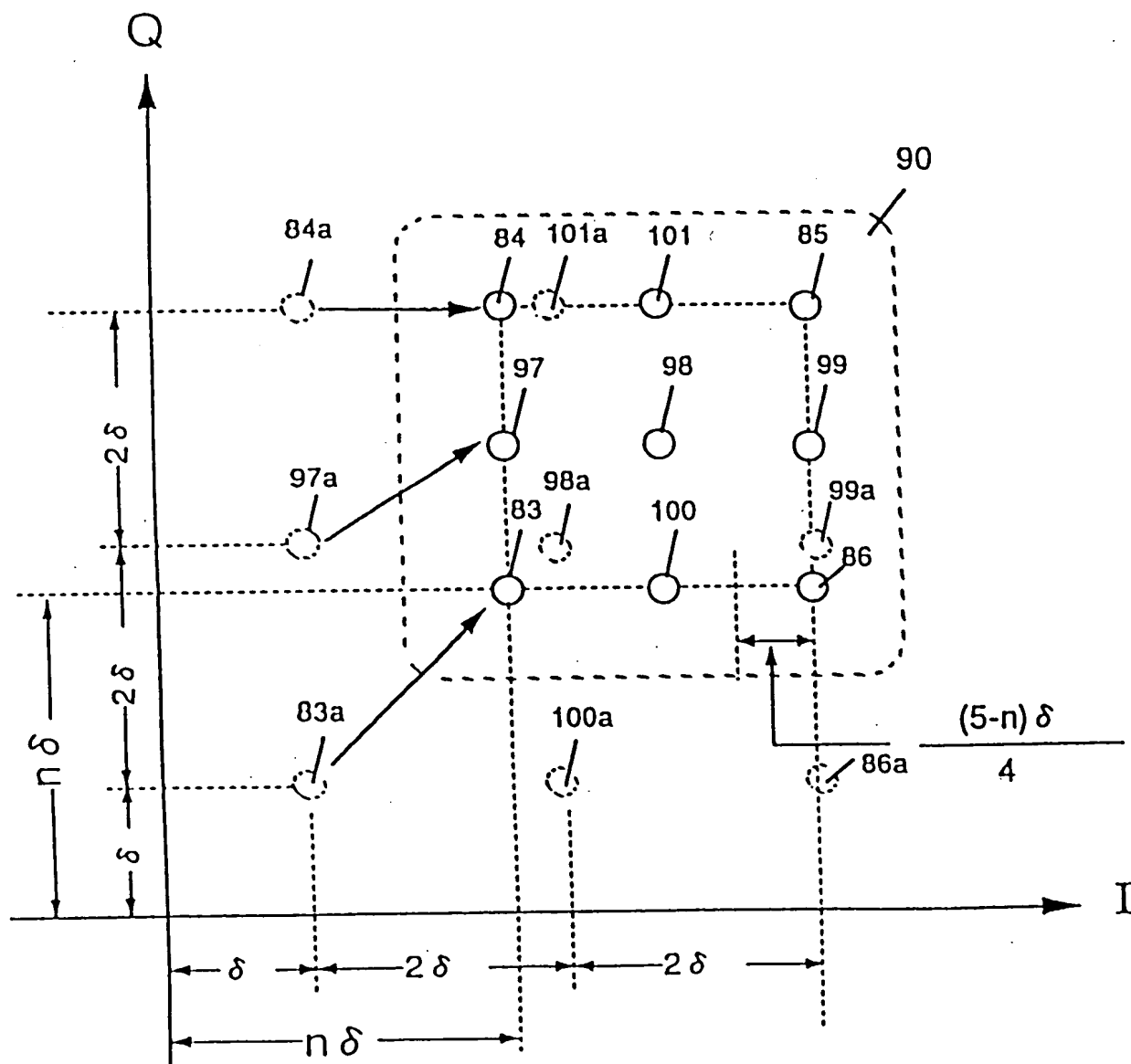


FIG. 100



1. The first step is to identify the problem. In this case, the problem is that the company is not meeting its sales targets.

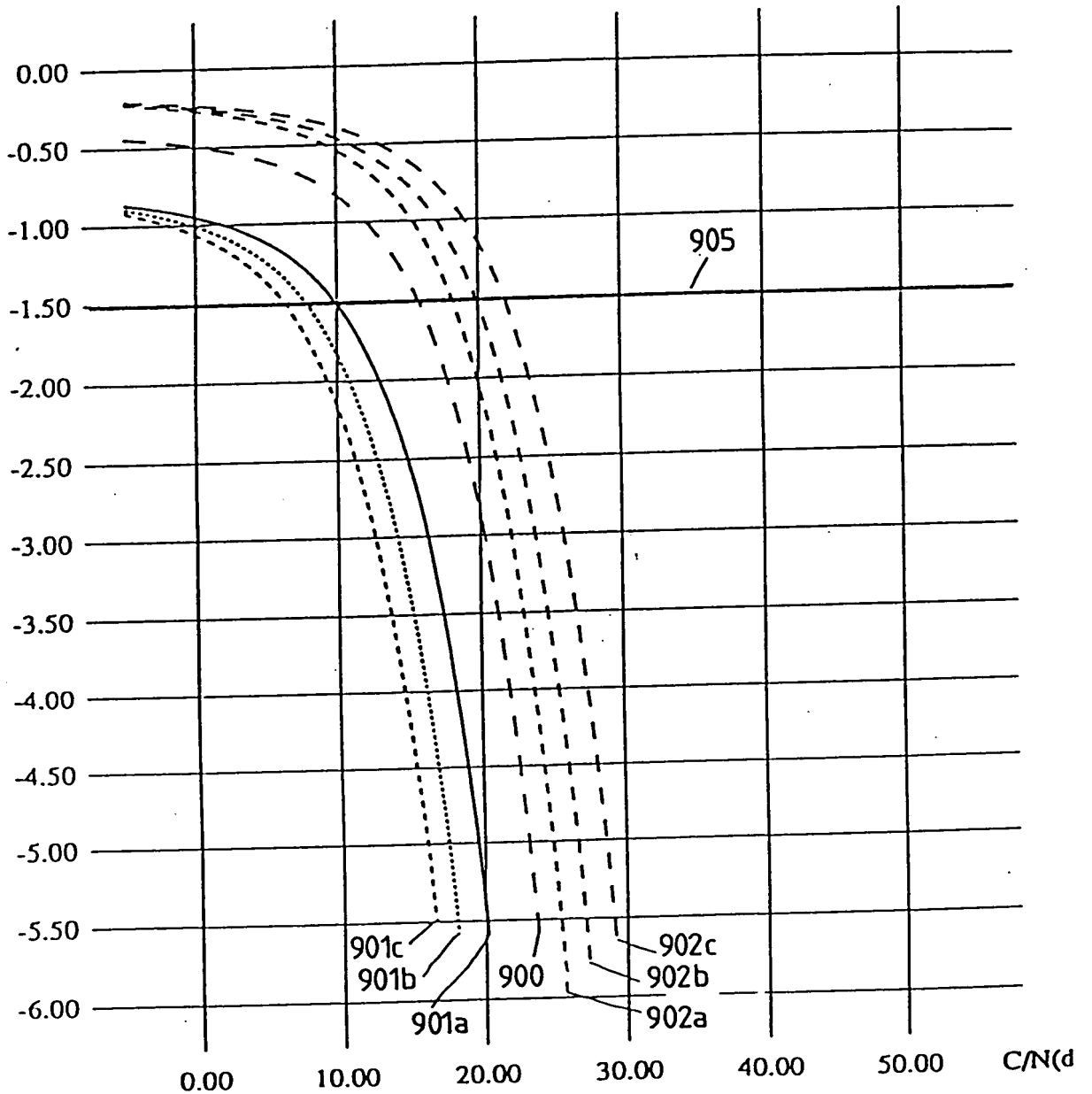


FIG. 102

Pe

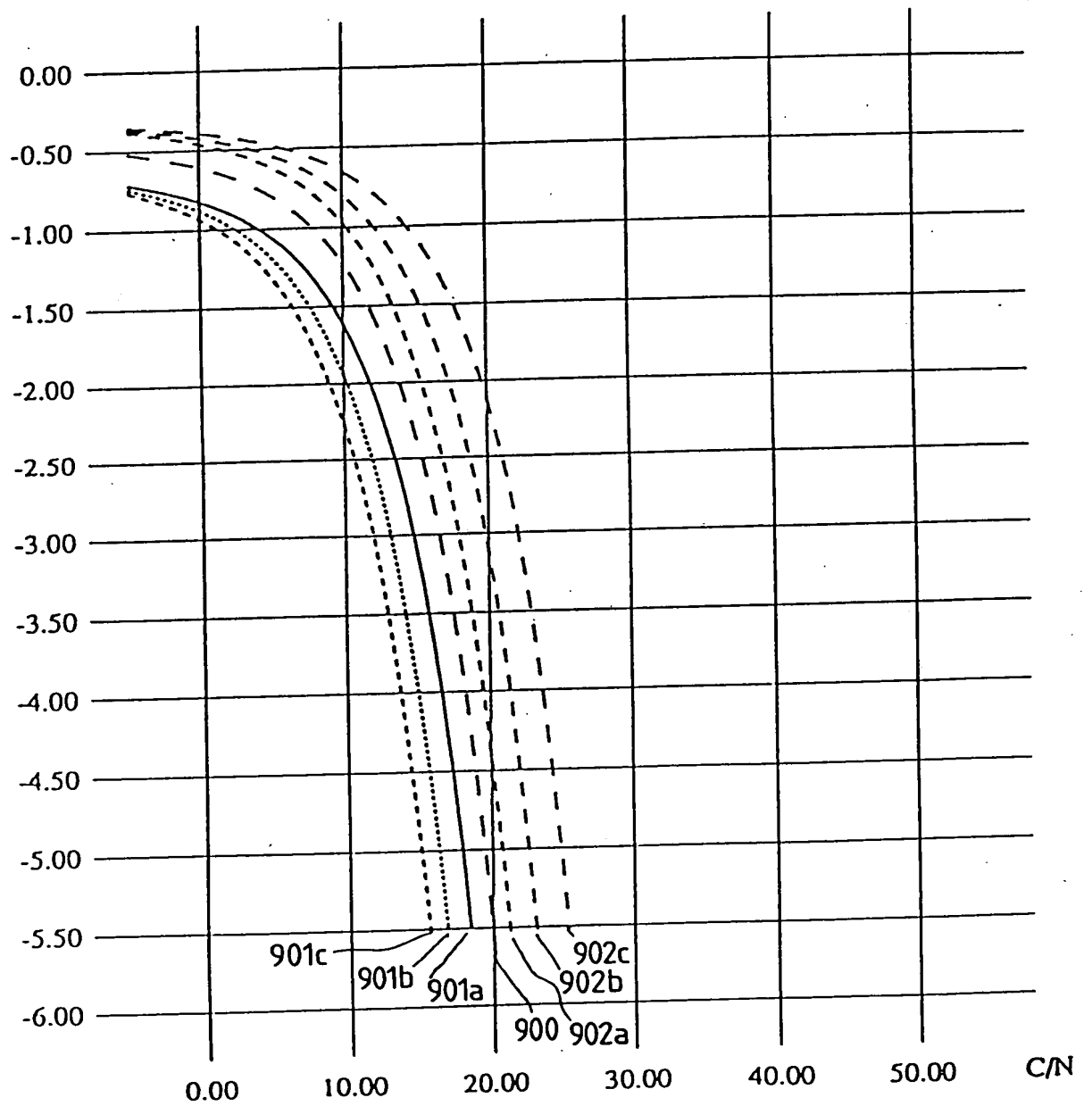


FIG. 103

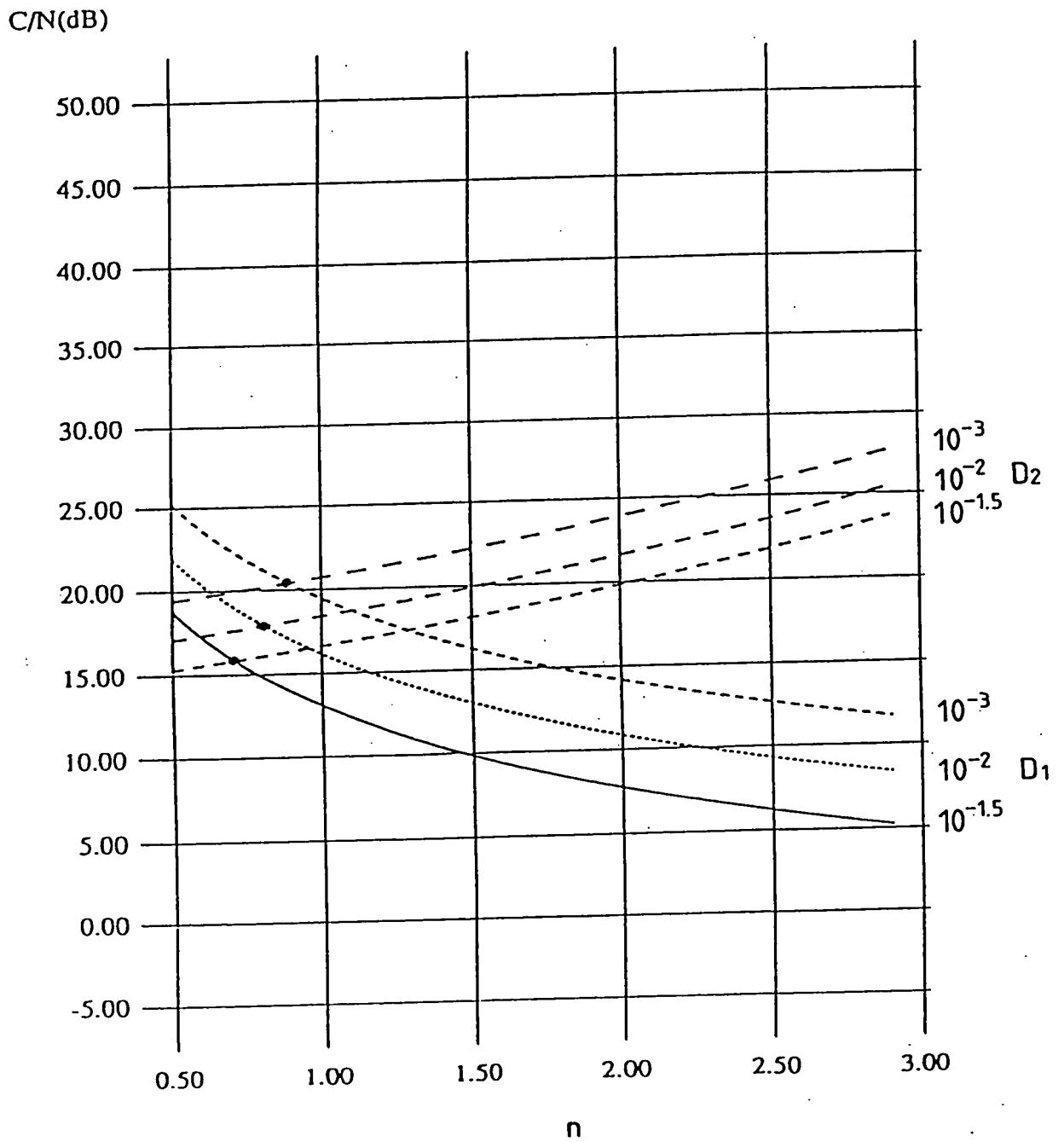


FIG. 104

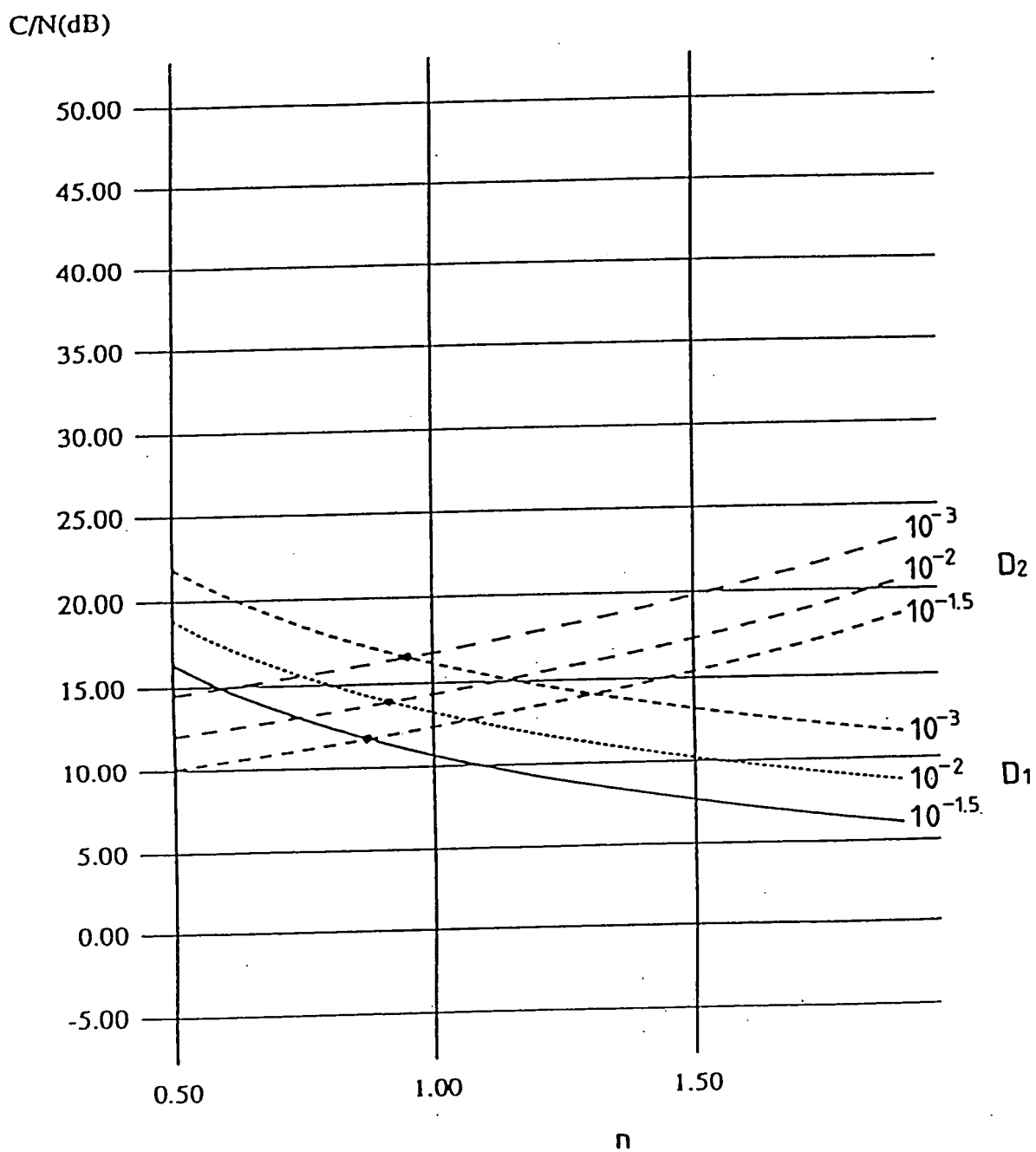


FIG. 105

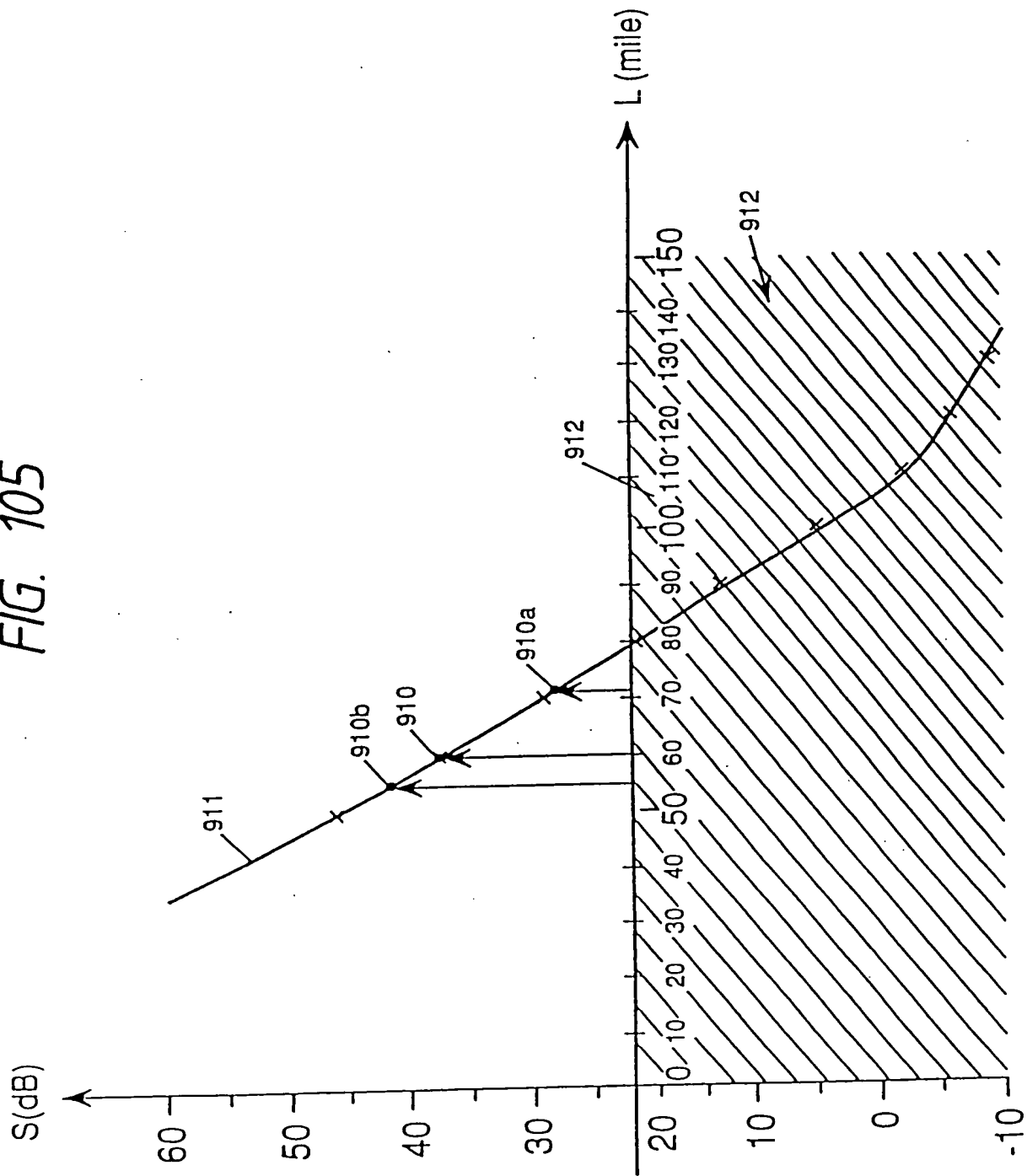


FIG. 106

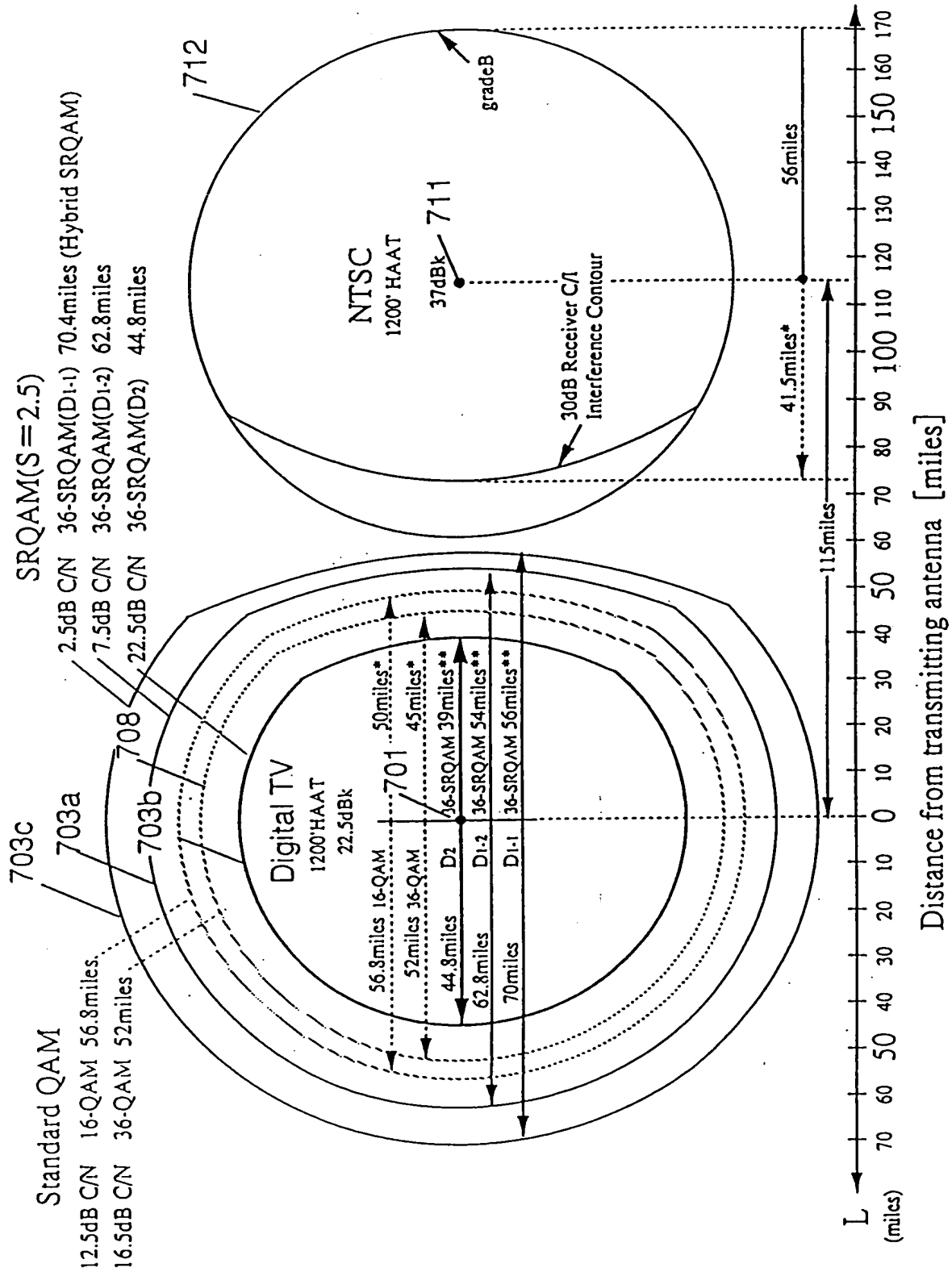


FIG. 107

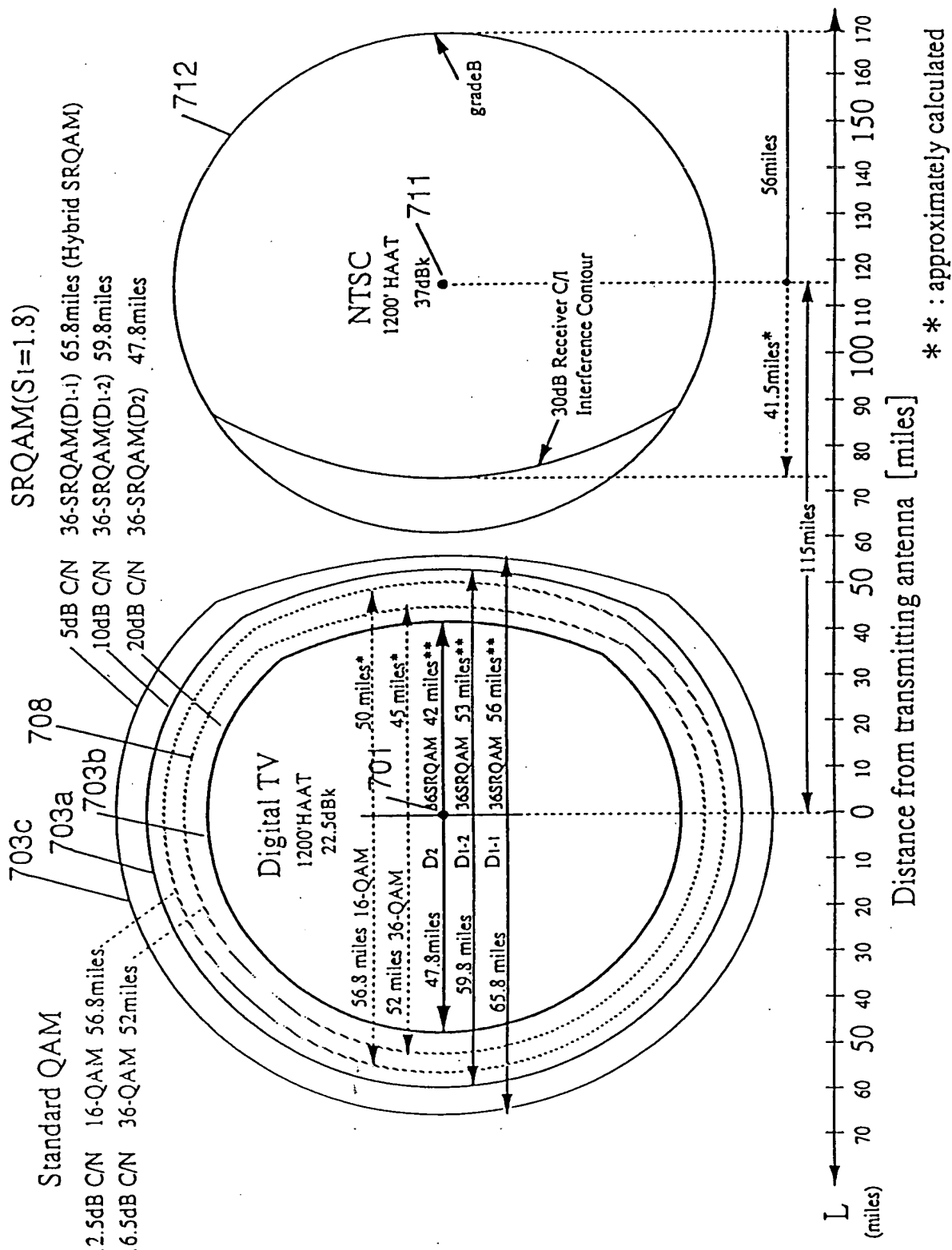


FIG. 108

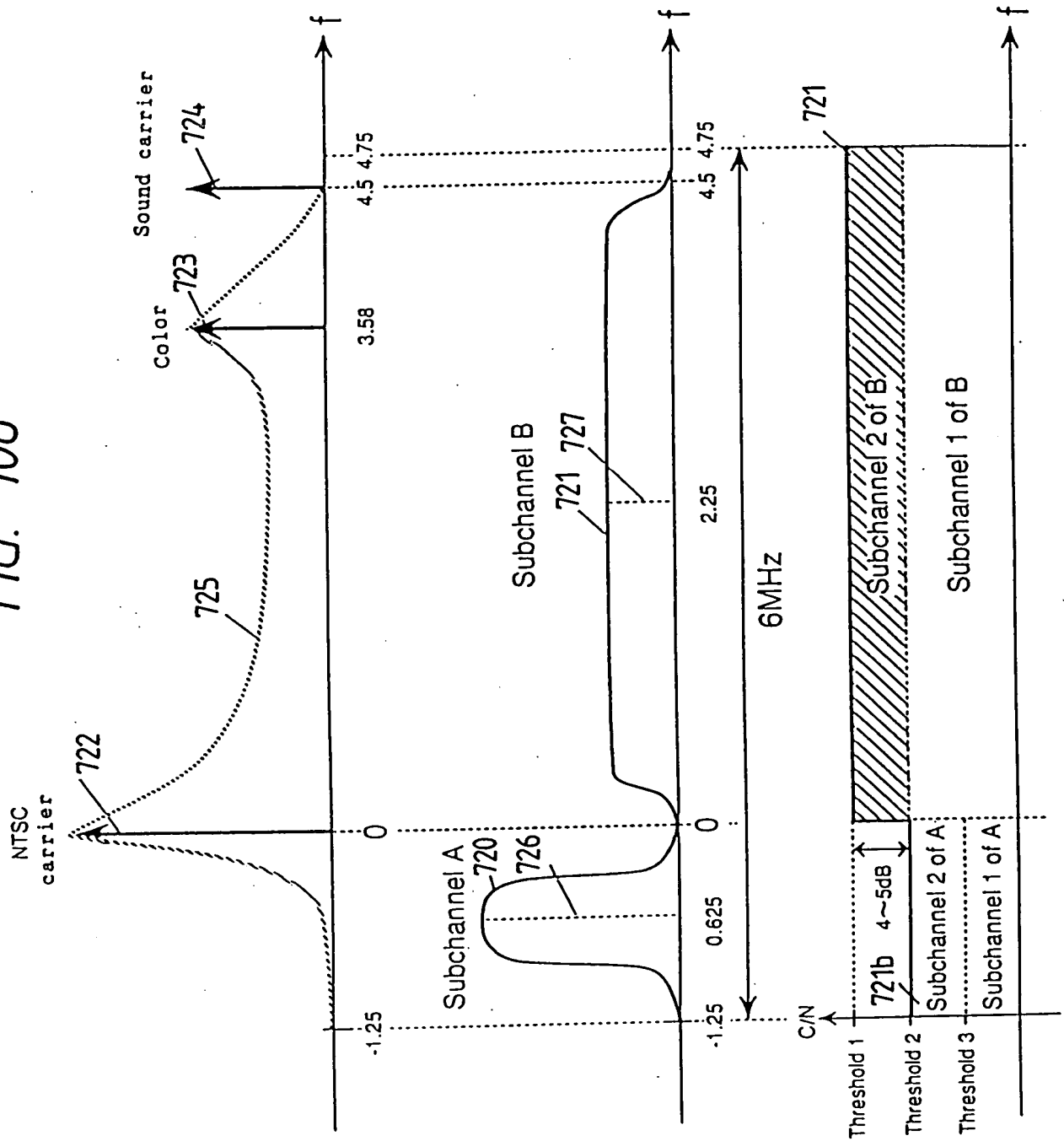


FIG. 109

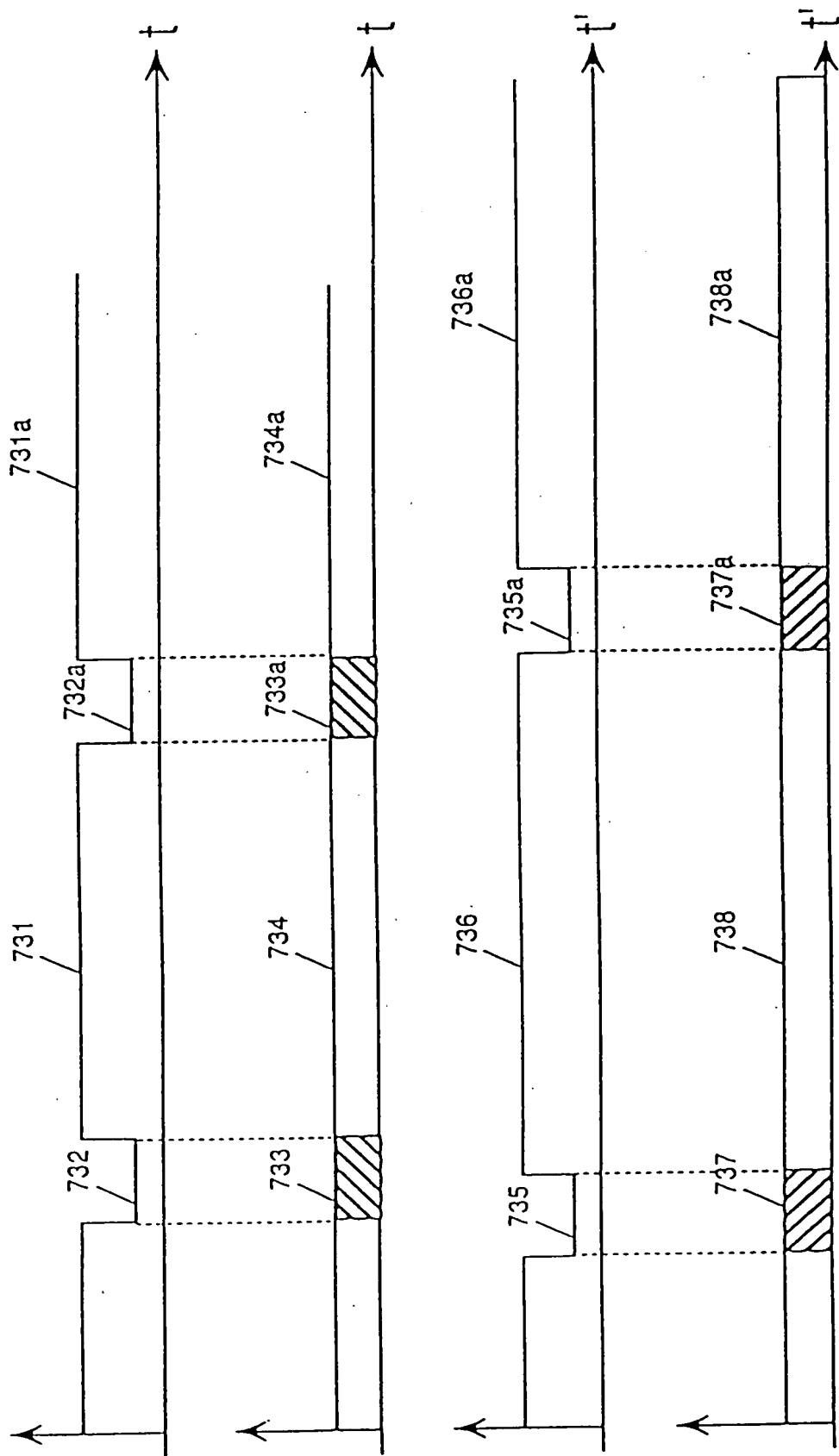


FIG. 110

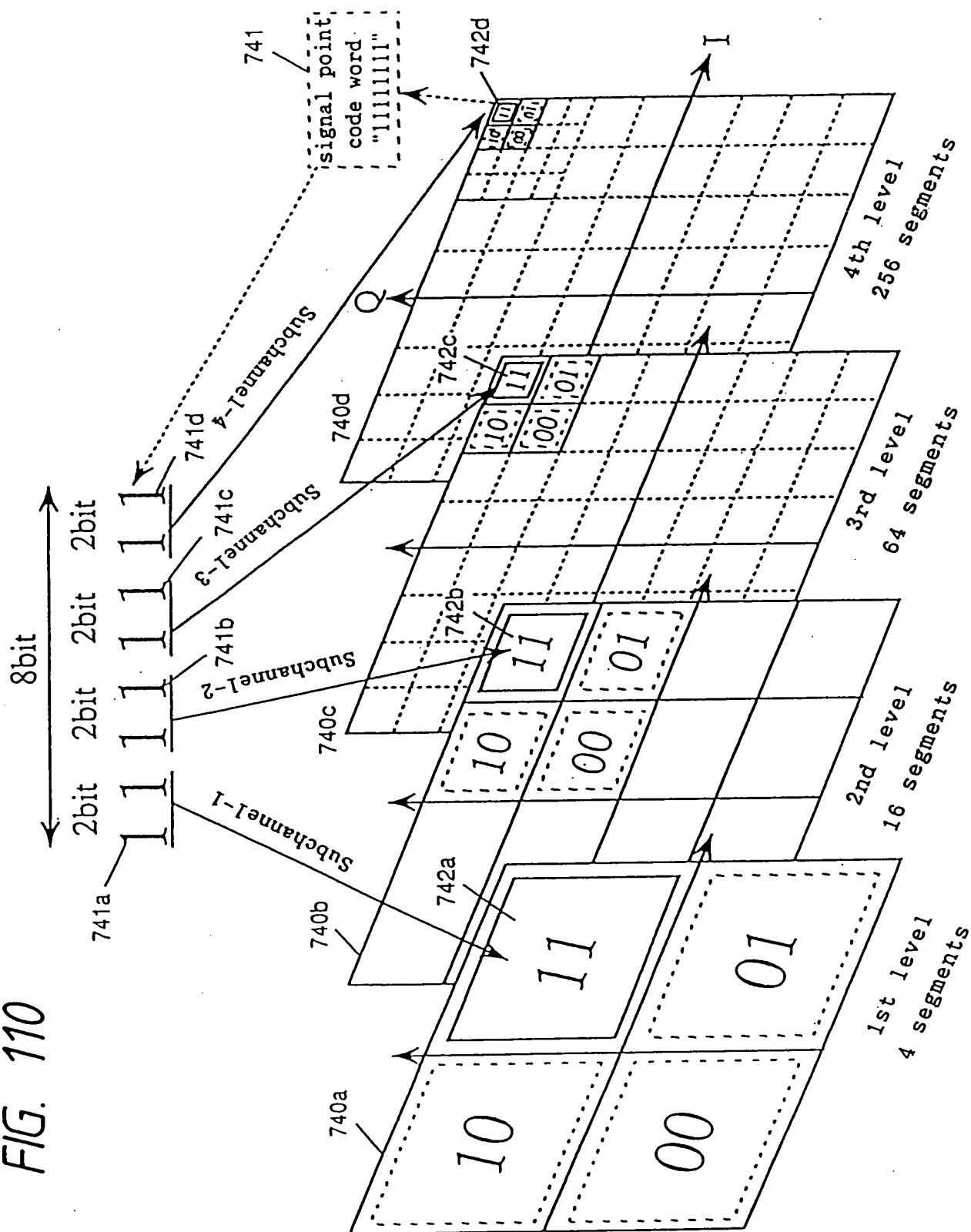
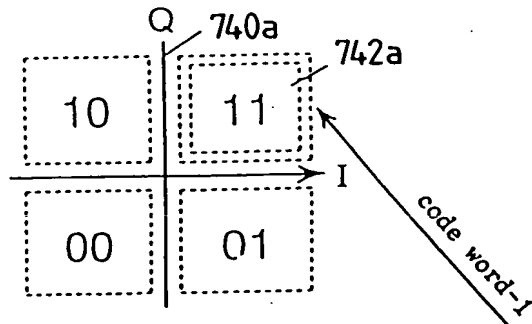
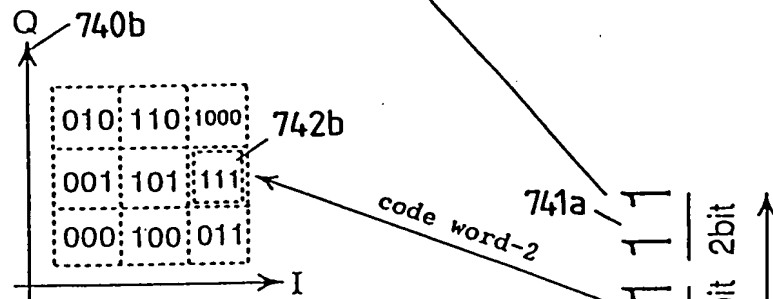


FIG. 111

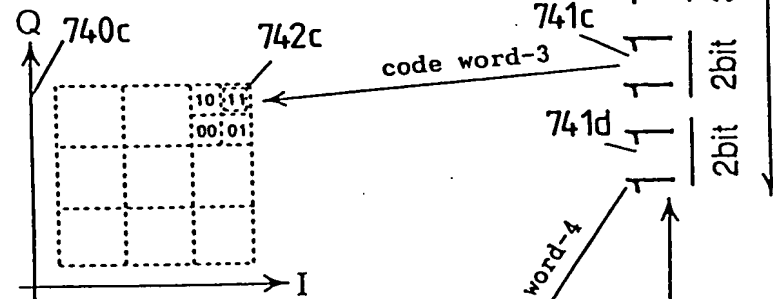
Subchannel-1 (SRQAM:D₁=2bit)



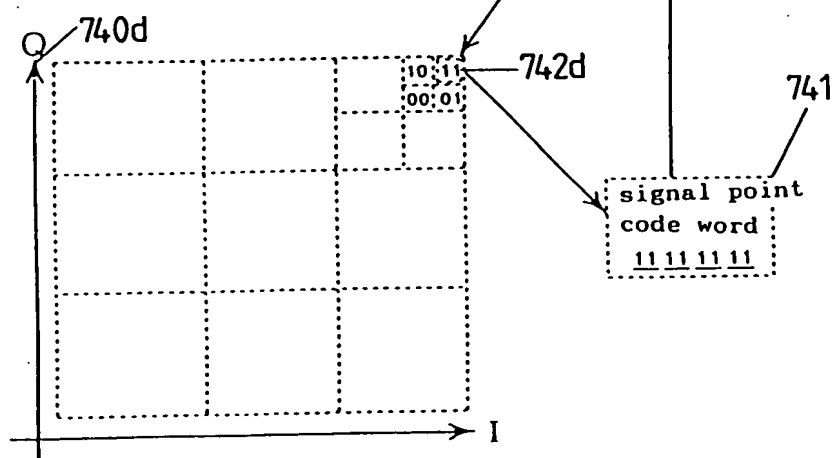
Subchannel-2 (36-SRQAM:D₂=3bit + 1/8bit)



Subchannel-3 (144-SRQAM:D₃=2bit)

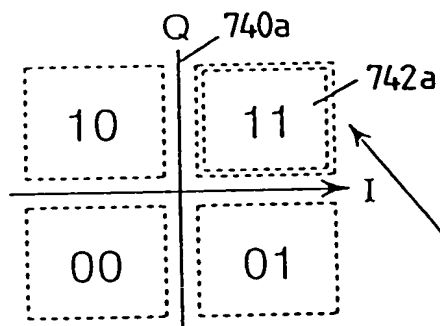


Subchannel-4 (576-SRQAM:D₄=2bit)

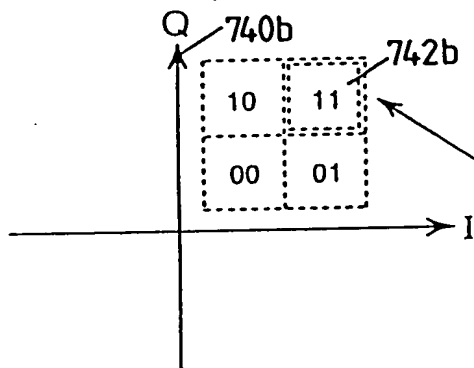


Subchannel-1 (SRQAM:D₁=2bit)

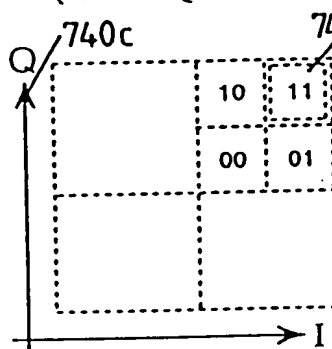
FIG. 112



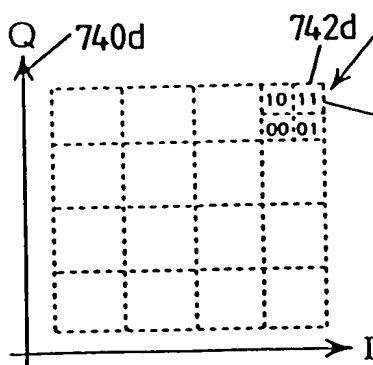
Subchannel-2 (16-SRQAM:D₂=2bit)



Subchannel-3 (64-SRQAM:D₃=2bit)



Subchannel-4 (256-SRQAM:D₄=2bit)



code word-1

code word-2

code word-3

code word-4

741a

741b

741c

741d

2bit

2bit

2bit

2bit

8bit

signal point
code word
11 11 11 11

FIG. 113

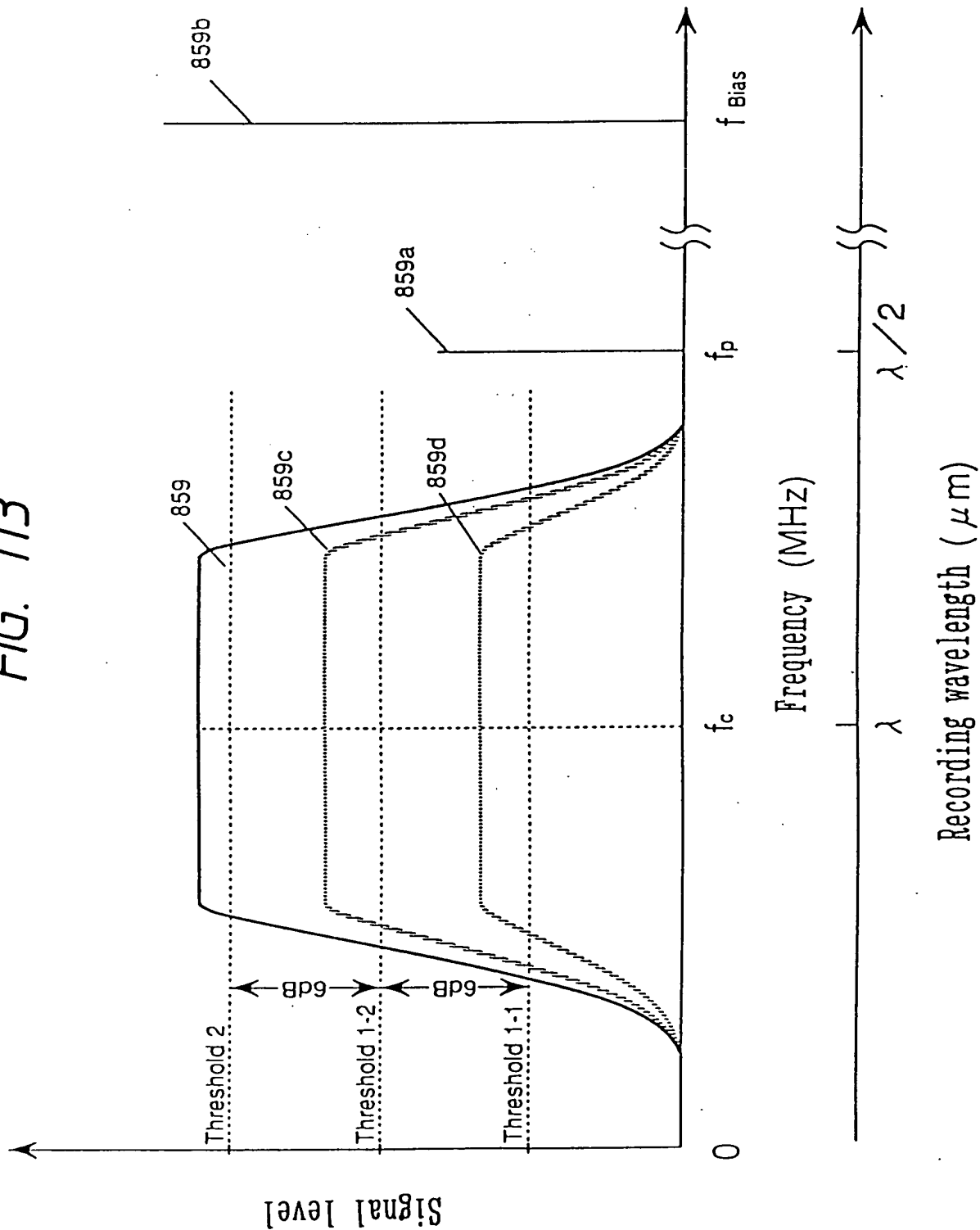


FIG. 114

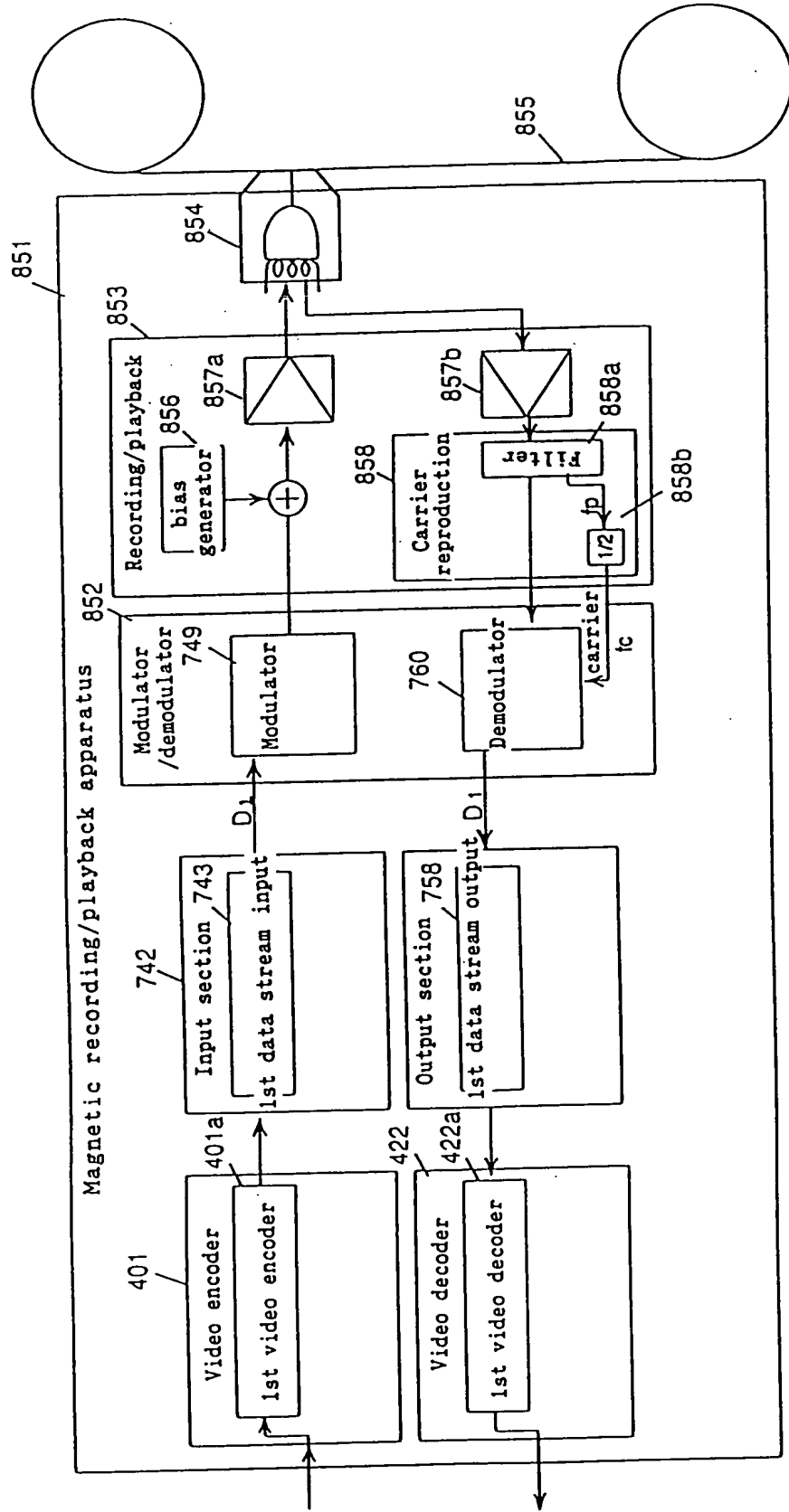


FIG. 115

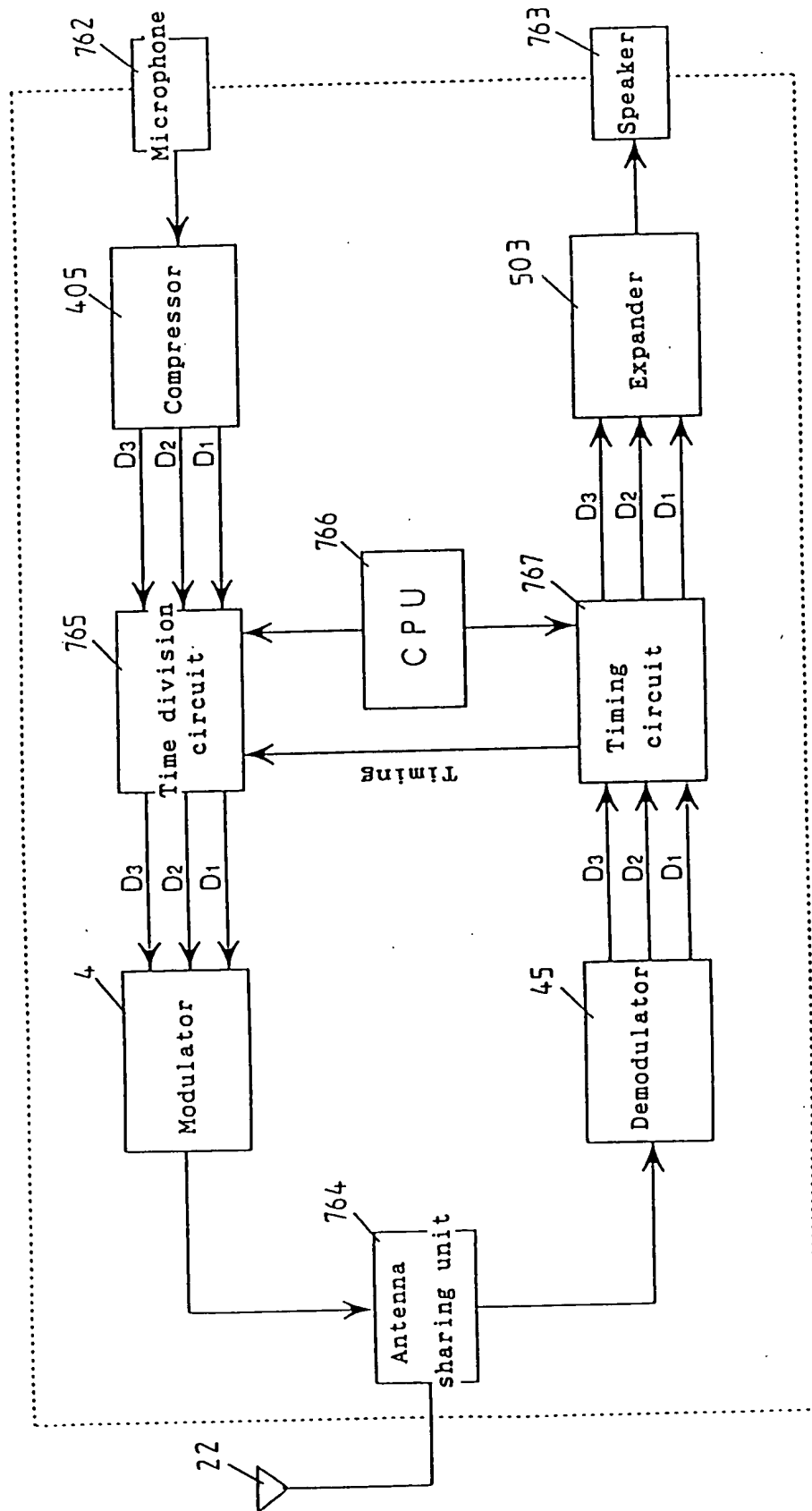
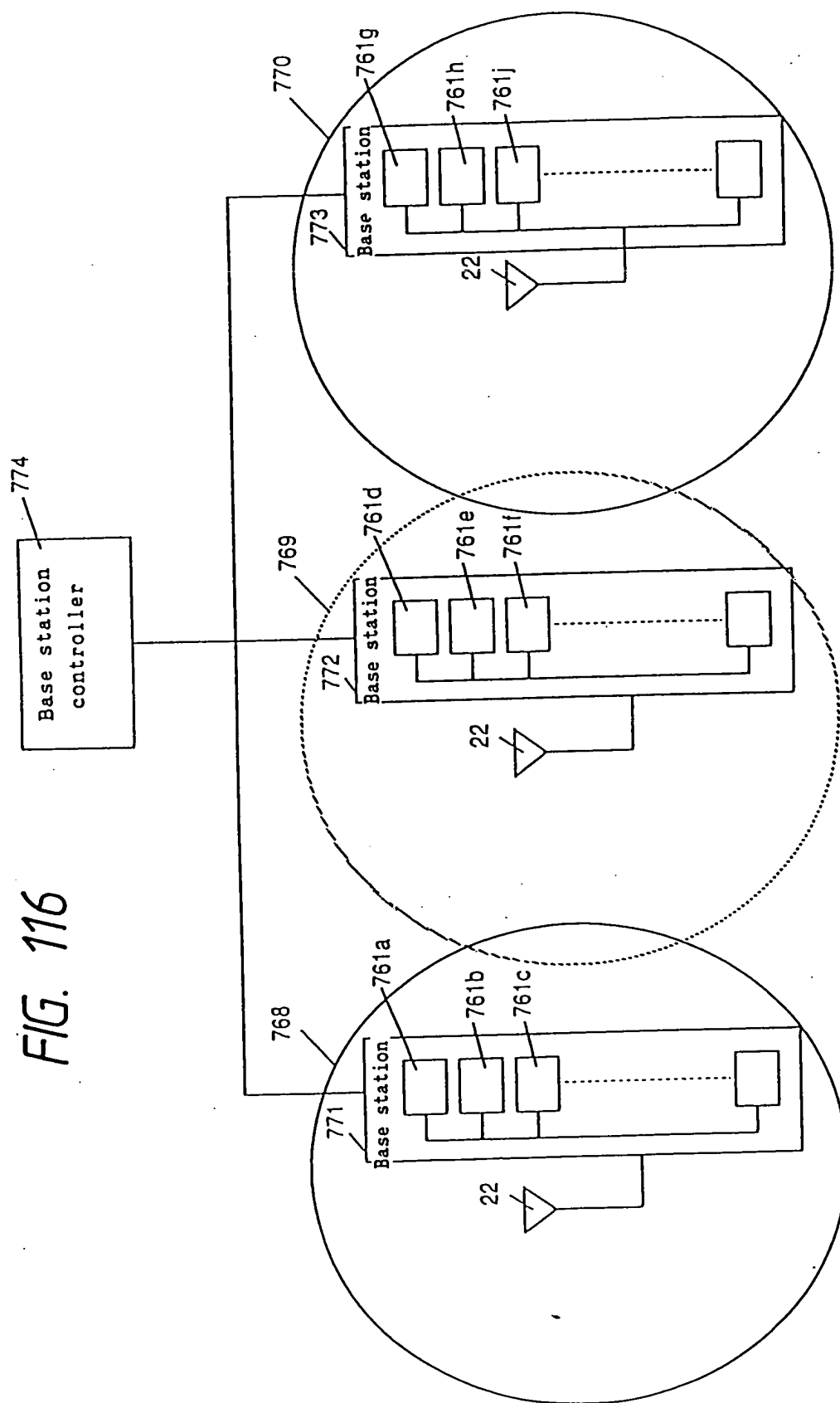


FIG. 116



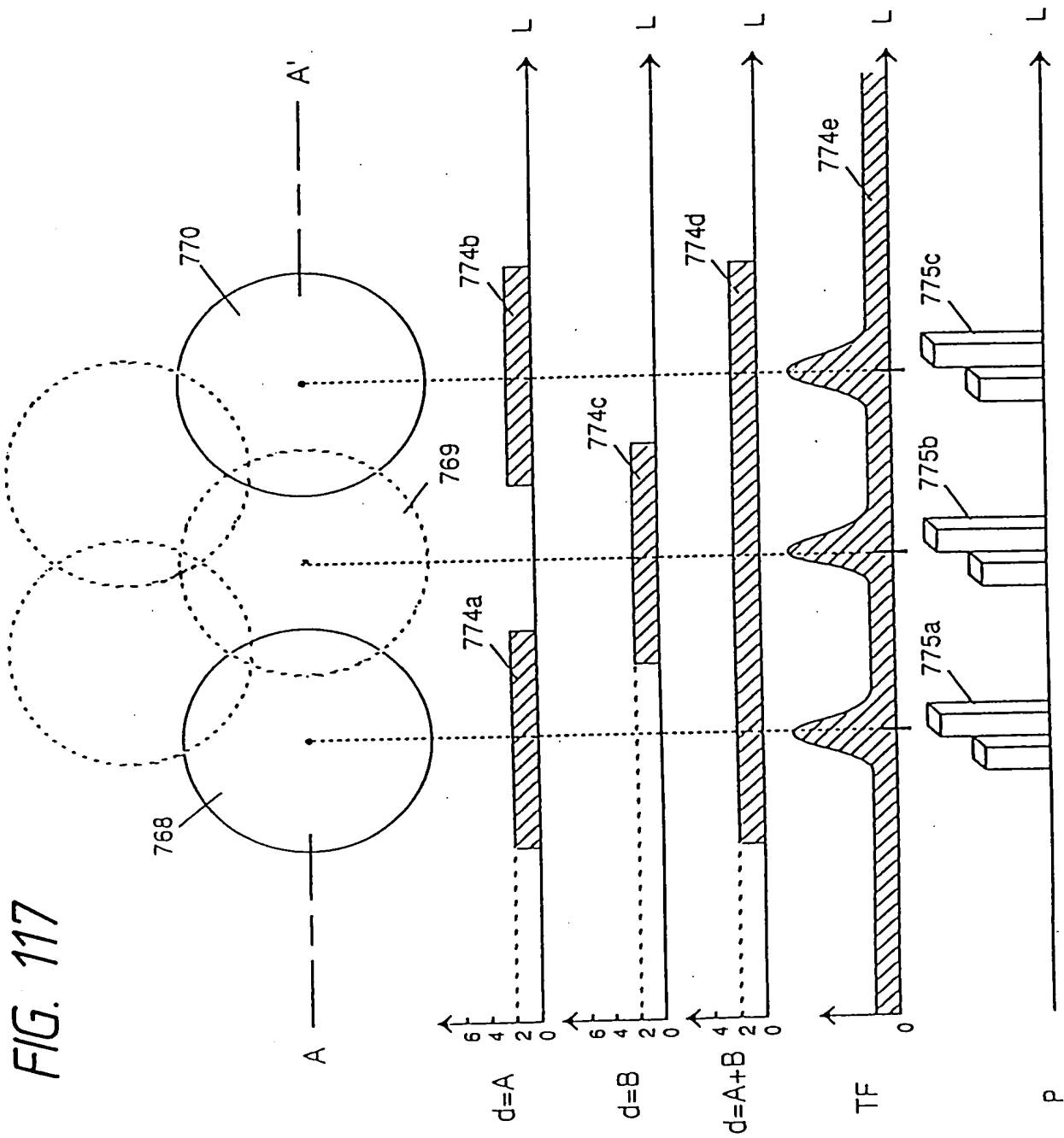


FIG. 118

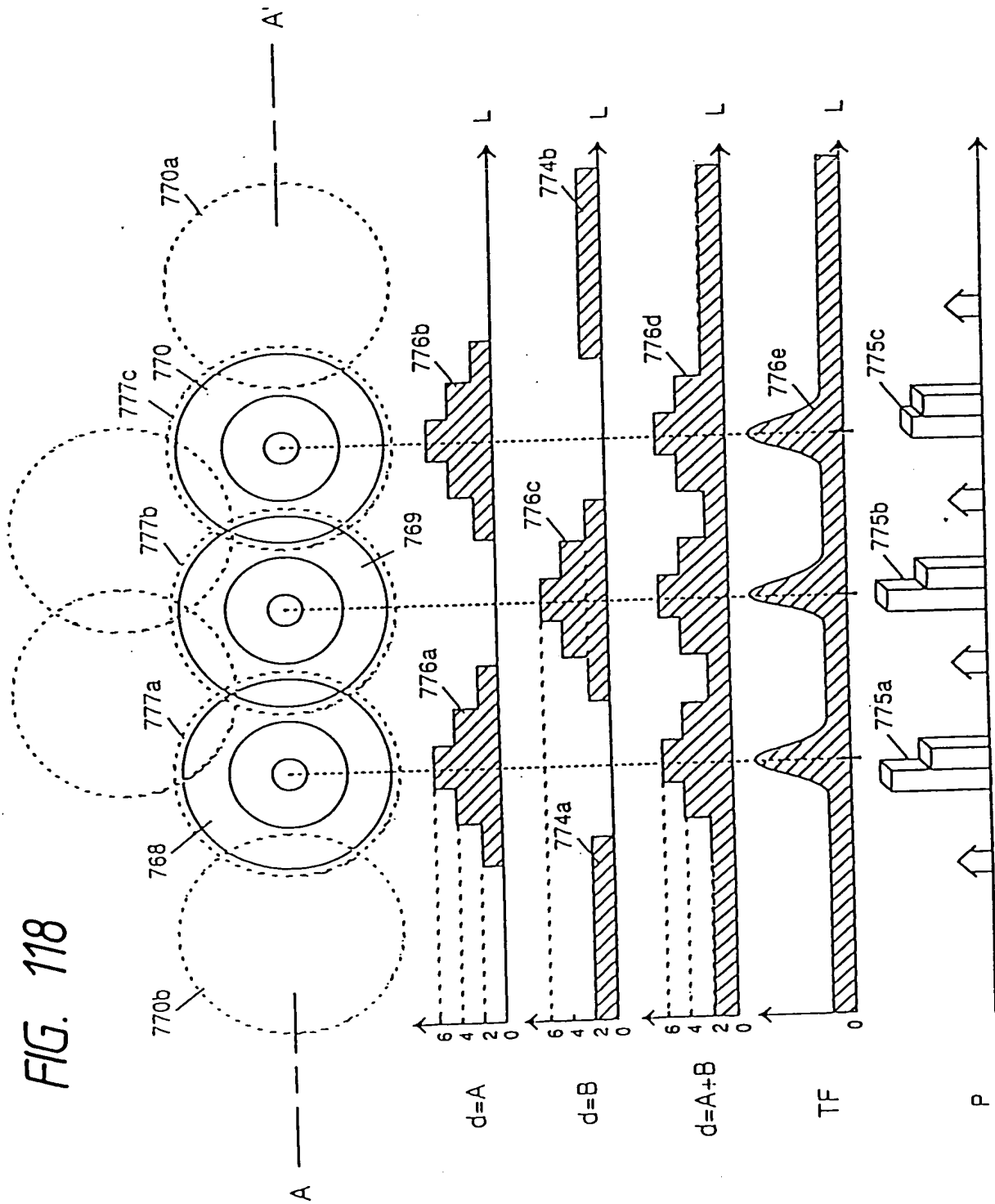
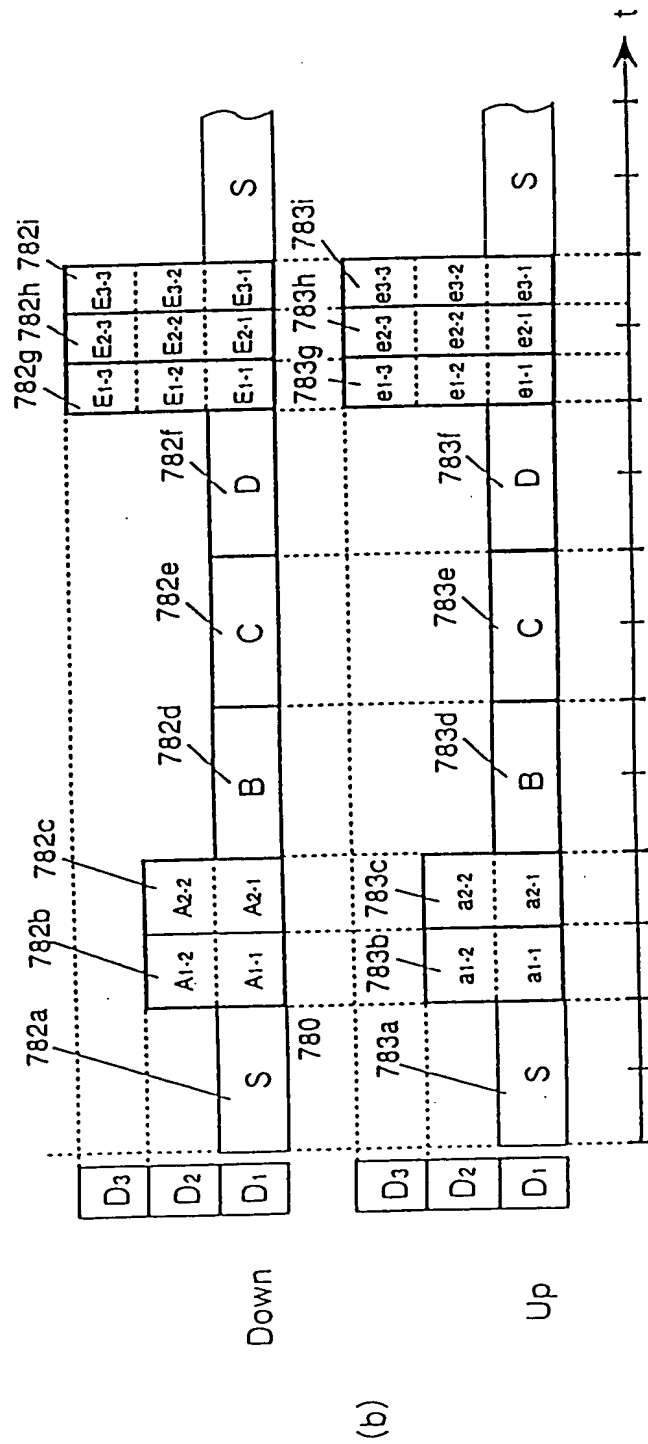
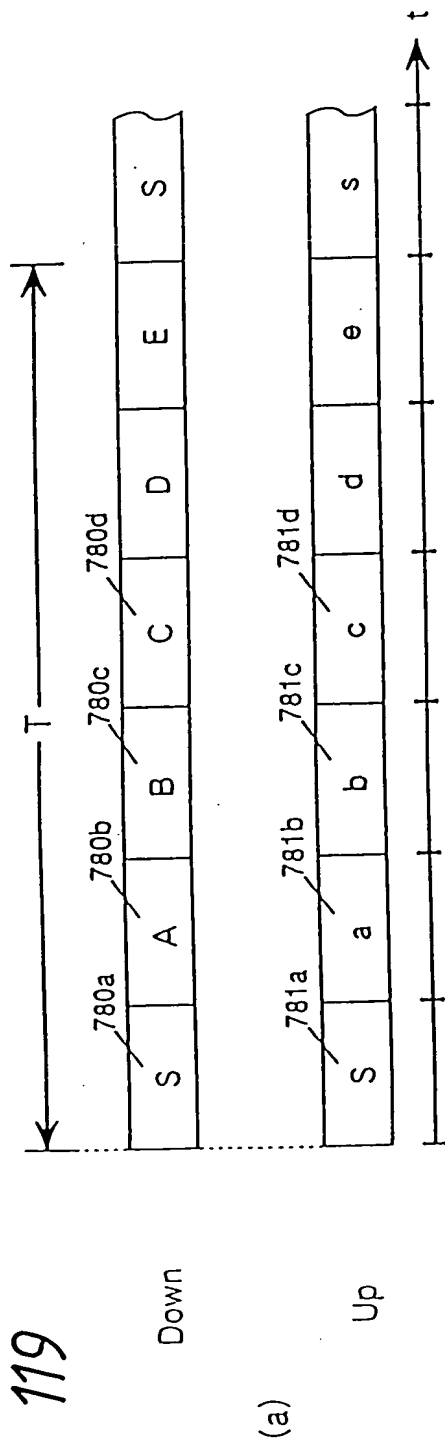


FIG. 119



1. The first step in the process is to identify the problem. This involves gathering information about the situation and the people involved.



FIG. 121

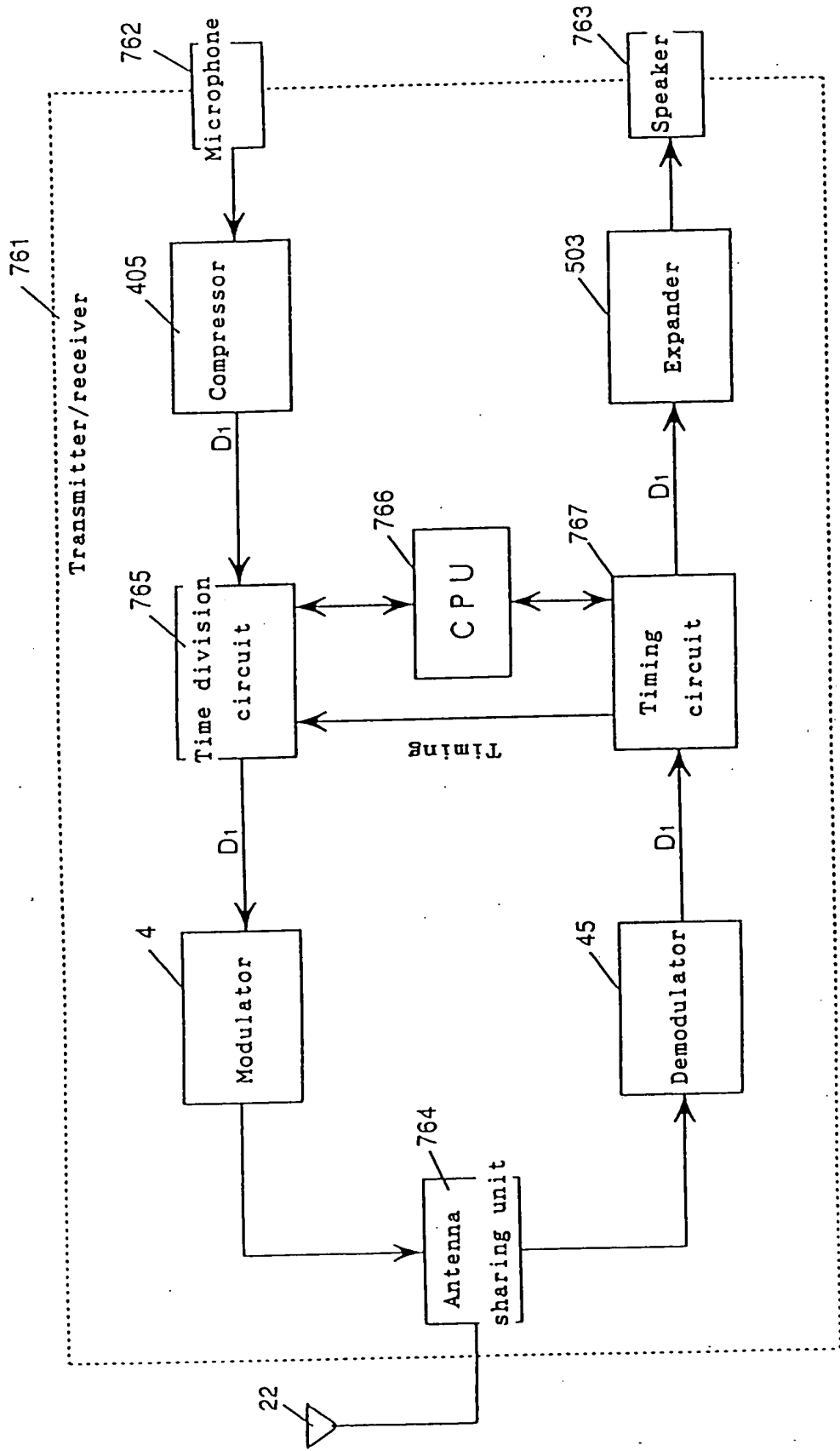


FIG. 122

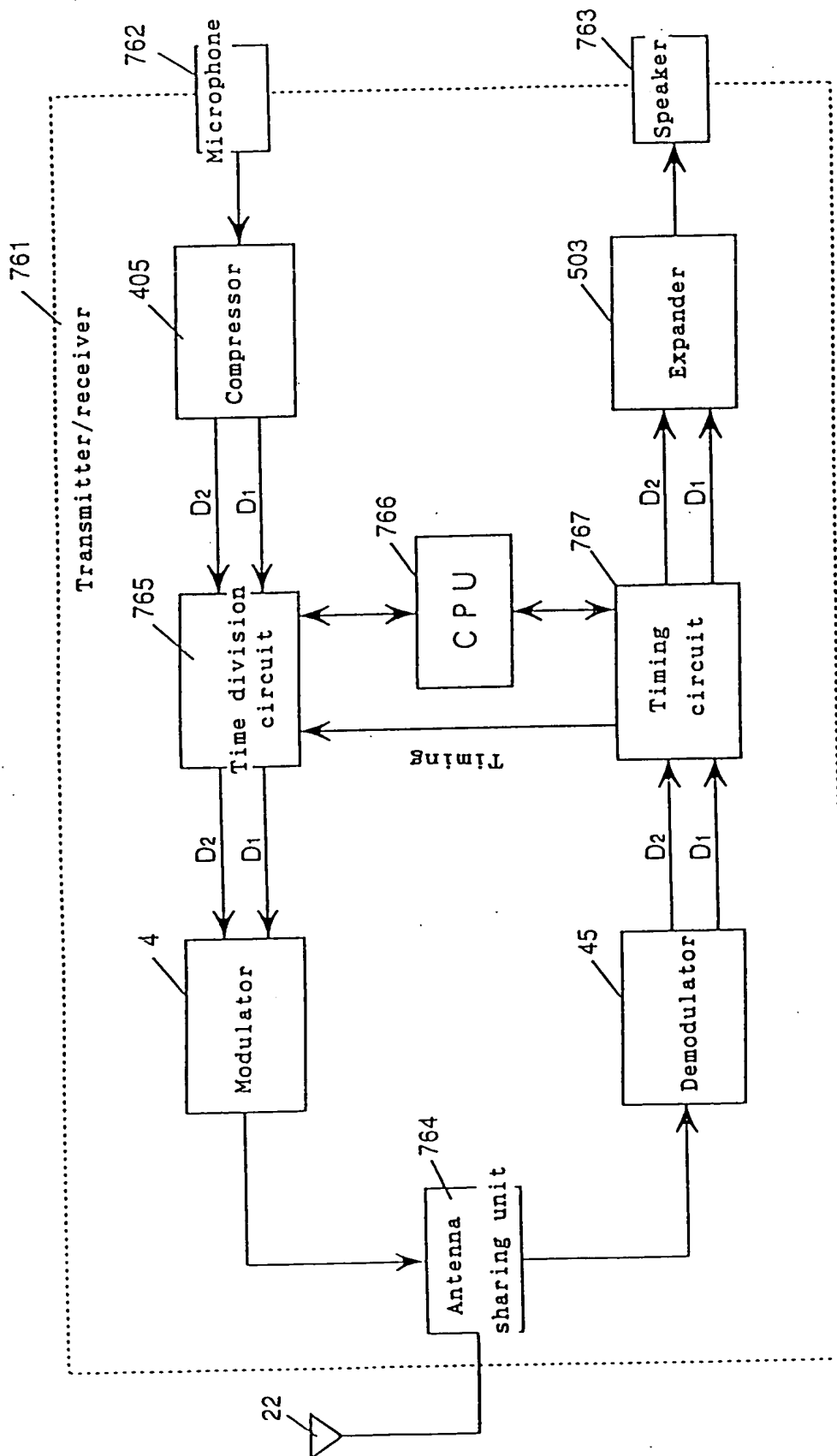
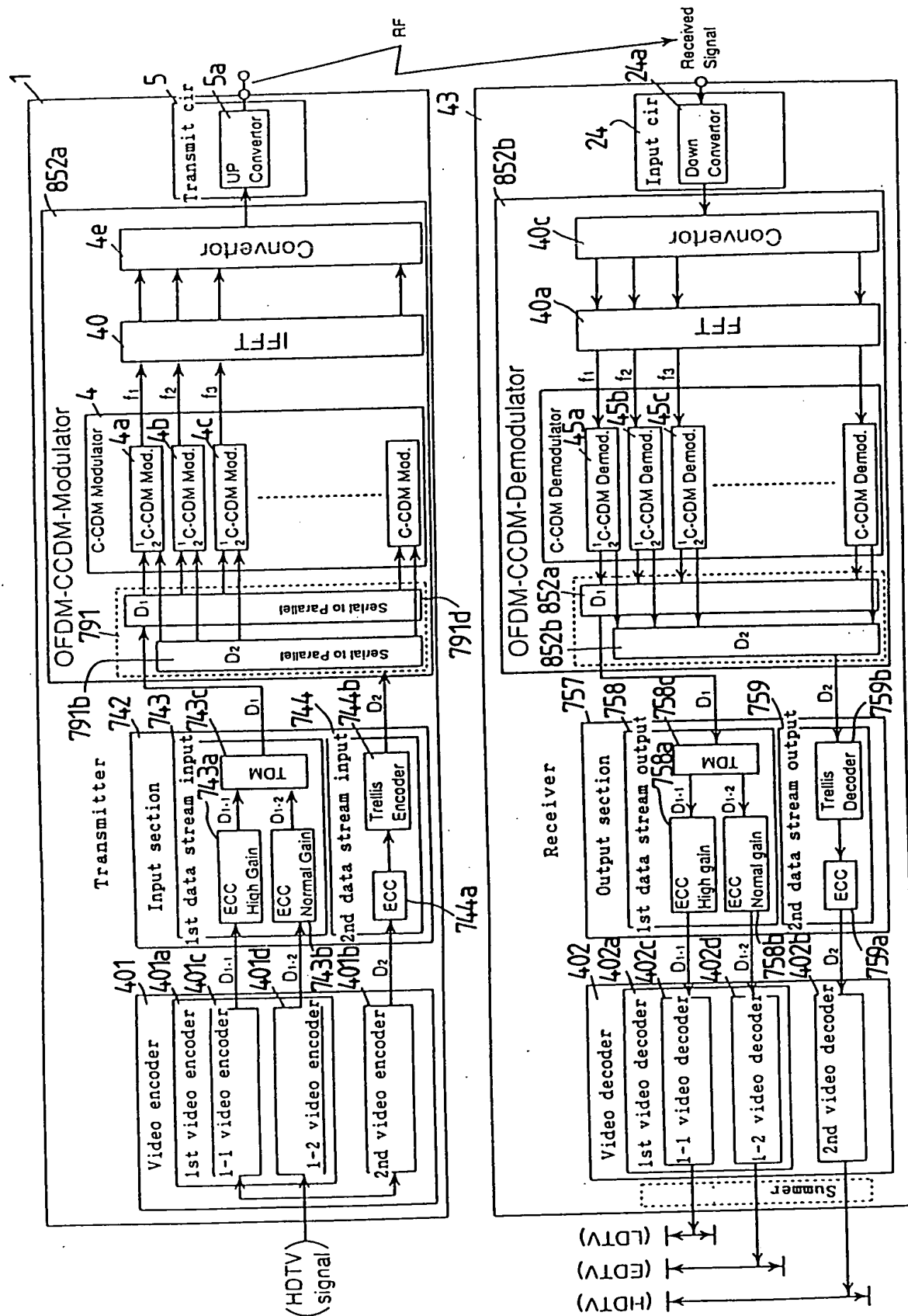


FIG. 123



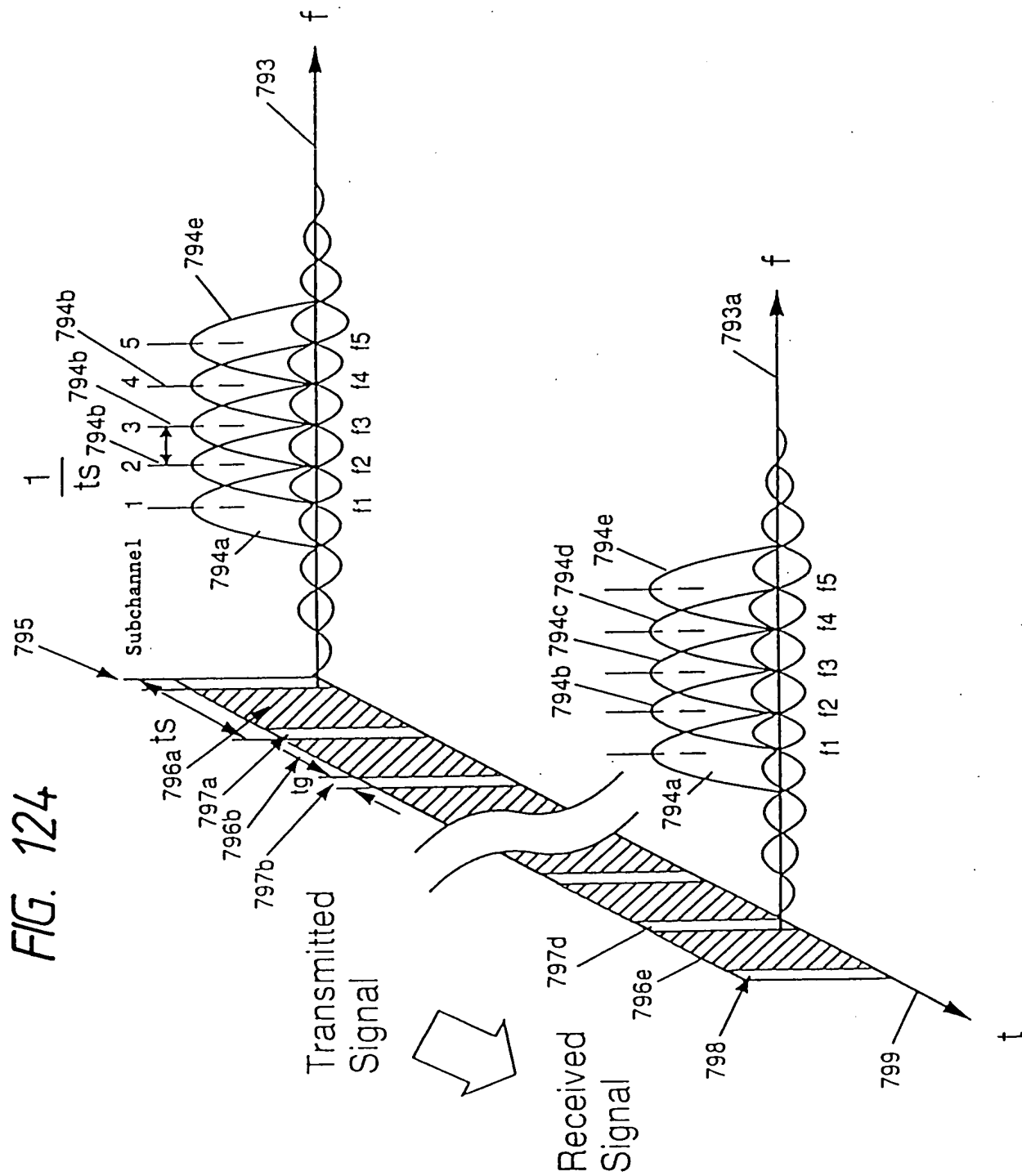


FIG. 125

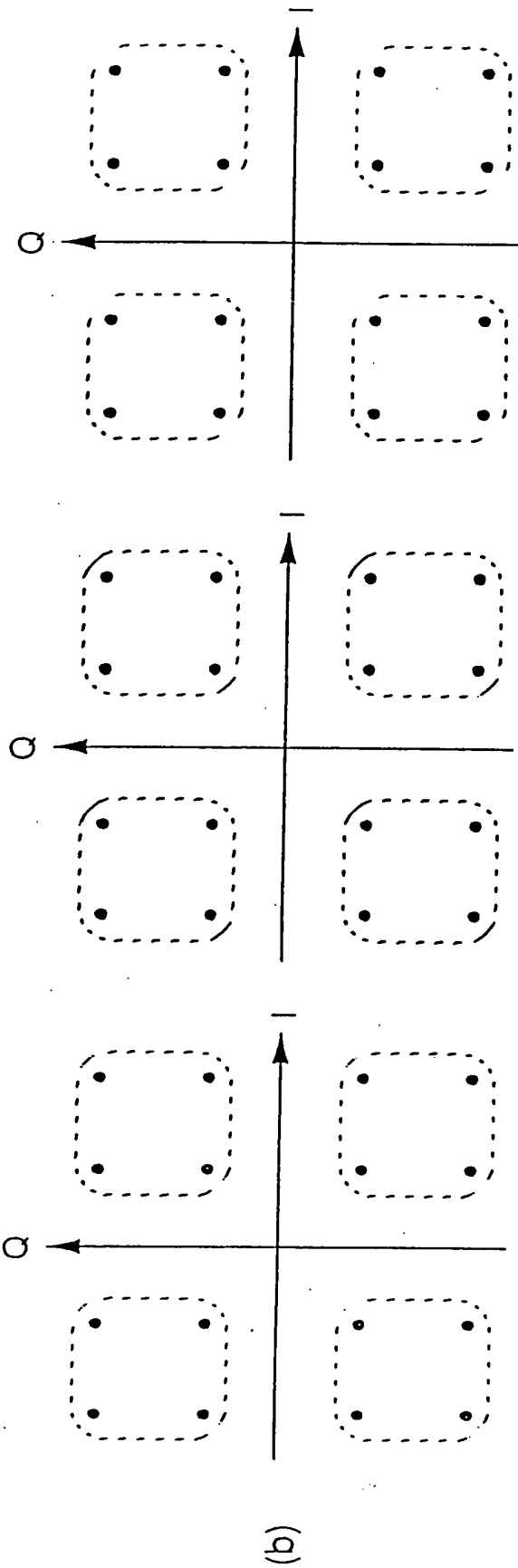
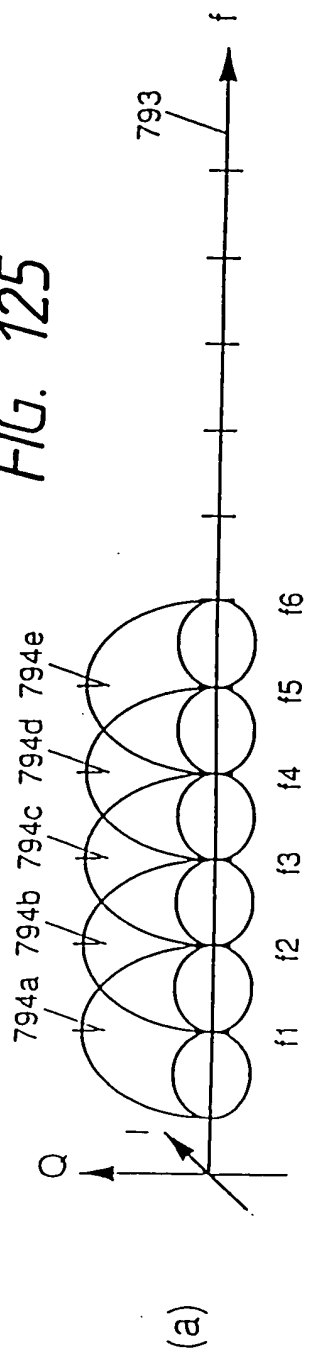


FIG. 126

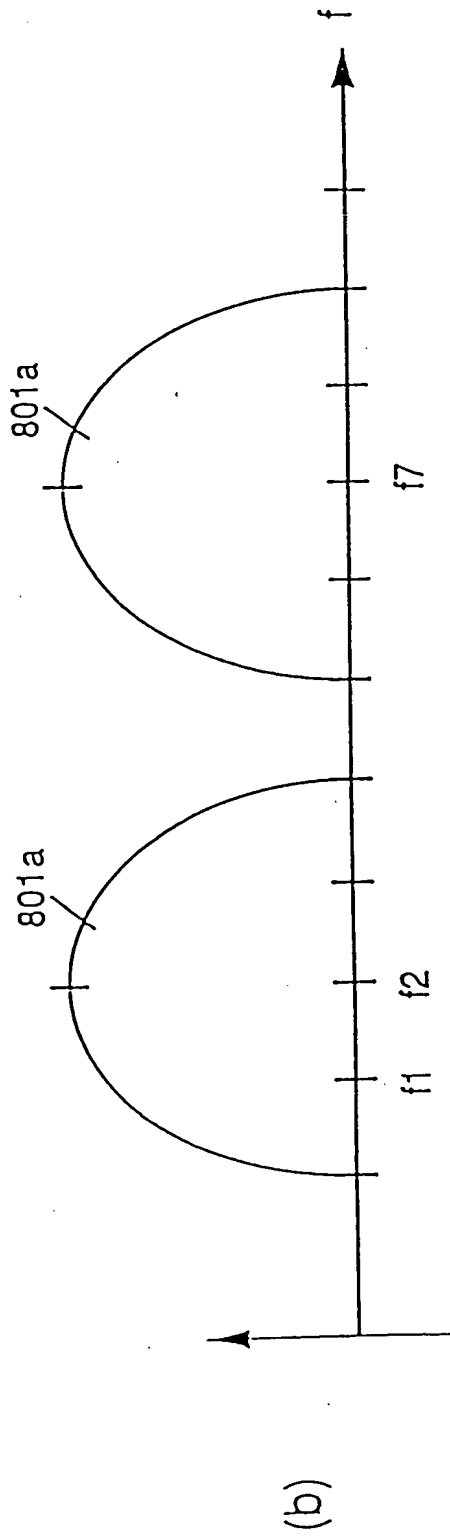
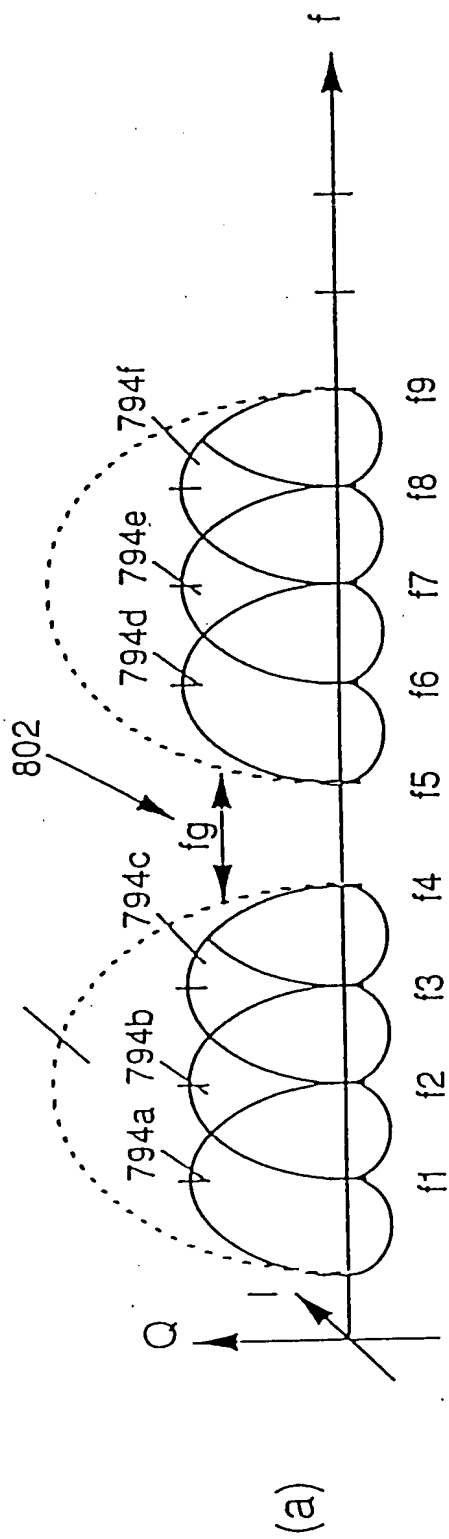


FIG. 128

Trellis Encoder
(Ratio:3/4)

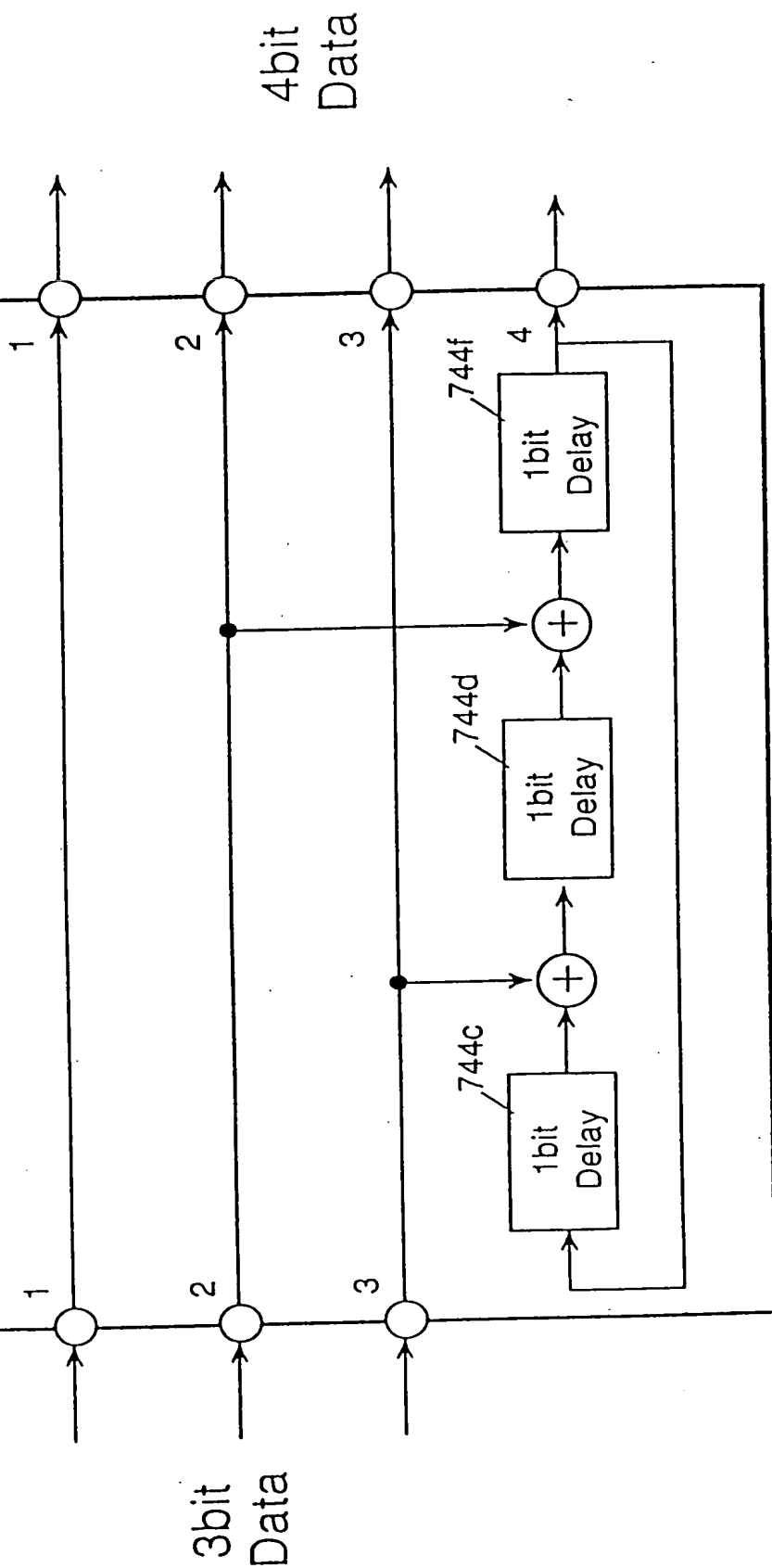


FIG. 129

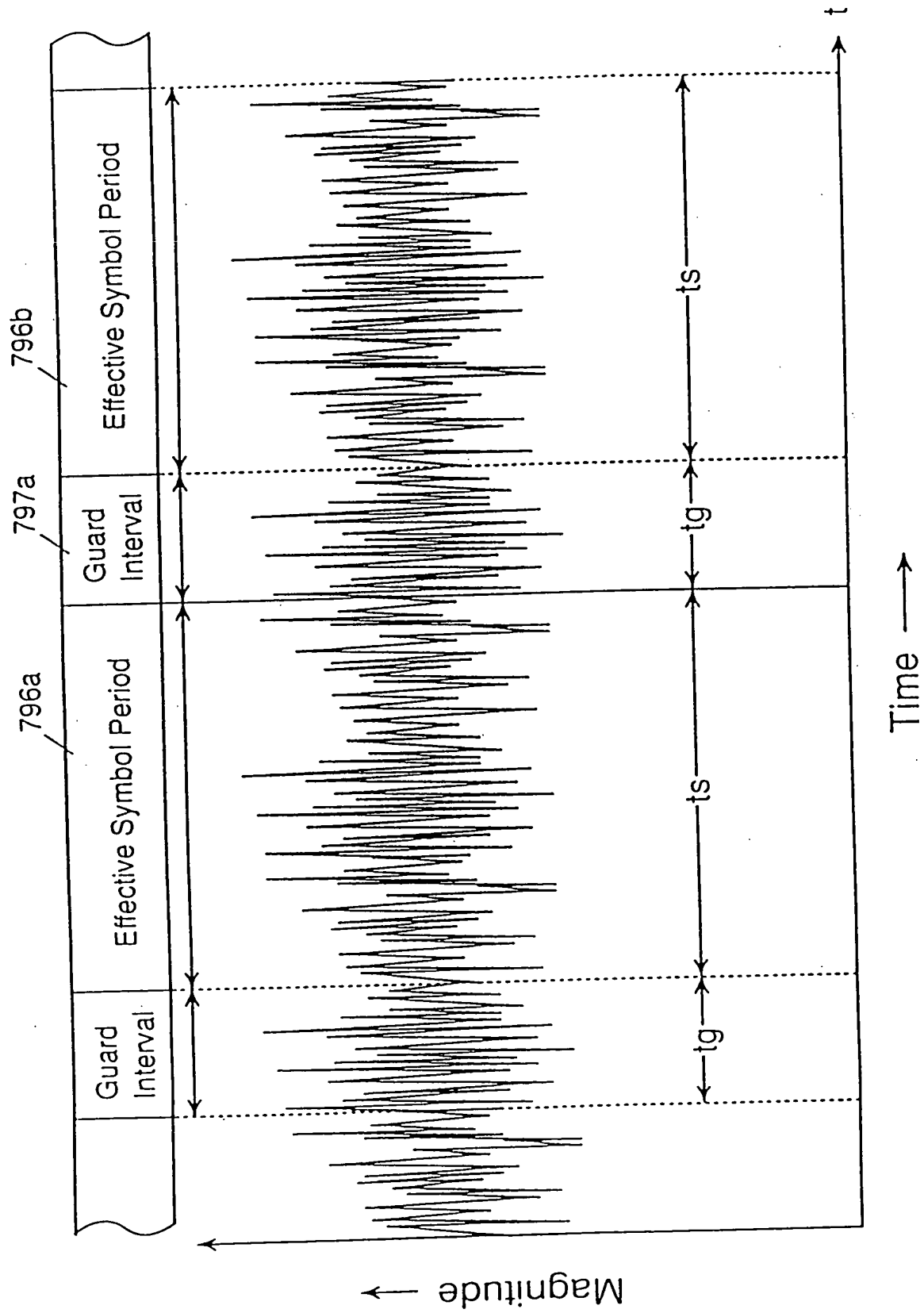


FIG. 130

GHOST DELAY=2us. D/U=8dB
Figure 8 Bit Error Rate Performance Under Single Ghost
and Gaussian Noise (1)

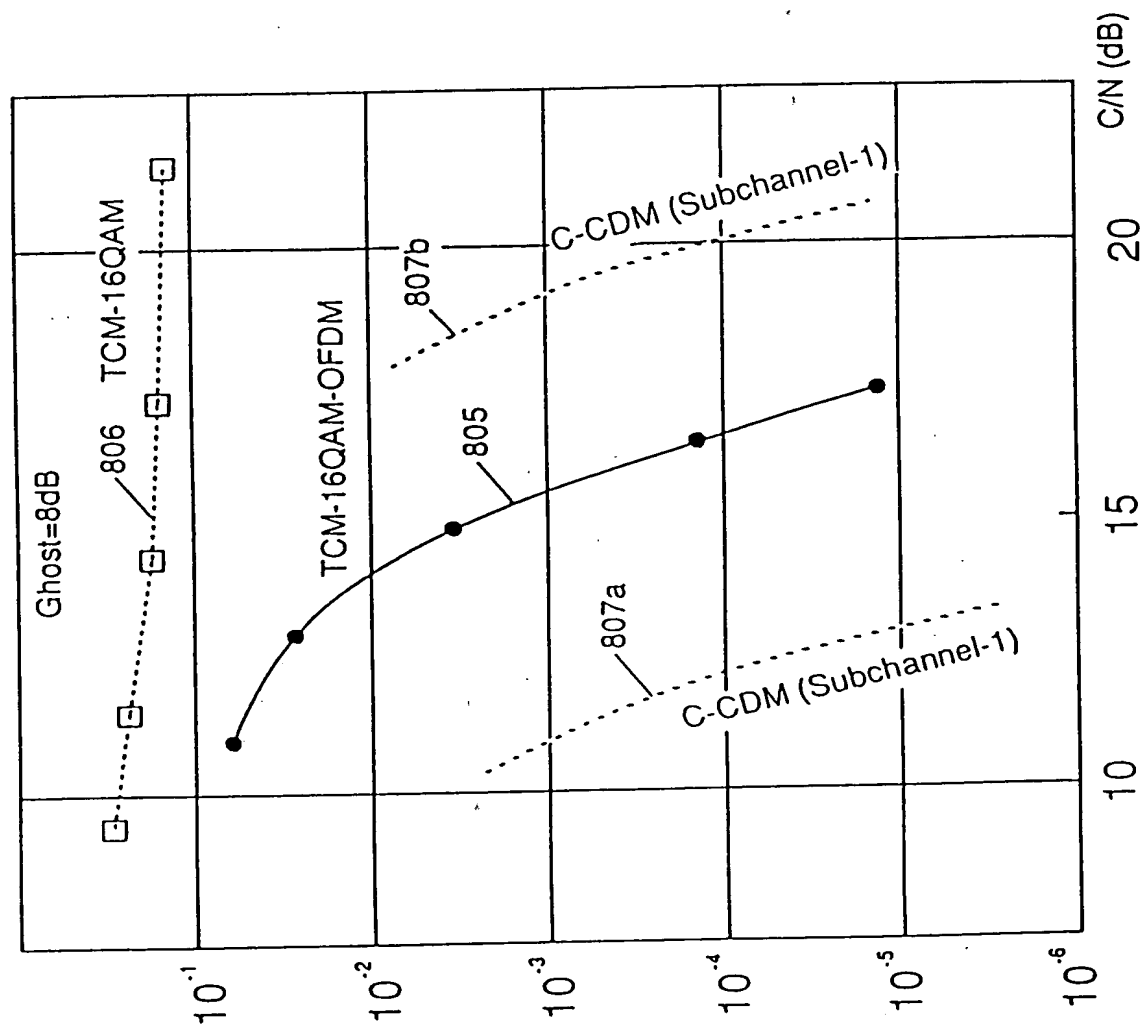


FIG. 131

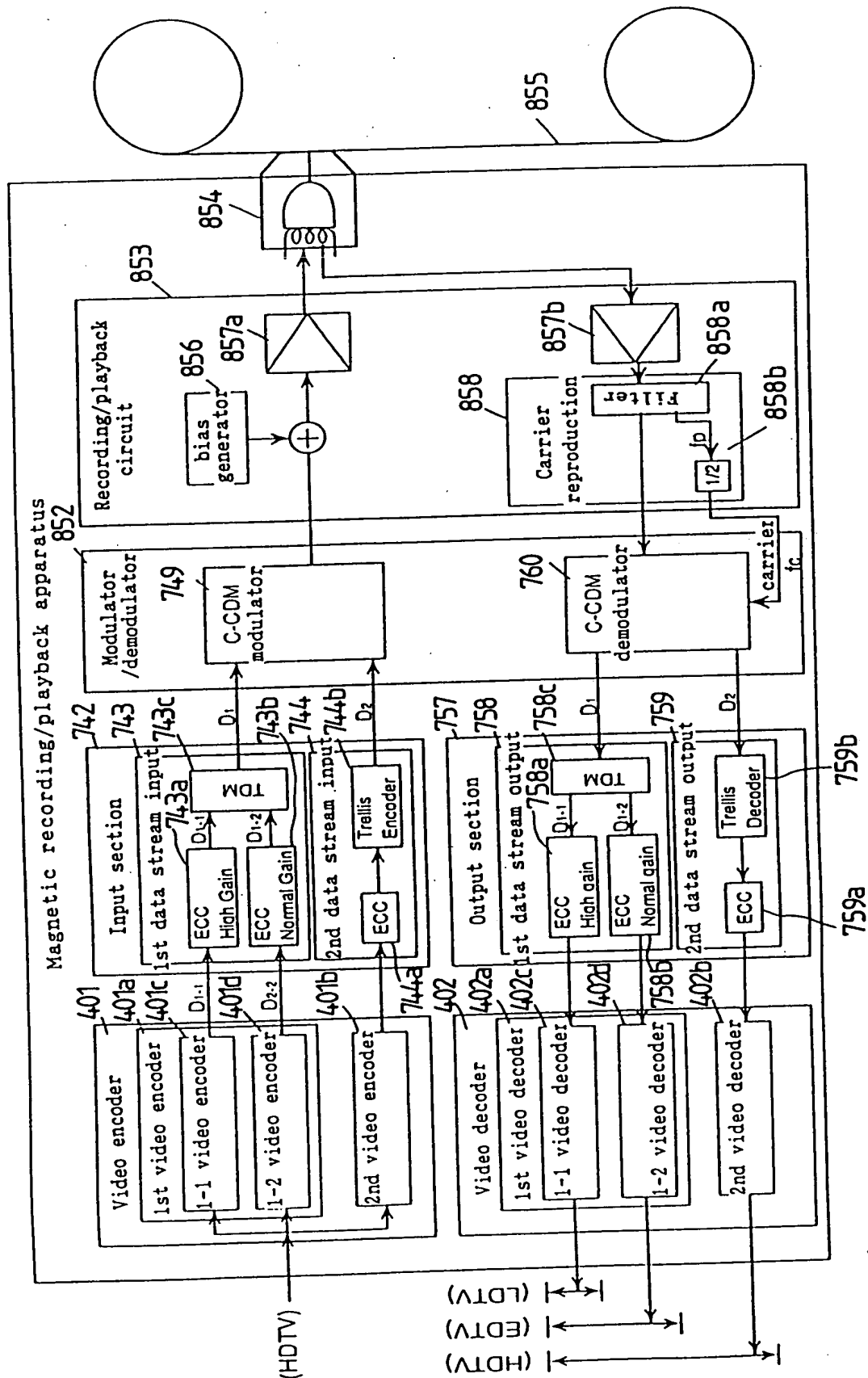


FIG. 132

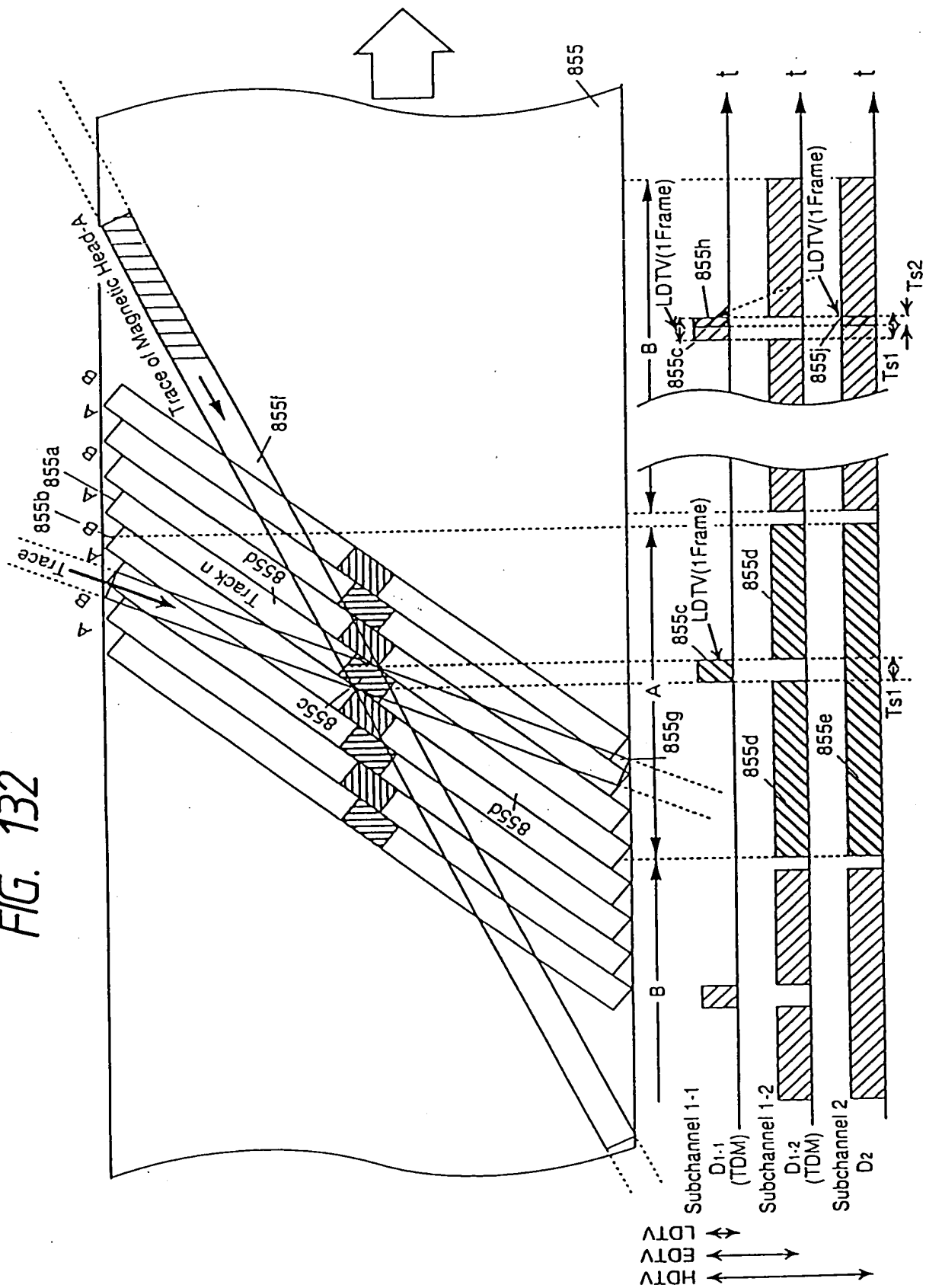


FIG. 133

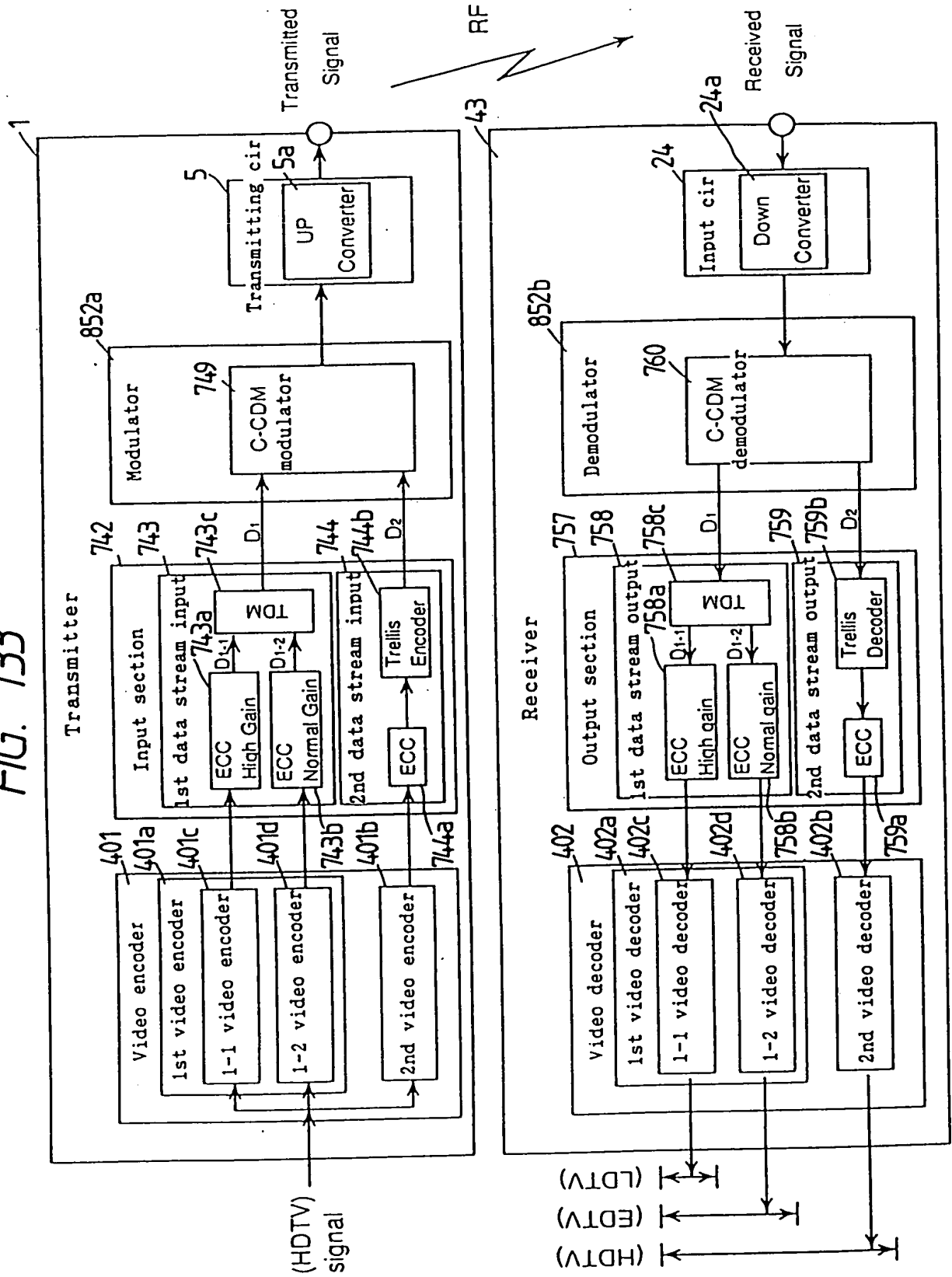


FIG. 134

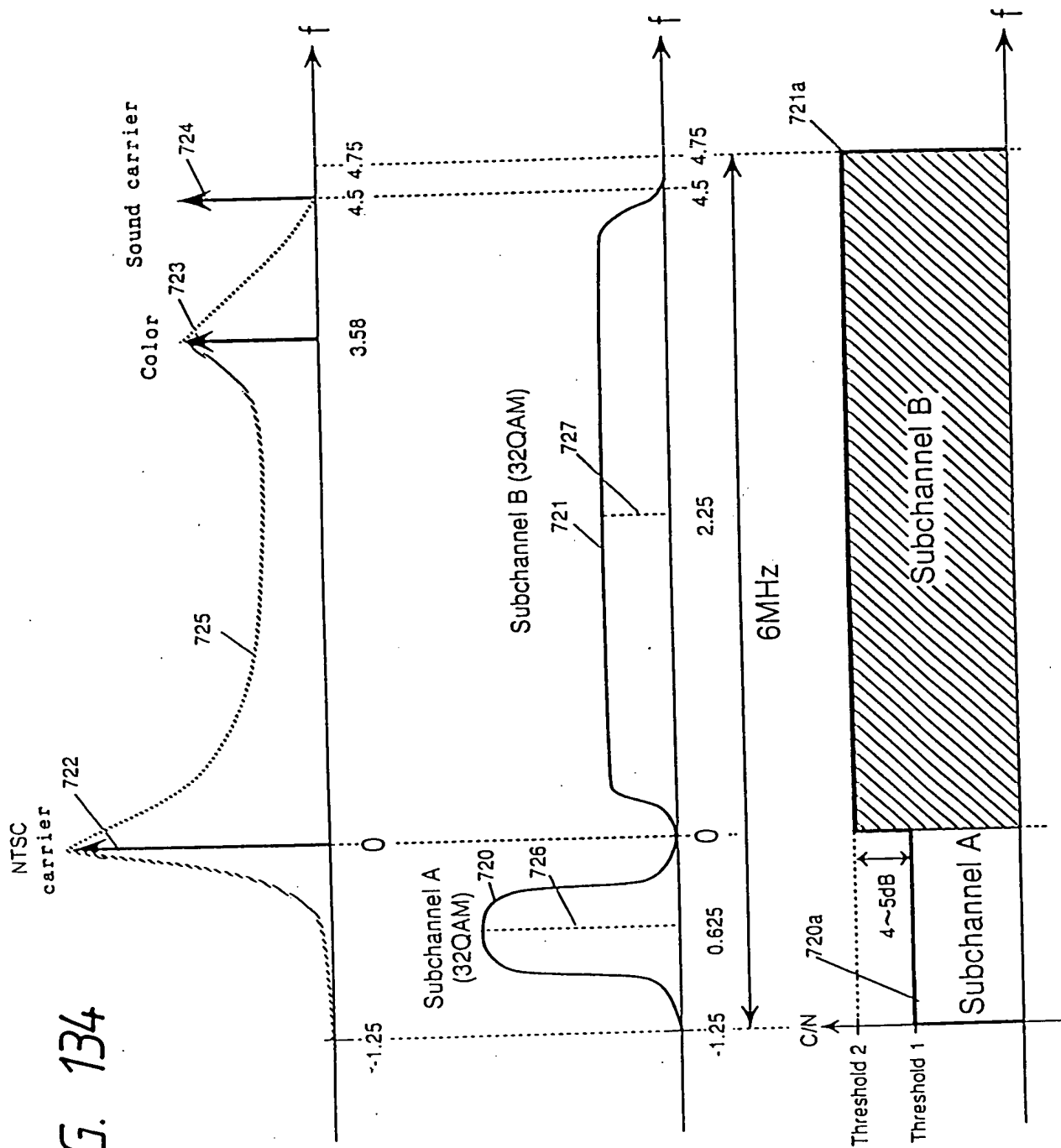


FIG. 135

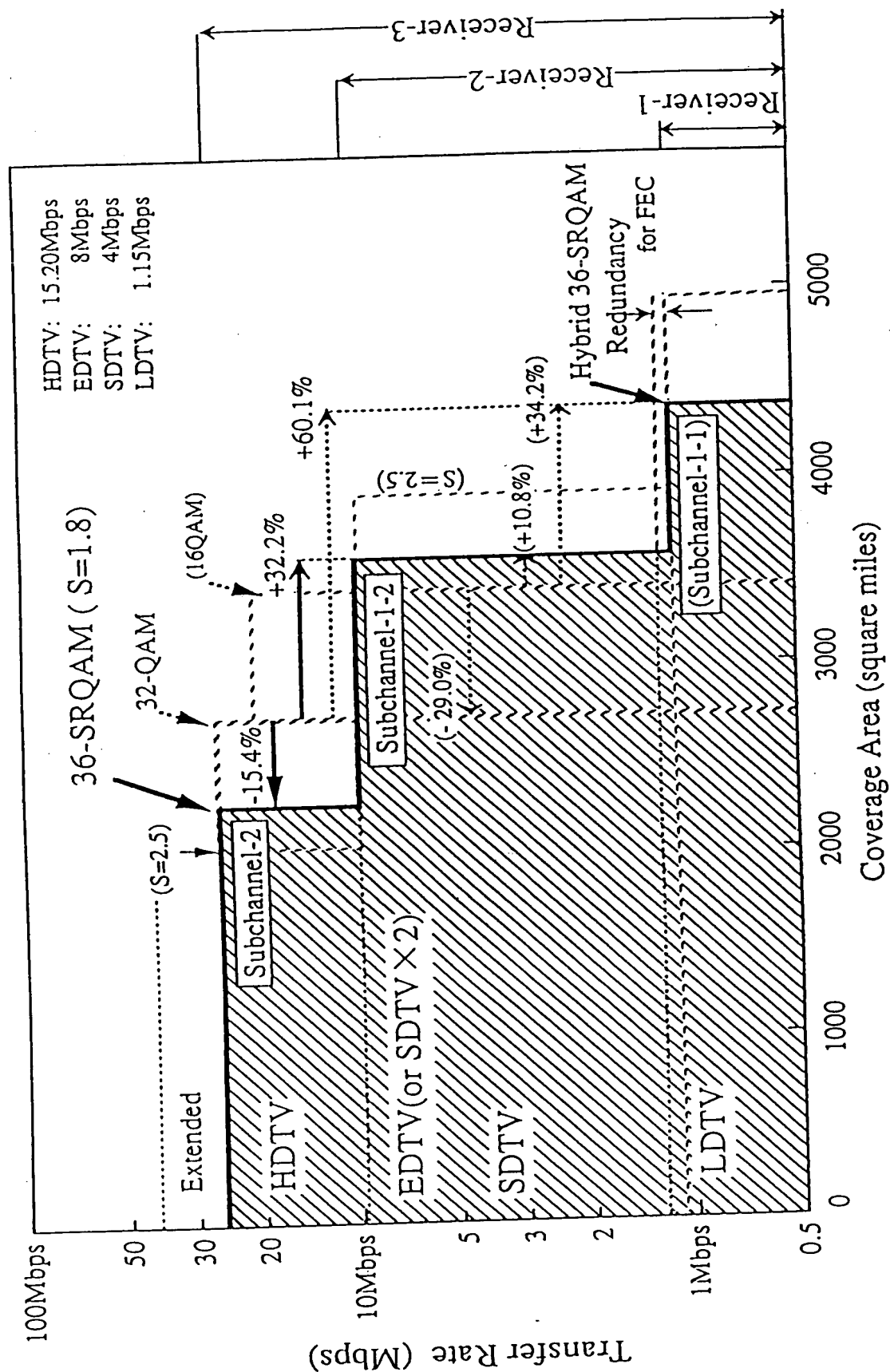


FIG. 136

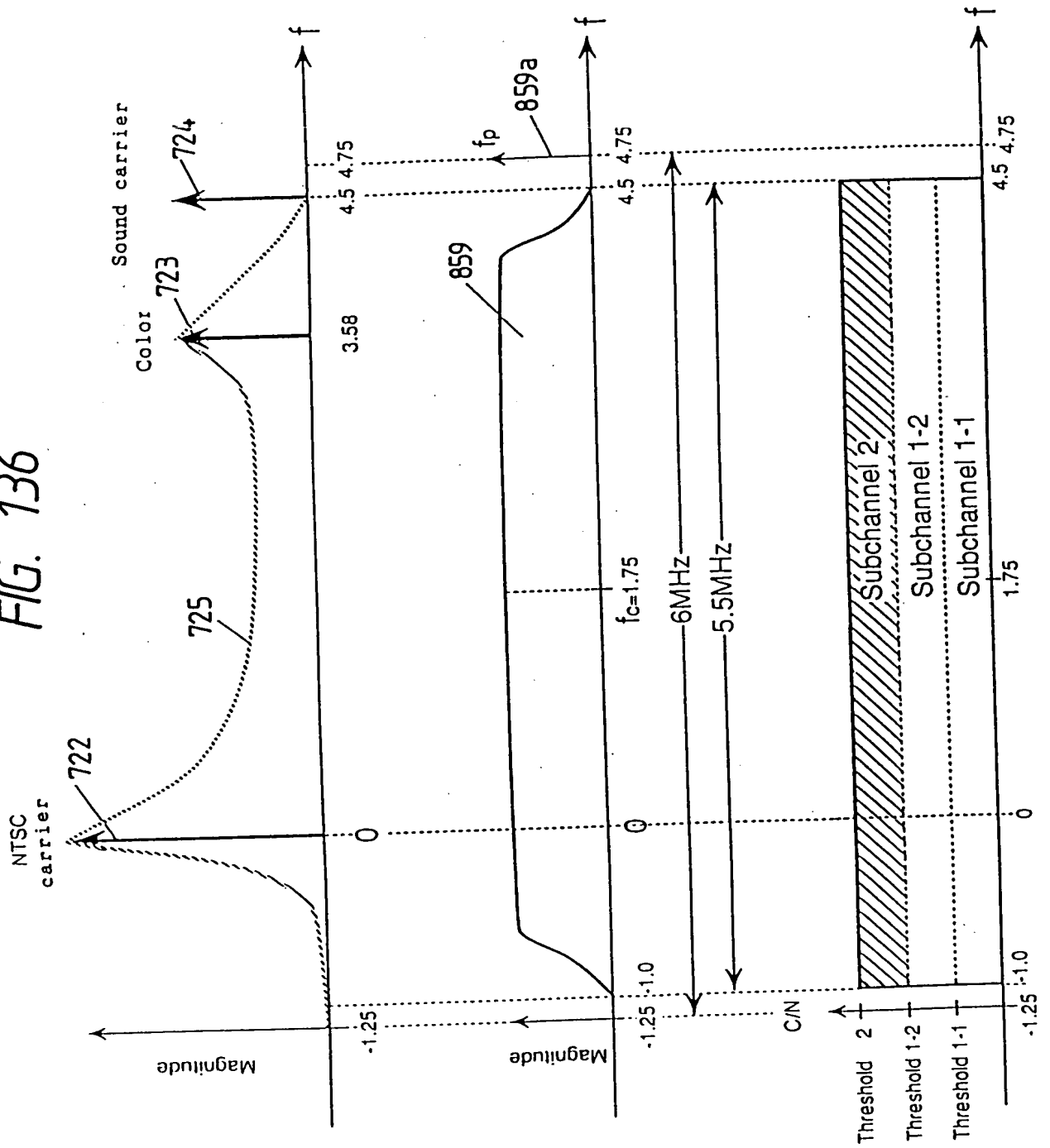


FIG. 137

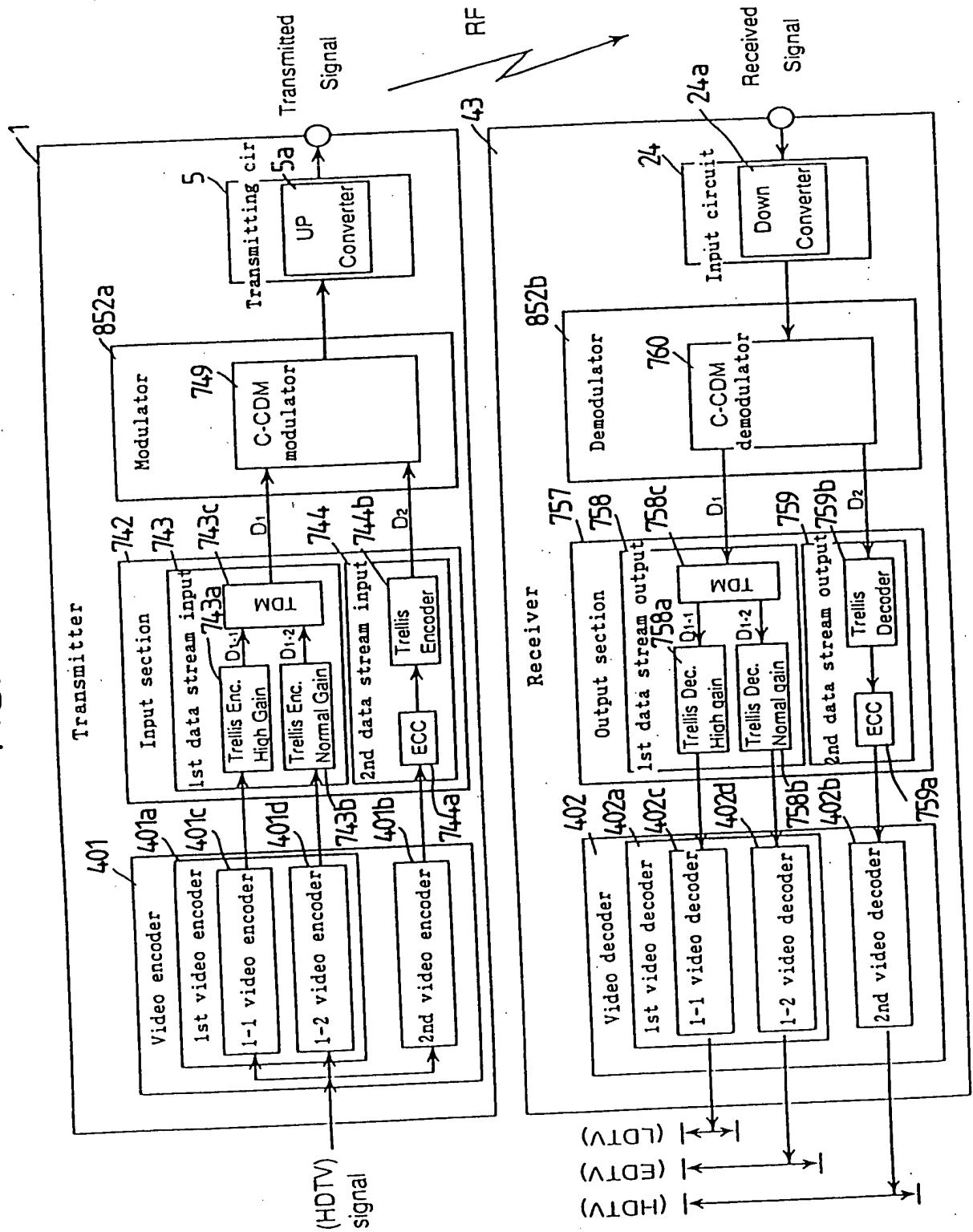


FIG. 138

